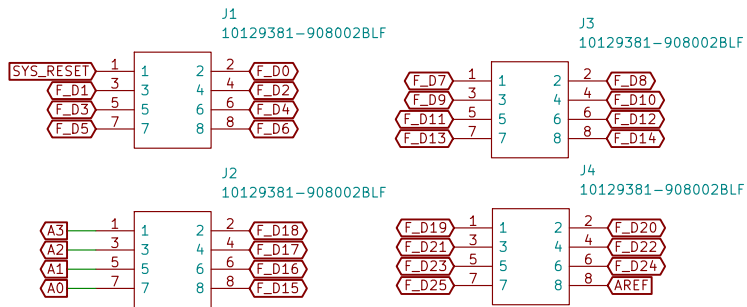
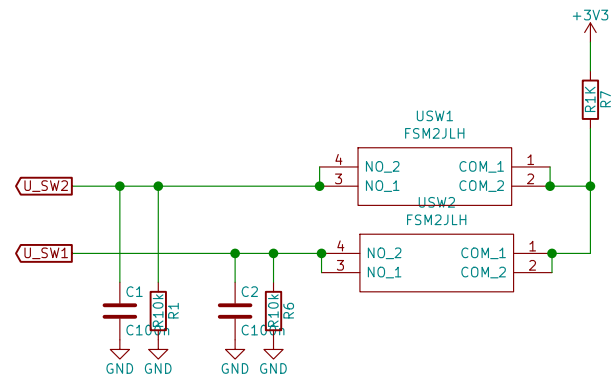


	1	2	3	4	5	6						
A	Sheet: FPGA		Sheet: SRAM		Sheet: uC							
B	File: FPGA.sch		File: SRAM.sch		File: uC.sch							
C	Sheet: Global Power		Sheet: USB		<div>Universidad de Granada</div> <div>Sheet: / File: TFG20.sch</div> <div>Title: <b>Placa entrenadora FPGA</b></div> <table><tr><td>Size: User</td><td>Date: 2020-03-19</td><td>Rev:</td></tr><tr><td colspan="2">KiCad E.D.A. kicad (5.1.5)-3</td><td>Id: 1/9</td></tr></table>		Size: User	Date: 2020-03-19	Rev:	KiCad E.D.A. kicad (5.1.5)-3		Id: 1/9
Size: User	Date: 2020-03-19	Rev:										
KiCad E.D.A. kicad (5.1.5)-3		Id: 1/9										
D	File: globalpower.sch		File: USB.sch									
	1	2	3	4	5	6						

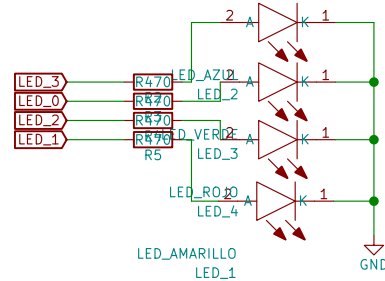
1	2	3	4	5	6
A	<div>Sheet: ICE40</div> <div>File: ICE40.sch</div>				
B	<div>Sheet: IO</div> <div>File: IO.sch</div> <div>Sheet: FPGA_Peripherals</div> <div>File: FPGA_Peripherals.sch</div>				
C					
D	<div></div> <div>Sheet: /FPGA/ File: FPGA.sch</div> <div>Title:</div> <div>Size: A4Date:KiCad E.D.A. kicad (5.1.5)–3</div> <div>Rev:Id: 2/9</div>				
1	2	3	4	5	6



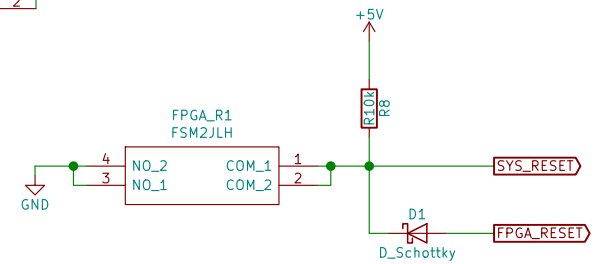
PINES



USER SWITCHES



USER LEDS



RESET

Sheet: /FPGA/I0/  
File: I0.sch

**Title:**

Size: A4

Date:

KiCad E.D.A. kicad (5.1.5)-3

**Rev:**

Id: 3/9

