

فراغات مهمة

- 1- Any instruction issued by the processor must carry at least two types of information instruction queue and IP.
- 2- The primary sources of interrupts connect to an intel 8259A
- 3- in direct mode the operand of instruction represent address of memory location.
- 4- relative mode is the same as indexed addressing except program counter (PC).
- 5- Interrupt instruction with type number is a two byte instruction , the first provides the OP-code and second byte interrupt type number.
- 6- Any instruction issued by the processor must carry at least two types of information op-code field and address field.
- 7- The 80x86 provides a 256 entry interrupt vector table beginning at address 0:0 in memory
- 8- The control unit decodes the instruction's function to determine
- 9- In response to the second INTA the microprocessor receives the 8 bit, say N, from a programmable Interrupt controller.

بالتوفيق للجميع

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