Alexander Farley

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Skills

Education

University of Calgary _

BSc Electrical Engineering

C, C#, ruby, MATLAB Kalman filtering, visual multi-object tracking Neural network design (Theano, DNN, CNN) PCB layout and firmware design FPGA design (Simulink, LabVIEW)

Sept 2005 - May 2010

Professional Experience

1 Totologica Emperiore	
FrobotAug	g 2016 - Current
Position: Developer	5
Developed vending machine system (electrical and software system architecture). Currently working with manu	facturers to bring
this product to market.	
Prolucid TechnologiesJan	2015 - Jul 2016
Position: System Integrator	
Developed FPGA components in LabVIEW for ultrasonic/electromagnetic non-destructive examination (NDE) plemented sensory input and signal conditioning routines. Provided on-site debugging support for system teclosed-loop motor controls, sensor calibration procedures and other subsystems.	
Developed closed-loop control for microfluidic sample flow. Developed Linux device driver for linear actuator countries and fixed issues in multiple-object tracking system. Improved system stability by implementing corrections for no system model. Developed tooling to aid in system diagnostics (live GigE video sniffing application).	_
AversanJan	2012 - Jan 2015
Position: Embedded Systems/Test Engineer DO-178B systems testing. Requirements analysis and automated test development. Root cause analysis and fix gration lab maintenance. Some experience with Serena Dimensions configuration management.	verification. Inte-
Developed automated tests for ADCs, linear actuator and temperature feedback controls, fault response and communications protocols, memory, system configuration, signal conditioning and related components. Maintain tooling scripts in Python.	_
Contract work (elance.com/s/asfarley)Aug	2010 - Jan 2015
• Industrial machine vision & control system development for Daqota Systems. Experience with Visual Stud Dalsa computer vision libraries, soft real-time blob detection, image segmentation and classification.	lio 2010, Teledyne
 Linear actuator PID control firmware programming (C, AVR Studio 5) & PCB layout (Eagle V5) for Prec Designed test jig circuit schematic and firmware. Laid out PCB, assembled prototype, initial PCB bring-u 	
• Developed AES-encrypted bootloader port (C, assembly, AVR Studio 5) for Dragon Link Labs	
 Developed image-based cell counting/hemocytometry script in MATLAB 	
SKF Magnetic BearingsMay 2	2008 - Sept 2009
Position: Electrical Engineering Intern	
PCB layout for analog oscillator source board. C# Windows GUI programming, analog/digital hardware troubles chopper system diagram illustrations. BOM maintenance. Automated PCB testing.	shooting. Neutron