

[illegible][illegible][illegible]

Timespan	Running	Ready queue		Waiting Queue		
0..1	P1	---	---	---	---	---
1..2	---	---	---	P1	---	---
2..3	P2	---	---	P1	---	---
3..4	P2	P1	---	---	---	---
4..5	P2	P1	P3	---	---	---
5..6	P2	P1	P3	---	---	---
6..7	P1	P3	P2	---	---	---
7..8	P1	P3	P2	---	---	---
8..9	P3	P2	---	P1	---	---
9..10	P2	---	---	P1	P3	---
10..11	---	---	---	P1	P3	P2
11..12	P1	---	---	P3	P2	---
12..13	P1	P3	---	P2	---	---
13..14	P1	P3	---	P2	---	---
14..15	P1	P3	P2	---	---	---
15..16	P3	P2	P1	---	---	---
16..17	P3	P2	P1	---	---	---
17..18	P2	P1	---	---	---	---
18..19	P1	---	---	---	---	---
19..20	---	---	---	---	---	---
20..21	---	---	---	---	---	---

P1: *arr(0), cpu_io(1, 2) cpu_io(2, 3), cpu(5), term.*

P2: *arr(2), cpu_io(5, 2) cpu(1), term.*

P3: arr(4),	cpu_io(1, 1)	cpu(2),	term.
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Round robin, time quantum=4

Note: I/O device can process only **one I/O request at a time!**

Residency									
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Average residence time	16
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16