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Section-06
Serial number-26
Lab-2

```
module lab_2(a,b,c,f);  
input a, b, c ;  
output f ;  
  
wire g,h;  
  
assign g = (~a & b) | (a & ~b);  
assign h = g ^ c;  
assign f = h & c;  
  
endmodule
```

