Experiment 1: Implement semaphore between 2 tasks

Approach: Create 2 tasks, one producer (Prio: 1) and another consumer (Prio: 2)

Make sure producer gets the semaphoref first

Get semaphore in producer and then release

Get semaphore in consumer and then release.

2) Semaphore is taken, performed a task, semaphore is given

- This task will run first, programmer have to implement vTaskDelay() to make sure producer gets the semaphore first
- 3) After producer releases the seamphore, consumer takes it, perform a task and releases it.

```
# Inside int main(void)

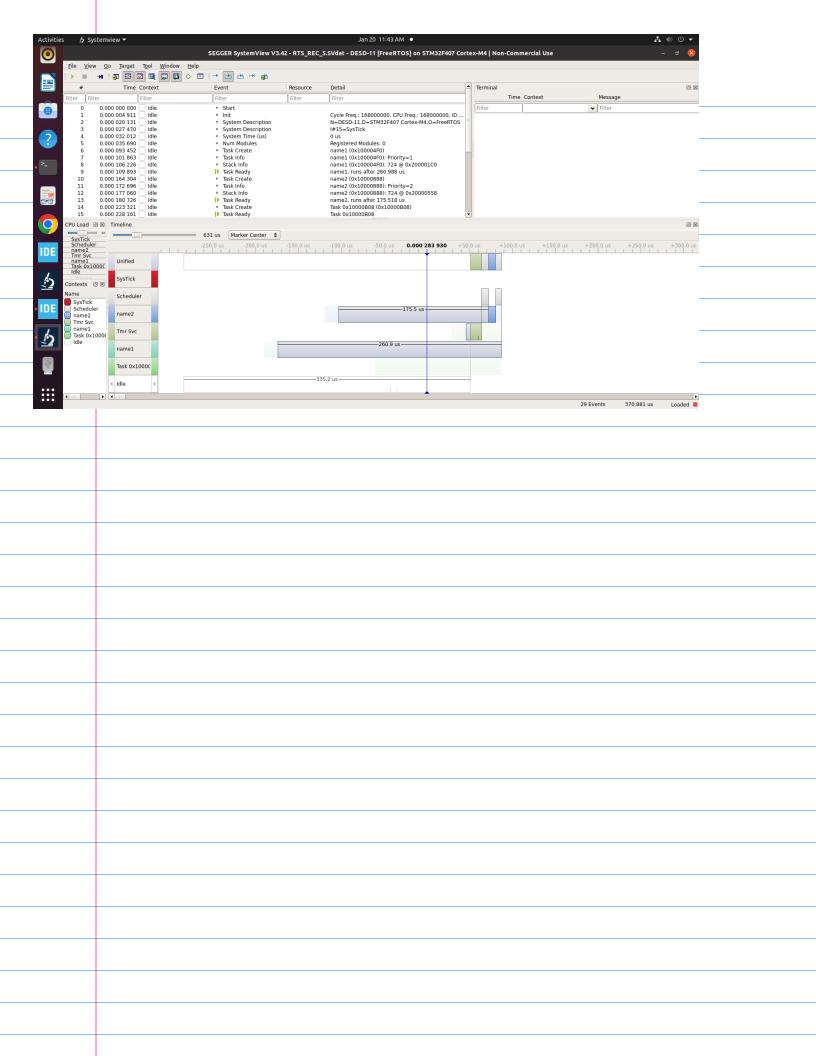
**DWT_CYCCNT |= (1 << 0);

**SEGGER_SYSVIEW_Conf();
    //SEGGER_UART_init(2000000);
    SEGGER_SYSVIEW_Start();

**VSemaphoreCreateBinary(sem);
    xTaskCreate(prod, "name1", 200, NULL, 1, NULL);
    xTaskCreate(cons, "name2", 200, NULL, 2, NULL);

**VTaskStartScheduler();</pre>
```

Creating task & setting the Priority is done using xTaskCreate()



2) Implement queue send and recieve by creating 2 tasks.

Approach: Create a Queue and store it's handle in a variable

Create 2 tasks using xTaskCreate and pass the handle as a parameter.

Send an element in Queue from task 1

Recieve it in another task.

In main function- Create a Queue using xQueueCreate(). This api returns the handle of the queue created. Store this handle in a variable.

Create 2 tasks using xTaskCreate & pass the queue handle in both the xTaskCreate calls.

Fetch the handle in void *arg. Send element in Queue using api xQueueSendToBack().

Fetch the queue handle in void *arg.
Task this will be in blocking state till the point the queue is empty. As soon as task1 fills some elements in queue, task2 gets unblocked.

Experiment 3) Implement the concept of notification between 2 tasks Approach: Create 2 tasks

From task 1, notify task2 & delete itself
In task 2, recieve notification sent from task1 & delete itself.

```
// Inside int main(void)

// *DWT_CYCCNT |= (1 << 0);

// SEGGER_SYSVIEW_Conf();

// /SEGGER_UART_init(200000);

SEGGER_SYSVIEW_Start();

// vSemaphoreCreateBinary(sem);

xTaskCreate(prod, "producer", 200, NULL, 1, &prod_hand);

xTaskCreate(cons, "consumer", 200, NULL, 1, &cons_hand);

vTaskStartScheduler();vTaskDelete(NULL);

// vTaskStartScheduler();vTaskDelete(NULL);

/
```

```
TaskHandle_t prod_hand;
TaskHandle_t cons_hand;
                                                          Use xTaskNotifyGive() api and pass
xSemaphoreHandle sem;
                                                          the handle of destination task as
                                                          parameter.
void prod(void *arg)
                                                          After that, delete the running task by
        TickType_t delay = pdMS TO TICKS(2);
                                                          vTaskDelete(NULL)
        while(1)
                HAL_GPIO_WritePin(GPIOD, LD1_Pin,1);
                xTaskNotifyGive(cons_hand);
                vTaskDelay(delay);
        //ulTaskNotifyTake( pdTRUE, portMAX_DELAY );
                vTaskDelete(NULL);
        //vTaskDelay(delay);
```

Use ulTaskNotifyTake() api to recieve the notification

Experiment 4)

Implement a mechanism where an external interrupt (a button click) comes, the ISR send an element in queue to task1. Every 5th time the button ispressed, the task1 should unblock another task (task2) by sending an element in another queue & glow an LED.

Approach:

- 1) Create 2 tasks, task1 (priority 1)& task2 (priority 2)
- 2) Wrie an ISR (which is triggered by an exterbal interrupt) which sends an element in a queue (queue1). Count the no of button clicks done (external interrupt triggers) and send the count queue1, which in turn is recieved by task1.
- 3) After recieving queue element (count) in task1, check if the count is divisible by 5 completely. If the (count%5==0), send the count in another queue (queue2) to task2.
- 4) Glow LED in task2.

Desired output: LED should be glown after every 5th button click.

```
Inside int main(void)

*DWT_CYCCNT |= (1 << 0);

SEGGER_SYSVIEW_Conf();
   //SEGGER_UART_init(200000);
SEGGER_SYSVIEW_Start();
TaskHandle_t task1_hand, task2_hand;

UBaseType_t len=50;
Queue_Hand1=xQueueCreate(len,5);
Queue_Hand2=xQueueCreate(len,5);
/*arg1: length of data, arg2: no of elements in queue*/

xTaskCreate(task1, "task1", 200, Queue_Hand1, 1, &task1_hand);

xTaskCreate(task2, "task2", 200, Queue_Hand2, 2, &task2_hand);
vTaskStartScheduler();</pre>
```

Create 2 Queues using xQueueCreate api, store the respective handles using variables Create 2 tasks using xTaskCreate and pass the Queue handles as a parameter.

```
void HAL_GPIO_EXTI_Callback(uint16_t GPIO_Pin)
{
    uint32_t delay=200;
    static uint32_t last_tick=0;
    BaseType_t xHigherPriorityTaskWoken = pdFALSE;
    uint32_t tick_start = HAL_GetTick();
    if(tick_start-last_tick > delay)
    {
        count++;
        xQueueSendFromISR(Queue_Hand1,&count, xHigherPriorityTaskWoken);
        portYIELD_FROM_ISR(xHigherPriorityTaskWoken);
    }
    last_tick = tick_start;
}
```

In ISR (triggered after an external interrupt at pin PAO, user button), get the system tick count using HAL_GetTick() api and store it in tick_start variable of type uint32_t. Initialise a static int variable tick_start (which is initialised with value 0).

To implement a mechanism to check debouncing, check if last called interrupt with the current called interruot has a difference of atleast 200 ticks. This way, we are able to implement a debouncing avoidance mechanism witout implementing delay. We are just making sure that difference between 2 truggers have a substantial tick difference of 200 ticks.

After the condition becomes true, increamente the count and send it in queue using ISR safe AP xQueueSendFromISR().

The Interrupt Safe API

Often it is necessary to use the functionality provided by a FreeRTOS API function from an interrupt service routine (ISR), but many FreeRTOS API functions perform actions that are not valid inside an ISR—the most notable of which is placing the task that called the API function into the Blocked state; if an API function is called from an ISR, then it is not being called from a task, so there is no calling task that can be placed into the Blocked state. FreeRTOS solves this problem by providing two versions of some API functions; one version for use from tasks, and one version for use from ISRs. Functions intended for use from ISRs have "FromISR" appended to their name.

Note: Never call a FreeRTOS API function that does not have "FromISR" in its name from an ISR.

The ARM Cortex cores, and ARM Generic Interrupt Controllers (GICs), use numerically *low* priority numbers to represent logically *high* priority interrupts. This can seem counter-intuitive, and is easy to forget. If you wish to assign an interrupt a logically low priority, then it must be assigned a numerically high value. If you wish to assign an interrupt a logically high priority, then it must be assigned a numerically low value.

For above mentioned conflict (lack of proper handshaking between FreeRTOS & ARM Cortex M4 NVIC convention), we have to make the priority of external interrupt highest (i.e, 15) in NVIC.

NVIC Interrupt Table	Enabled	Preemption Priority	Sub Priority
Non maskable interrupt	✓	0	0
Hard fault interrupt	✓	0	0
Memory management fault	✓	0	0
Pre-fetch fault, memory access fault	✓	0	0
Undefined instruction or illegal state	✓	0	0
System service call via SWI instruction	✓	0	0
Debug monitor	✓	0	0
Pendable request for system service	✓	0	0
System tick timer	✓	15	0
PVD interrupt through EXTI line 16		0	0
Flash global interrupt		0	0
RCC global interrupt		0	0
EXTI line0 interrupt	✓	15	0
Time base: TIM3 global interrupt	✓	15	0
FPU global interrupt		0	0

```
void task1(void *arg)
{
    int count;
    char msg[20];
    TickType_t delay = pdMS_TO_TICKS(5);
    while(1)
    {
        xQueueReceive(Queue_Hand1,&count,NULL);
        if(count%5==0)
        {
            strcpy(msg,"LED_ON");
            xQueueSendToBack(Queue_Hand2,msg, portMAX_DELAY);
        strcpy(msg,"LED_OFF");
        }
        vTaskDelay(delay);
}
```

Note: It seems that xQueueRecieveFromISR is a right fit to recieve Queue from ISR in task2, these ISRs which are appended with "FromISR" are only valid to be used inside ISR.

Recive the count inside Queue1 and check if count is in multiple of 5. If the condition is true, send the message LED_ON in another Queue2 and send it to task2.

```
ovoid task2(void *arg)

          char rbuff[20];
          TickType t delay = pdMS TO TICKS(5);
          while(1)
                                                                     Recieve Queue2 from task1 in task2.
          {
                                                                     Just check if the recieved element
              xQueueReceive(Queue Hand2,rbuff,portMAX DELAY);
                                                                     (a buffer) have message LED_ON. If yes, then Toggle the LED.
              if(strcmp(rbuff,"LED ON"))
                   HAL_GPIO_TogglePin(GPIOD, LD1_Pin);
              vTaskDelay(delay);
          }
```