

①

→ Oscilloscope:

Instrument display all electrical signal graphically and shows how these signals are varying w.r.t time.

Function generator:

Instrument used to generate different type of electrical square waves.

Protel:

Instrument used to design and simulating electronic circuit.

Protel advantages:

- take less time than practical designing
- error chance is less
- low possibility of burning & damaging element

→ feature of Protel:

- ① Schematic design
- ② PCB layout

(2)

Diode ID = 1A (400 etc 5A)
390, 900

Diode, ^{Semiconductor device} ~~component~~ → Conduct current in one direction & Resist current in other direction.

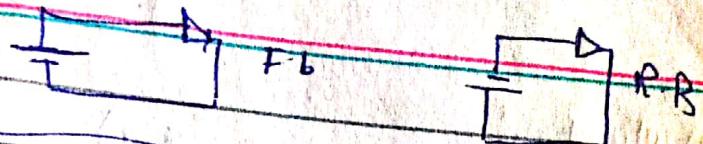
→ In forward bias current flow.

→ In reverse bias current stop.

Main function:

- Main function is that allow current in one direction, to resist current in other direction.

+ Δ -



(3)

→ Series

Diode combination

→ Voltage

→ Current

same

diode current across each Diode

* Characteristic in series:

→ Resultant

diode forward voltage increases bc in series added

→ Reverse blocking capability increases

mean no conduction.

→ Parallel Combination of Diodes

→ Voltage sum across each diode

→ Current different across each diode

* Characteristic in parallel

→ current carrying capacity increases bc in all adding

→ No conduction in resultant diode in both sides

(4)

Rectifier convert A.C to D.C

→ half wave Rectifier

Type of Rectifier not convert

entry one-half of AC cycle to pulsating DC

→ bc first F.B. then R.B. due to change in polarity of A.C.

→ full wave Rectifier

convert complete cycle

→ A.C to pulsating D.C.

→ due to Diode bridge.

→ Diode Application:

→ Switch in logic gate

→ As Rectifier

→ As clipper

→ As clumper

→ Reverse current protection

→ Voltage multiplier

(S)

→ Remove excess voltage.

Clipper circuit: $i_{out} = i_{in}$ if $i_{in} < V_c$ and $i_{out} = 0$ if $i_{in} \geq V_c$

→ limiter also

→ Wave shaping circuit remove or clip off

one half cycle of the wave
(or) without changing the remaining part of the wave
remove small portion of wave or signal

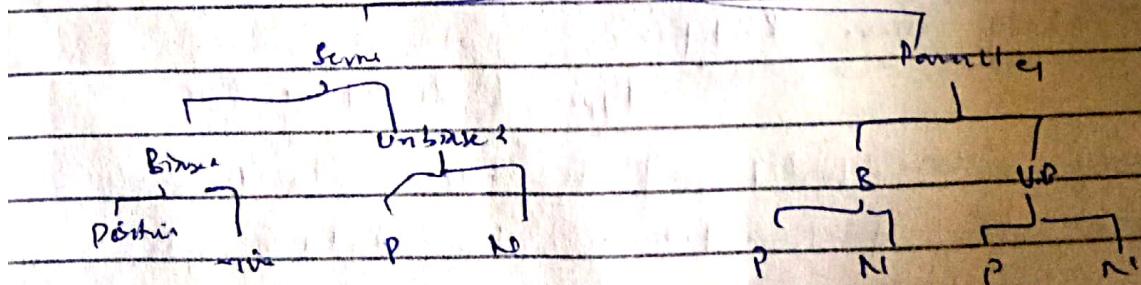
→ Also known as protecting device.

$\approx V_c$ Required voltage input or output

- If $i_{in} > V_c$ then $i_{out} \approx 0$,

Classification

Clipper circuit



→ Series:

= for unbiased series clipper circuit

no extra DC and diod is connect
in reverse biased.

→ - N number diodes

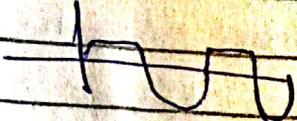
diode is connected in forward
bias

→ biased clipper circ

extra DC Voltage. Since and we can remove any portion of graph

→ +ve b.c.c.

diode in reverse biased



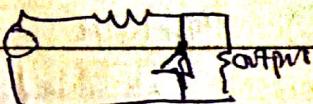
→ -ve bias,

→ Diode forward biased



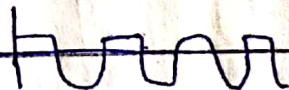
⇒ Parallel:

→ Diode connected in parallel with voltage sum



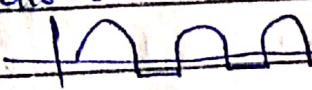
① +ve unbiased P.C.C

→ diode is connected in forward bias



② -ve unbiased P.C.C

→ diode is in reverse bias



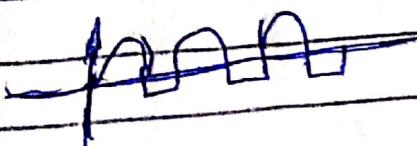
① Positive bias P.C.C

→ diode forward bias + DC Voltage sum



② -ve bias P.C.C

→ diode reverse bias + DC voltage sum



Clipper circuit consists of linear & non-linear elements. Not energy storage elements.

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Clamper: \rightarrow it does not clip the signal.

\rightarrow Signal level shifter.

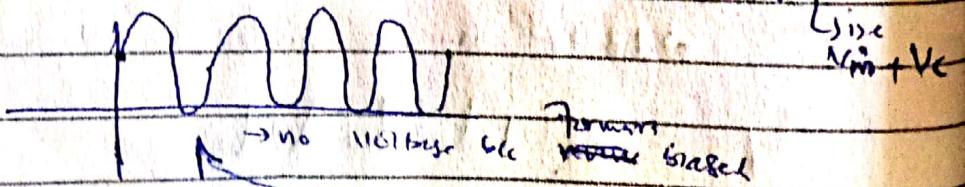
~~changes when no changes like DC~~
Level of the signal to desired
~~level~~

\rightarrow device which shift the DC level of the output signal to either +ve or -ve direction \uparrow or \downarrow .

Three types of Clamper Circuit.

(i) Positive Clamper = (Diode Reverse biased)

(ii) +ve half cycle \rightarrow Diode reverse biased $\rightarrow V_{out} = V_{in}$
 \rightarrow capacitor discharge $\rightarrow V_{out} = 2V_{in}$,
 $\therefore V_{out} = V_{in} + V_C$



(iii) -ve half cycle Clamper circuit

\rightarrow Diode forward biased $\therefore V_{out} = 0$

\rightarrow capacitor charging $\therefore V_C = 0$
 $\therefore V_{out} = 0$



(Time)

- V_{out}

Clamper (Diode forward biased)

- it shifts output signal to down.

① During +ve half cycle

→ Diode forward biased so $V_{out} = 0$

→ Capacitor charging so $V_c = 0$

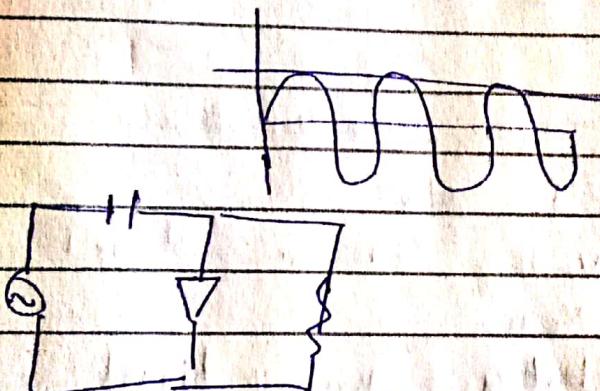
$$\therefore V_{out} = 0$$

② During -ve half cycle

→ Diode reverse biased so $V_{out} = V_{in}$

→ capacitor discharging so $V_{out} = V_c$

$$\therefore V_{out} = 2V_{in} \text{ i.e } V_{in} + V_c$$



③ Biased clamper

→ sometimes additional shift is need

So we add another DC source.

Ferr type

④ Positive clamper with positive bias

- During +ve half cycle

→ if $V_{in} < V_{bottom}$ Diode forward biased & capacitor charge

if $V_{in} > V_{bottom}$ Diode Reverse biased & capacitor discharge

$$V_{out} = V_{in} + V_c \rightarrow here V_c more charge \rightarrow extra shift.$$

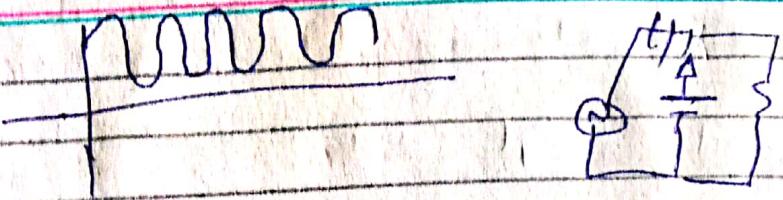
⑤ → During -ve half cycle.

→ Diode forward by both voltage source so

$$V_{out} = 0$$

$$V_c = 0$$

Capacitor charging $V_c = 0$

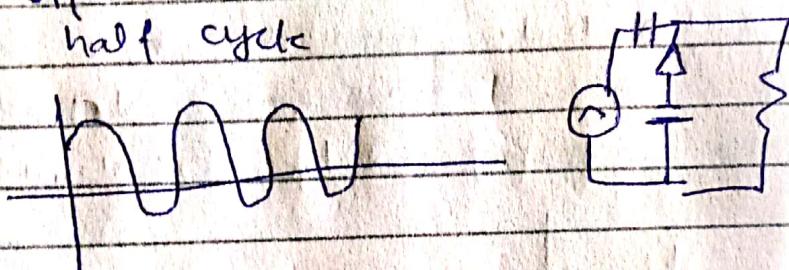


② Positive clumper with -ive branch.

→ +ive half cycle

if $V_{in} < V_{battery}$ Diode reverse biased so small output in -ive cycle.

→ +ive half cycle



③ -ive clumper with +ive Branch

-positive half cycle

if $V_{in} < V_{battery}$ reverse branch s.
 $V_{out} = V_{battery}$

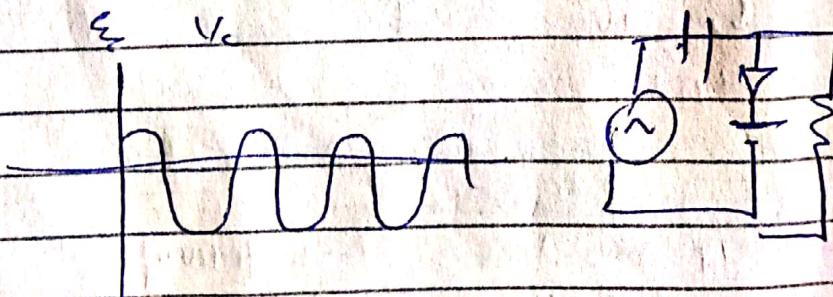
if $V_{in} > V_{battery}$ forward biased

$V_{out} = 0$ Capacitor charge

⇒ -ive half cycle,

Diode is reverse biased by both V_{in}

$$\therefore V_c$$



④ -ive clumper with -ive biased

→ During +ive half cycle.

Diode ~~reverse~~ forward biased by both voltages

$$\text{So } V_{out} = 0$$

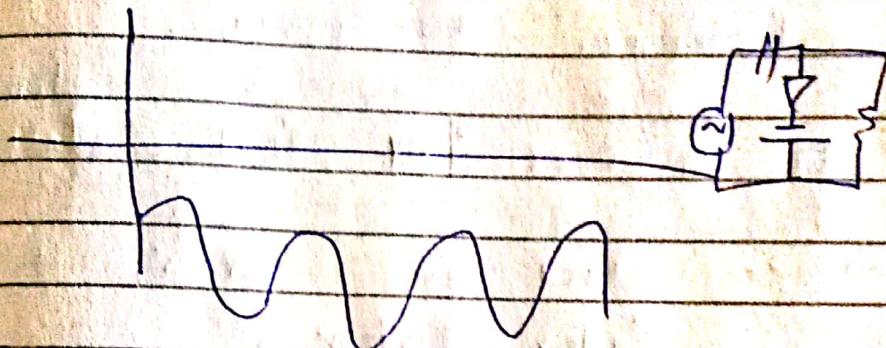
Capacitor charge more due to biasing which result in extra switching.

-ive half cycle

\rightarrow if $V_{in} < V_{battery}$ D forward biased
capacitor charge $V_{out} = 0$

If $V_{in} > V_{battery}$ D reverse biased

$$V_{out} = V_{in} + V_c$$



D/f b/w Clipper & Clamper

Clipper

- ① limit voltage
- ② clipper does not require any energy storing element
- ③ shape of output signal is differ than shape of input signal

- ④ Also called current, voltage or amplitude delimiter
- ⑤ DC level ~~source~~ remain same

Clamper

- ① shift voltage up or down.
- ② while clamper requires capacitor to complete the circuit.
- ③ shape of output signal exactly same as shape of input signal

- ④ Voltage multiplier.

- ⑤ DC level shifted

Uses of Clipper & Clamper

→ Clippers are used in communication
clipper circuits such as Transmitters or Receivers.

→ Clipper also used in Wave Shaping
circuit to generate triangular &
rectangular ~~wave~~ waves.

Clamper, it is used for voltage ~~shorten~~ ^{duration}

→ It is used in Sonar, &
Radar systems,

(7)

Zener Diode

→ Special Diode work at voltage
equal to ~~breakdown~~ or greater
than b.d.v.

→ act both in reverse & forward bias

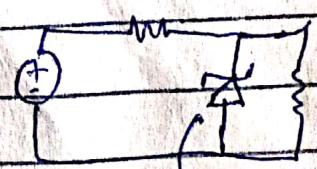
→ It act as normal diode in forward bias

→ It act as voltage regulator in reverse
bias

→ Principle is Zener breakdown.

(mean we doped diode heavily to narrow
depletion region.)

(7)



→ It will be in reverse biased

Zener Diode will work properly when its threshold load resistance is greater or equal to threshold resistance

threshold Resistance:

$$V_{\text{Rt}} = \frac{V_{\text{in}} \times R_L}{R + R_L} \quad V_{\text{in}} = (V_0)$$

maximum efficiency:

I D/I bw Zener diode \approx normal diode conduct uni-directional current
~~Zener Diode~~ conduct bi-directional current.

② In Zener diode sharply dropping in normal & moderately dropping.

③ breakdown voltage is sharp in Zener diode
" " high " " normal diode

④ Zener diode work in reverse biased mode while normal diode don't work in reverse biasing mode.

⑤ Normal diode used as Rectifier, clapper clipper circuit, Zener diode used as voltage regulator.

Lab 8

Transistor

Semiconductor

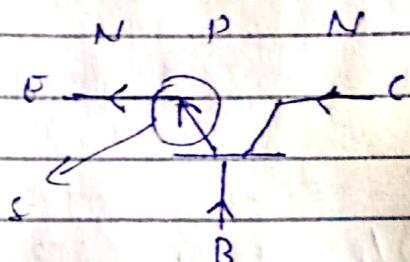
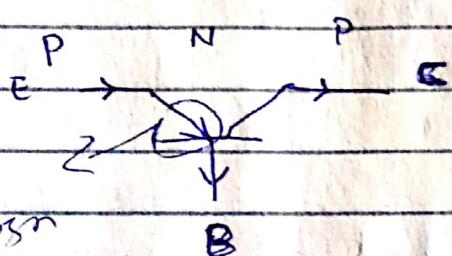
→ electronic device used to amplify

or switch electronic signal

→ made from 2 PN-Junction Diodes

→ 3 - terminals

Emitter - base current I_E
Collector - base current I_C



⇒ three terminal

① Emitter → heavily doped, b/a
through which electron enter.
→ always F. biased w.r.t base

③ Base : lightly doped $b/a/c$ through
which electron pass from Emitter to
Collector. $B-E \rightarrow$ low resistance
 $B-C \rightarrow$ high resistance.

③ Collector:

→ moderately doped $b/a/c$
which collect electron.

Types two types

③ Bi-polar junction T

functionality
depends on both

NPN

majority & minority
charge carrier

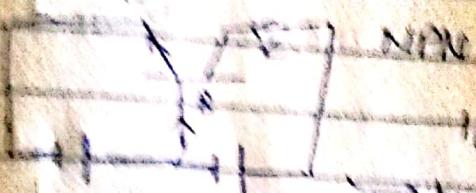
③ uni-polar junction T

depends on majority
charge carrier.

① NPN

Structure in which P-type material
sandwiched between 2 N-type material.

CB and E base
CE & R BE

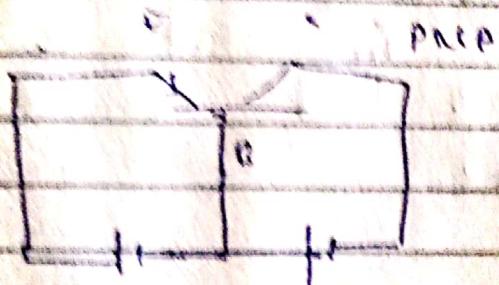


$$I_C = I_B + I_E$$

Current due to negative charge carrier electrons.

② PNP:

Structure in which N-type material contains 2 P-type material.



Current due to hole charge carriers.

$$I_B = \frac{V_{EE} - V_{BE}}{R_B}$$

$$I_E = \frac{V_{EE} - V_{BE}}{R_E}$$

Ques (a)

① Cutoff mode:

→ In cutoff mode both E.B & C.B Transistor are in reverse biased.

② Active mode

In A.M E.B Transistor is F.based & C.B Transistor is Rebased
biased mode.
→ In active mode it is act as Amplifier

③ Saturation mode:

In saturation mode both E.B & C.B Transistor are in F.based.

Notes: In case of cutoff & saturation mode Transistor act as switch

END

→ In case of saturation Transistor act as open switch

→ In case of Cutoff if act as close switch