*/

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```
/* File name Primary2.yal
/* Copyright (C) 1987 by Bryan Preas and Ken Roberts.
                  All rights reserved.
/* Primary2.yal (RPROC). This circuit is a 16-bit microprocessor.*/
                         It includes a sizeable register stack
/*
                         and some large pieces of decode logic.
    Rules: - May be layed out as either a gate array or
             standard circuit.
           - The required pad placement is given below.
/*
           - For standard cell, the library is SClib.
           - For gate array, the library is GAClib. The gate
             array parameters are (see GAClib library
             definition):
                Rows - 36
                Columns - 500 placement slots per row,
                           or 10000u long.
                Pads - 36 each side.
                Routing channels - 18 tracks each.
/* Circuit statistics: 2907 internal cells, 107 signal pads,
                        3029 nets. (91.3% internal utilization
                        in gate array)
/*
                                                              */
/* File name SCLib.yal
/* Copyright (C) 1987 by Bryan Preas and Ken Roberts.
/*
                  All rights reserved.
/*
                                                              */
                                                              */
/* Library: SCLib.yal
                                                              */
                                                              */
/* Technology: Standard cell, 2-metal routing.
                                                              */
                                                              */
/* Design rules: Metal width, 4u, both levels.
                                                              */
                 Via size = 4u.
                                                              */
/*
                 Via oversize = 1u
/*
                 Metal spacing = 5u (both levels).
                                                              */
                                                              */
/* Routing grid: 10u on both levels. Horizontally and
                                                              */
                 vertically adjacent vias NOT allowed,
                                                              */
/*
                 but diagonally adjacent vias OK.
                                                              */
/*
                                                              */
/* Cell characteristics:
                                                              */
                        Cell placements can abut, but not
                                                              */
/*
                        overlap. The legal orientations for */
/*
                        internal cell placements are normal
/*
                        (RFLNONE ROT0) and reflected about Y */
/*
                                                              */
                        (RFLY ROT0).
/*
/*
                        Over-cell feedthru paths are provided*/
/*
                        in the cell definitions. You should */
/*
                        indicate in your results whether or */
/*
                        not your system can make use of them.*/
/*
                                                              */
/*
                        Feedthrus external to cells must be
                                                              */
/*
                        centered at least 5u from the near-
                                                              */
/*
                        cell boundary. A feedthru cell,
                                                              */
/*
                        which can be abutted to cells is
                                                              */
                        provided in case your system re-
                                                              */
                        quires it.
                                                              */
                                                              */
                        Pad cells are placed at 300u
                                                              */
                        increments. Pad cell orientations
                                                              */
                        are: right side - RFLNONE ROT0
                                                              */
                             left side - RFLY ROT0
                                                              */
/*
                                        - RFLY ROT270
                                                              */
                             top
```

```
- RFLNONE ROT270
                            bottom
/*
                                                            */
/*
                                                            */
/*
                                                            */
/* Cell FEED. Feedthrough cell.
                                                            */
MODULE FEED;
TYPE FEEDTHROUGH;
WIDTH 10;
HEIGHT 150;
IOLIST;
 F1 F BOTTOM 5 4 METAL2;
 F1 F TOP
                 5 4 METAL2;
ENDIOLIST;
ENDMODULE;
/*
                                                            */
/*
                                                            */
/* Cell G11. Combinational gate, 1 input, 1 output.
                                                            */
MODULE G11;
TYPE STANDARD;
WIDTH 30;
HEIGHT 150;
 IOLIST;
 I1 I BOTTOM 5 4 METAL2;
 I1 I TOP
                 5 4 METAL2;
 01 0 BOTTOM 15 4 METAL2;
 01 0 TOP
               15 4 METAL2;
 F1 F BOTTOM 25 4 METAL2;
 F1 F TOP
                25 4 METAL2;
ENDIOLIST;
ENDMODULE;
/*
/*
                                                            */
/* Cell G21. Combinational gate, 2 inputs, 1 output.
                                                            */
/*
MODULE G21;
TYPE STANDARD;
WIDTH 50;
HEIGHT 150;
 IOLIST;
 I1 I BOTTOM 5 4 METAL2;
 I1 I TOP
                 5 4 METAL2;
 I2 I BOTTOM 25 4 METAL2;
  I2 I TOP
                25 4 METAL2;
 01 0
        BOTTOM 35 4 METAL2;
 01 0 TOP
                35 4 METAL2;
 F2 F
         BOTTOM 15 4 METAL2;
 F2 F
        TOP
               15 4 METAL2;
 F3 F
         BOTTOM 45 4 METAL2;
 F3 F TOP
                45 4 METAL2;
 ENDIOLIST;
ENDMODULE;
                                                            */
/*
/*
                                                            */
/* Cell G31. Combinational gate, 3 inputs, 1 output.
/*
MODULE G31;
 TYPE STANDARD;
WIDTH 60;
 HEIGHT 150;
 IOLIST;
  I1 I
         BOTTOM 5 4 METAL2;
  I1 I
         TOP
                 5 4 METAL2;
  I2 I
         BOTTOM 15 4 METAL2;
  12
     Ι
         TOP
                15 4 METAL2;
  Ι3
     Ι
         BOTTOM 35 4 METAL2;
  Ι3
      Ι
         TOP
                35 4 METAL2;
         BOTTOM 45 4 METAL2;
```

```
01 0
        TOP
               45 4 METAL2;
  F2 F
        BOTTOM 55 4 METAL2;
 F2 F TOP
               55 4 METAL2;
ENDIOLIST;
ENDMODULE;
                                                            */
/*
/*
                                                            */
/* Cell G41. Combinational gate, 4 inputs, 1 output.
                                                            */
MODULE G41;
TYPE STANDARD;
WIDTH 70;
HEIGHT 150;
 IOLIST;
 I1 I BOTTOM 5 4 METAL2;
  I1 I TOP
                 5 4 METAL2;
     Ι
        BOTTOM 15 4 METAL2;
  I2
     I TOP
  12
               15 4 METAL2;
     I
        BOTTOM 35 4 METAL2;
  Ι3
     I TOP
  I3
               35 4 METAL2;
     I
        BOTTOM 45 4 METAL2;
  14
     I TOP
  14
               45 4 METAL2;
 01 0
        BOTTOM 55 4 METAL2;
 01 0 TOP
               55 4 METAL2;
 F2 F
        BOTTOM 65 4 METAL2;
 F2 F TOP
               65 4 METAL2;
ENDIOLIST;
ENDMODULE;
/*
/*
                                                            */
/*
  Cell G61. Combinational gate, 6 inputs, 1 output.
/*
MODULE G61;
TYPE STANDARD;
WIDTH 100;
HEIGHT 150;
 IOLIST;
 I1 I BOTTOM 5 4 METAL2;
  I1 I TOP
                 5 4 METAL2;
     I
  12
        BOTTOM 15 4 METAL2;
  12
     Ι
        TOP
               15 4 METAL2;
  I3 I
        BOTTOM 35 4 METAL2;
  I3 I
        TOP
               35 4 METAL2;
  14
     Ι
        BOTTOM 45 4 METAL2;
  14
     Ι
        TOP
               45 4 METAL2;
  I5
     Ι
        BOTTOM 65 4 METAL2;
  15
     Ι
        TOP
                65 4 METAL2;
     I
  16
        BOTTOM 75 4 METAL2;
  16
     Ι
        TOP
               75 4 METAL2;
 01
     0
        BOTTOM 85 4 METAL2;
 01 0
        TOP
               85 4 METAL2;
 F1 F
        BOTTOM 25 4 METAL2;
 F1 F
        TOP
                25 4 METAL2;
 F2 F
        BOTTOM 95 4 METAL2;
 F2 F
        TOP
                95 4 METAL2;
 ENDIOLIST;
ENDMODULE;
/*
/*
/* Cell G81. Combinational gate, 8 inputs, 1 output.
/*
MODULE G81;
 TYPE STANDARD;
 WIDTH 160;
 HEIGHT 150;
 IOLIST;
  I1
     Ι
        BOTTOM
                5 4 METAL2;
        TOP
                 5 4 METAL2;
        BOTTOM 15 4 METAL2;
```

```
I2 I
        TOP
                15 4 METAL2;
  Ι3
     Ι
         BOTTOM 25 4 METAL2;
         TOP
                25 4 METAL2;
  Τ3
     Т
  14
     Ι
         BOTTOM 35 4 METAL2;
                35 4 METAL2;
  14
     Ι
         TOP
  I5
     Ι
         BOTTOM 115 4 METAL2;
  I5
     Ι
         TOP
                115 4 METAL2;
         BOTTOM 125 4 METAL2;
  16
     Ι
  I6
     Ι
        TOP
               125 4 METAL2;
         BOTTOM 135 4 METAL2;
  17
     Ι
  17
     Ι
        TOP
                135 4 METAL2;
  18
     Ι
         BOTTOM 145 4 METAL2;
  18
     Ι
                145 4 METAL2;
        TOP
 01 0
         BOTTOM 75 4 METAL2;
 01 0
        TOP
               75
                   4 METAL2;
 F1 F
         BOTTOM 45 4 METAL2;
 F1 F
         TOP
                45 4 METAL2;
 F2 F
         BOTTOM 55 4 METAL2;
 F2 F
         TOP
                55 4 METAL2;
 F3 F
         BOTTOM 85 4 METAL2;
     F
 F3
         TOP
                85 4 METAL2;
     F
  F4
         BOTTOM 155 4 METAL2;
     F
 F4
         TOP
                155 4 METAL2;
 ENDIOLIST;
ENDMODULE;
                                                            */
/*
                                                             */
/* Cell G12. Combinational gate, 1 input, 2 outputs.
                                                            */
MODULE G12;
TYPE STANDARD;
WIDTH 60;
HEIGHT 150;
 IOLIST;
         BOTTOM 5 4 METAL2;
 I1 I
 I1 I
        TOP
                 5 4 METAL2;
 01 0
         BOTTOM 35 4 METAL2;
 01 0
         TOP
                35 4 METAL2;
 02 0
         BOTTOM 45 4 METAL2;
 02 0
         TOP
                45 4 METAL2;
  F1 F
         BOTTOM 15 4 METAL2;
     F
  F1
         TOP
                15 4 METAL2;
 F2 F
         BOTTOM 55 4 METAL2;
 F2 F
         TOP
                55 4 METAL2;
 ENDIOLIST;
ENDMODULE;
/*
/*
/* Cell F22.
             Sequential function 2 inputs, 2 outputs.
/*
MODULE F22;
 TYPE STANDARD;
 WIDTH 140;
 HEIGHT 150;
 IOLIST;
  I1 I
         BOTTOM 15 4 METAL2;
  I1 I
         TOP
                15 4 METAL2;
  I2 I
         BOTTOM 45 4 METAL2;
  I2 I
         TOP
                45 4 METAL2;
 01 0
         BOTTOM 105 4 METAL2;
 01 0
         TOP
                105 4 METAL2;
 02 0
         BOTTOM 125 4 METAL2;
 02 0
         TOP
                125 4 METAL2;
  F1
     F
         BOTTOM 5 4 METAL2;
     F
  F1
         TOP
                 5 4 METAL2;
     F
  F2
         BOTTOM 35 4 METAL2;
      F
  F2
         TOP
                35 4 METAL2;
      F
  F3
         BOTTOM 75 4 METAL2;
         TOP
                75 4 METAL2;
```

```
F4 F
        BOTTOM 95 4 METAL2;
  F4
     F
        TOP
               95 4 METAL2;
  F5
     F
        BOTTOM 115 4 METAL2;
  F5
     F
        TOP
               115 4 METAL2;
  F6 F
        BOTTOM 135 4 METAL2;
 F6 F TOP
               135 4 METAL2;
 ENDIOLIST;
ENDMODULE;
/*
                                                            */
/*
                                                            */
/* Cell F32. Sequential function 3 inputs, 2 outputs.
                                                            */
MODULE F32;
TYPE STANDARD;
WIDTH 200;
HEIGHT 150;
 IOLIST;
 I1 I BOTTOM 15 4 METAL2;
  I1 I TOP
              15 4 METAL2;
  I2 I
        BOTTOM 55 4 METAL2;
  I2
     I TOP
               55 4 METAL2;
  I3
     I
        BOTTOM 95 4 METAL2;
  I3 I TOP
               95 4 METAL2;
 01 0
        BOTTOM 155 4 METAL2;
 01 0 TOP
               155 4 METAL2;
 02 0
        BOTTOM 175 4 METAL2;
 02 0 TOP
               175 4 METAL2;
 F1 F
        BOTTOM 5 4 METAL2;
 F1 F
        TOP
                 5 4 METAL2;
 F2 F
        BOTTOM 35 4 METAL2;
 F2 F
        TOP
               35 4 METAL2;
 F3 F
        BOTTOM 65 4 METAL2;
 F3 F
        TOP
                65 4 METAL2;
  F4 F
        BOTTOM 85 4 METAL2;
  F4 F
        TOP
               85 4 METAL2;
  F5 F
        BOTTOM 115 4 METAL2;
  F5 F
        TOP
               115 4 METAL2;
  F6 F
        BOTTOM 135 4 METAL2;
  F6 F
        TOP
               135 4 METAL2;
     F
 F7
        BOTTOM 195 4 METAL2;
 F7 F TOP
               195 4 METAL2;
 ENDIOLIST;
ENDMODULE;
                                                            */
/*
/*
                                                            */
/* Cell F42. Sequential function 4 inputs, 2 outputs.
/*
MODULE F42;
TYPE STANDARD;
WIDTH 200;
 HEIGHT 150;
 IOLIST;
        BOTTOM 15 4 METAL2;
 I1 I
  I1 I
        TOP
               15 4 METAL2;
  I2 I
        BOTTOM 55 4 METAL2;
  I2 I
        TOP
               55 4 METAL2;
  Ι3
     Ι
        BOTTOM 95 4 METAL2;
  Ι3
     Ι
        TOP
               95 4 METAL2;
  14
     Ι
        BOTTOM 125 4 METAL2;
  14
     Ι
        TOP
               125 4 METAL2;
  01
     0
        BOTTOM 155 4 METAL2;
  01
     0
        TOP
               155 4 METAL2;
 02 0
        BOTTOM 175 4 METAL2;
 02 0
        TOP
               175 4 METAL2;
  F1
     F
        BOTTOM 5 4 METAL2;
     F
  F1
         TOP
                 5 4 METAL2;
     F
  F2
         BOTTOM 35 4 METAL2;
      F
  F2
         TOP
                35 4 METAL2;
        BOTTOM 85 4 METAL2;
```

```
85 4 METAL2;
 F3 F
        TOP
         BOTTOM 115 4 METAL2;
 F4 F
  F4
     F
         TOP
              115 4 METAL2;
  F5 F
         BOTTOM 135 4 METAL2;
 F5 F
         TOP
               135 4 METAL2;
 F6 F
         BOTTOM 165 4 METAL2;
 F6 F
        TOP
               165 4 METAL2;
 F7
     F
         BOTTOM 195 4 METAL2;
 F7 F TOP
               195 4 METAL2;
ENDIOLIST;
ENDMODULE;
/*
                                                            */
/*
                                                            */
/* Cell I1.
           Input pad cell, 1 output.
                                                            */
MODULE I1;
TYPE PAD;
WIDTH 600;
HEIGHT 300;
 IOLIST;
 I1 PI RIGHT 150 0 METAL2;
 01 0 LEFT 155 4 METAL1;
ENDIOLIST;
ENDMODULE;
/*
/*
                                                            */
/* Cell I2.
            Input pad cell, 2 outputs.
/*
MODULE I2;
TYPE PAD;
WIDTH 600;
HEIGHT 300;
 IOLIST;
 I1 PI RIGHT 150 0 METAL2;
 01 0 LEFT 145 4 METAL1;
 02 0 LEFT 165 4 METAL1;
ENDIOLIST;
ENDMODULE;
/*
/*
                                                            */
/* Cell 01.
                                                            */
           Output pad cell, 1 input.
/*
MODULE 01;
TYPE PAD;
WIDTH 600;
HEIGHT 300;
 IOLIST;
 I1 I LEFT 155 4 METAL1;
 01 PO RIGHT 150 0 METAL2;
 ENDIOLIST;
ENDMODULE;
/*
/*
                                                            */
/* Cell O2. Output pad cell, 2 inputs.
/*
MODULE 02;
TYPE PAD;
WIDTH 600;
HEIGHT 300;
 IOLIST;
 I1 I LEFT
              145 4 METAL1;
 I2 I LEFT 165 4 METAL1;
 01 PO RIGHT 150 0 METAL2;
 ENDIOLIST;
ENDMODULE;
/*
/*
/* Cell B21.
              Bidirectional pad cell, 2 inputs, 1 output.
```

```
11/19/24, 4:30 PM
 MODULE B21;
  TYPE PAD;
  WIDTH 600;
  HEIGHT 300;
  IOLIST;
   I1 I LEFT 145 4 METAL1;
   12 I LEFT 155 4 METAL1;
   01 0 LEFT 165 4 METAL1;
   B1 PB RIGHT 150 0 METAL2;
  ENDIOLIST;
 ENDMODULE;
 MODULE RPROC ;
 TYPE PARENT;
 WIDTH 10500;
 HEIGHT 10500;
 IOLIST;
 S3 PI RIGHT 1800;
 S4 PI RIGHT 2100;
 S5 PI RIGHT 1500;
 S6 PI RIGHT 2400;
 S7 PI BOTTOM 5700;
 S8 PI BOTTOM 6000;
 S9 PI BOTTOM 6300;
 S10 PI BOTTOM 6600;
 S11 PI BOTTOM 6900;
 S12 PI BOTTOM 7200;
 S13 PI BOTTOM 7800;
 S14 PI BOTTOM 7500;
 S15 PI RIGHT 1200;
 S16 PI BOTTOM 9900;
 S17 PI BOTTOM 9300;
 S18 PI TOP
                  0;
 S19 PI TOP
               7200;
 S20 PI TOP
               6900;
 S21 PI BOTTOM 8700;
               10500;
 S22 PI TOP
 S23 PI LEFT
                  0;
 S24 PI LEFT
                600;
 S25 PI LEFT
               1200;
 S26 PI LEFT
               1500;
 S27 PI LEFT
               1800;
 S28 PI LEFT
               2100;
 S29 PI LEFT
               2400;
 S30 PI LEFT
               2700;
 S31 PI LEFT
               3000;
 S32 PI LEFT
               3300;
 S33 PI LEFT
               3600;
 S34 PI LEFT
               3900;
 S35 PI LEFT
               4200;
               4500;
 S36 PI LEFT
               4800;
 S37 PI LEFT
 S38 PI RIGHT
                900;
 S39 PI BOTTOM 9000 ;
 S40 PI BOTTOM 8400 ;
 S41 PI BOTTOM 8100 ;
               6300;
 S42 PI TOP
               9900;
 S43 PO RIGHT
               9300;
 S44 PO RIGHT
               9000;
 S45 PO RIGHT
               8700;
 S46 PO RIGHT
               8400;
 S47 PO RIGHT
               8100;
 S48 PO RIGHT
               7800;
 S49 PO RIGHT
               7500;
 S50 PO RIGHT
               7200;
 S51 PO RIGHT
               6900;
 S52 PO RIGHT
 S53 PO RIGHT
               6600;
```

```
6300;
S54 PO RIGHT
              6000;
S55 PO RIGHT
S56 PO RIGHT
              5700;
S57 PO RIGHT
              4800;
S58 PO RIGHT
              4500;
S59 PO RIGHT
              4200 ;
S60 PO TOP
              7500;
S61 P0 T0P
              7800;
              8100 ;
S62 PO TOP
S63 P0 T0P
              8400;
S64 PO RIGHT
              2700;
S65 PO RIGHT
              10500;
S66 PO RIGHT
              3900;
S67 PO RIGHT
              3000;
S68 PO RIGHT
               600;
S69 PO RIGHT
              3300;
S70 PO RIGHT
                 0;
S71 P0 T0P
              8700;
S72 P0 T0P
              9000;
S73 P0 T0P
              9300;
S74 P0 T0P
              9600;
S75 PO TOP
               600;
S76 PO TOP
              1200;
S77 PO TOP
              1500;
S78 P0 T0P
              1800;
S79 P0 T0P
              2100;
S80 PO TOP
              2400;
S81 P0 T0P
              2700;
S82 PO TOP
              3000;
S83 P0 T0P
              3300;
S84 P0 T0P
              3600;
S85 PO TOP
              3900;
S86 P0 T0P
              4200;
S87 PO TOP
              4500;
S88 P0 T0P
              4800;
              5700;
S89 P0 T0P
S90 PO TOP
              6000;
S91 P0 T0P
              6600;
S92 PO RIGHT
              3600;
              900 ;
S93 PO BOTTOM
S94 PB LEFT
              6000;
S95 PB LEFT
              6300;
S96 PB LEFT
              6600;
S97 PB LEFT
              6900;
S98 PB LEFT
              7200;
S99 PB LEFT
              7500;
S100 PB LEFT
              7800;
S101 PB LEFT
              8100;
S102 PB LEFT
              8400;
S103 PB LEFT
              8700;
S104 PB LEFT
              9000;
S105 PB LEFT
              9300;
S106 PB LEFT
              9600;
S107 PB LEFT
              9900;
S108 PB LEFT
              10500;
S109 PB BOTTOM 10500 ;
ENDIOLIST;
NETWORK;
C1 I2 S8 U1 S111 ;
C2 I2 S9 U2 S113 ;
C3 I2 S10 U3 S115 ;
C4 I2 S11 U4 S117;
C5 I2 S12 U5 S119
C6 I2 S13 U6 S121
C7 I2 S14 U7 S123
C8 I2 S41 U8 S125 ;
C9 I2 S40 U9 S127 ;
C10 I2 S4 S128 U10 ;
C11 I2 S6 S130 U11 ;
```

```
C12 I2 S19 U12 S133 ;
C13 I2 S42 U13 S135 ;
C14 I2 S20 U14 S137
C15 I2 S15 U15 S139 ;
C16 I2 S17 S140 U16 ;
C17 I2 S16 S142 U17 ;
C18 I2 S3 S144 S145 ;
C19 I2 S39 U18 S147 ;
C20 I2 S21 U19 S149;
C21 I2 S7 U20 S151 ;
C22 G11 S152 S153 ;
C23 G11 S152 S154;
C24 G11 S155 S156 ;
C25 G11 S156 S157 ;
C26 G11 S156 S158;
C27 G11 S159 S160;
C28 G11 S135 S161;
C29 G11 S162 S163 ;
C30 G11 S164 S165;
C31 G11 S166 S167 ;
C32 G11 S168 S169 ;
C33 I2 S5 U21 S171 ;
C34 G11 S172 S152;
C35 G11 S152 S173 ;
C36 01 S174 S93 ;
C37 O1 S175 S68 ;
C38 G11 S176 S175 ;
C39 B21 S157 S177 S178 S94 ;
C40 B21 S157 S179 S180 S95 ;
C41 B21 S157 S181 S182 S96 ;
C42 B21 S157 S183 S184 S97 ;
C43 B21 S157 S185 S186 S98 ;
C44 B21 S157 S187 S188 S99 ;
C45 B21 S157 S189 S190 S100 ;
C46 B21 S157 S191 S192 S101;
C47 02 S153 S193 S43 ;
C48 02 S153 S194 S44 ;
C49 02 S153 S195 S45 ;
C50 02 S153 S196 S46 ;
C51 O2 S153 S197 S47 ;
C52 02 S153 S198 S48 ;
C53 O2 S153 S199 S49 ;
C54 02 S153 S200 S50 ;
C55 G11 S201 S202 ;
C56 G21 S201 S162 S155 ;
C57 O2 S153 S172 S65 ;
C58 02 S153 S202 S66 ;
C59 O2 S173 S172 S59 ;
C60 02 S154 S203 S71 ;
C61 02 S154 S204 S72 ;
C62 02 S154 S205 S73 ;
C63 02 S154 S206 S74 ;
C64 02 S154 S207 S60 ;
C65 02 S154 S208 S61 ;
C66 02 S154 S209 S62 ;
C67 02 S154 S210 S63 ;
C68 B21 S158 S211 S212 S102 ;
C69 B21 S158 S213 S214 S103 ;
C70 B21 S158 S215 S216 S104 ;
C71 B21 S158 S217 S218 S105 ;
C72 B21 S158 S219 S220 S106 ;
C73 B21 S158 S221 S222 S107 ;
C74 B21 S158 S223 S224 S108 ;
C75 B21 S158 S225 S226 S109 ;
C76 02 S173 S227 S51 ;
C77 O2 S173 S228 S52 ;
C78 02 S173 S229 S53;
C79 O2 S173 S230 S54 ;
C80 02 S173 S231 S55 ;
```

```
C81 02 S173 S232 S56 ;
C82 02 S173 S233 S57 ;
C83 O2 S173 S234 S58 ;
C84 01 S169 S64 ;
C85 I2 S22 U22 S236
C86 I2 S23 U23 S238 ;
C87 I2 S32 U24 S240 ;
C88 I2 S33 U25 S242 ;
C89 I2 S34 U26 S244 ;
C90 I2 S35 U27 S246 ;
C91 I2 S36 U28 S248;
C92 I2 S37 U29 S250;
C93 I2 S24 U30 S252 ;
C94 I2 S25 U31 S254;
C95 I2 S26 U32 S256;
C96 I2 S27 U33 S258;
C97 I2 S28 U34 S260;
C98 I2 S29 U35 S262;
C99 I2 S30 U36 S264;
C100 I2 S31 U37 S266 ;
C101 I2 S38 U38 S164;
C102 02 S154 S167 S67;
C103 O2 S154 S163 S92 ;
C104 02 S154 S160 S69 ;
C105 02 S161 S268 S75 ;
C106 02 S161 S269 S76 ;
C107 02 S161 S270 S85;
C108 02 S161 S271 S86 ;
C109 02 S161 S272 S87
C110 02 S161 S273 S88 ;
C111 02 S161 S274 S89 ;
C112 02 S161 S275 S90 ;
C113 O2 S161 S276 S77
C114 02 S161 S277 S78;
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