

32-bit Floating-Point Multiplier Workflow

Input Breakdown:

- Inputs are 32-bit floating-point numbers: $A[31:0]$ and $B[31:0]$.
 - Split the inputs into components:
 - **Sign:** $A[31]$ and $B[31]$.
 - **Exponent:** $A[30:23]$ and $B[30:23]$ (8-bit, biased by 127).
 - **Mantissa:** $A[22:0]$ and $B[22:0]$ (23 bits, add an implied leading 1).
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Step 1: Sign Calculation:

- Compute the result's sign using XOR: $\text{Signresult} = A[31] \oplus B[31]$
 $\text{Signresult} = A[31] \oplus B[31]$
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Step 2: Exponent Addition:

- Add the exponents: $\text{Exponentsum} = A[30:23] + B[30:23]$
 $\text{Exponentsum} = A[30:23] + B[30:23]$
 - Subtract the bias (127) to get the adjusted exponent:
 $\text{Exponentresult} = \text{Exponentsum} - 127$
 $\text{Exponentresult} = \text{Exponentsum} - 127$
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Step 3: Mantissa Multiplication:

- Add the implied leading 1 to each mantissa:
 $\text{MantissaA} = \{1'b1, A[22:0]\}$, $\text{MantissaB} = \{1'b1, B[22:0]\}$
 $\text{MantissaA} = \{1'b1, A[22:0]\}$, $\text{MantissaB} = \{1'b1, B[22:0]\}$
 - Multiply the 24-bit mantissas to get a 48-bit intermediate product:
 $\text{Mantissaproduct} = \text{MantissaA} \times \text{MantissaB}$
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Step 4: Normalization:

- If the most significant bit of the product is 0, shift left and decrement the exponent by 1.
- Keep the top 23 bits as the normalized mantissa.

Step 5: Rounding:

- Apply truncation (round toward zero) by taking the top 23 bits of the normalized mantissa.

Step 6: Output Assembly:

- Combine the computed components to form the 32-bit result:
$$\text{Result} = \{\text{Signresult}, \text{Exponentresult}, \text{Mantissareult}[22:0]\}$$
$$\text{Result} = \{\text{Signresult}, \text{Exponentresult}, \text{Mantissareult}[22:0]\}$$