Pin Planning of DE1 Board

Experiment No: 1
Experiment Name: 8-bit Up-Down Counter

Node Name	Direction	Location
clk	Input	PIN_A12
up_down	Input	PIN_L22
reset	Input	PIN_L21
out[7]	Output	PIN_U18
out[6]	Output	PIN_Y18
out[5]	Output	PIN_V19
out[4]	Output	PIN_T18
out[3]	Output	PIN_Y19
out[2]	Output	PIN_U19
out[1]	Output	PIN_R19
out[0]	Output	PIN_R20