

CPU Scheduling"

Approval time: The time at which process enter the Ready Eleve On State Distration

Burst time. Time graquised by a process to get execute on CPU.

Completion time: The time at which process complete its execution.

Twin Assaud time: {Completion time - Assival time } Ip bound

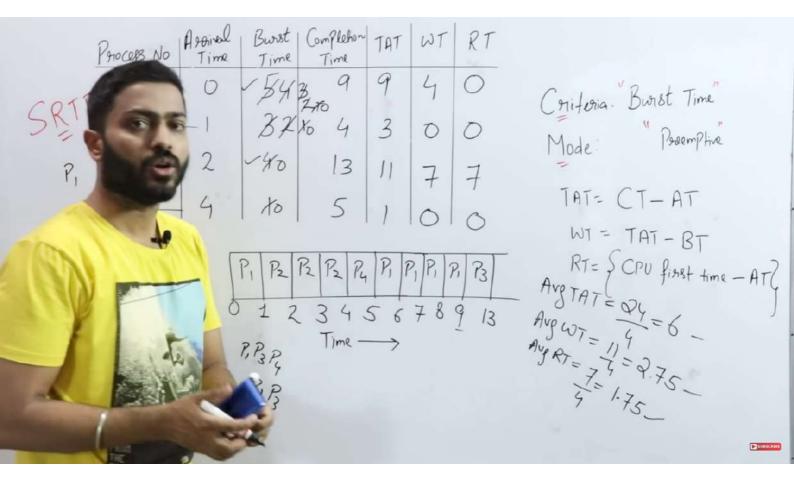
Waiting time: {Twin Assaud time - Bust time }

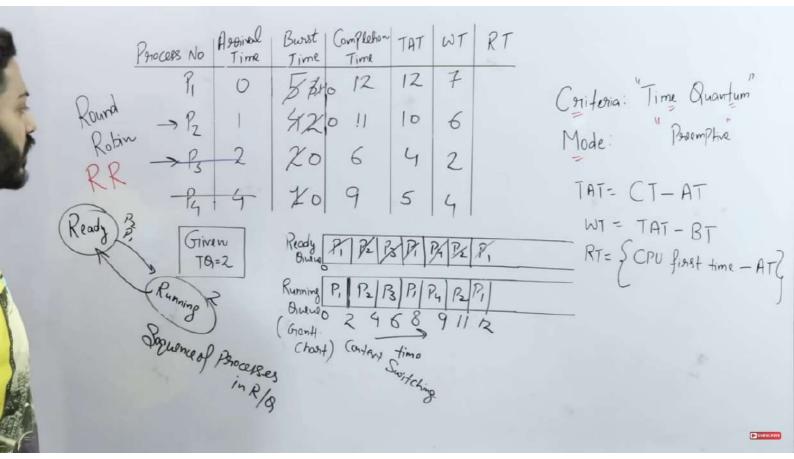
Response time: {The time at which a process gpt CPU first time}

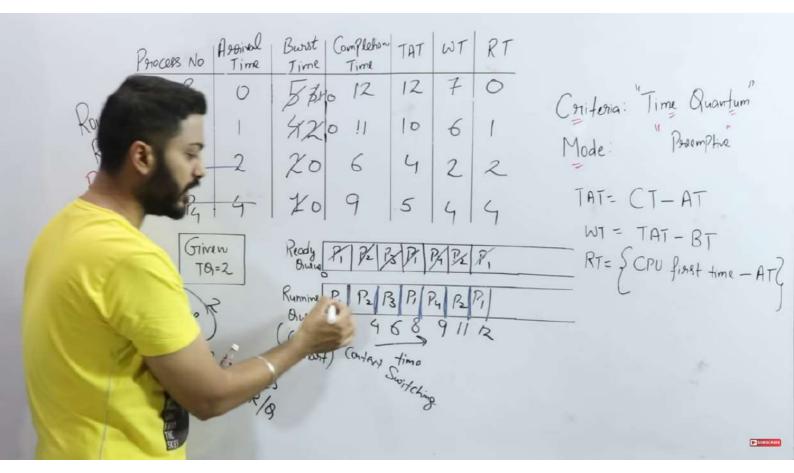
		Process No	Assiral	Execu Burst Time	Completion Time	TAT	WT	RT	
	FCFS	是是是	0 1 5 6	2 2 3	2 4 8	2 3 3	0 1 0	0 1 0	Conitoria: "Apaival Time" Mode: Non-Possemptive
		Grant Cho			12 P3 P4		2	2 CT	- AT = TAT T-BT = WT
Z.									<b>→</b> 500950400

Parocess No	Asomel Time	Burst	Completion Time	TAT	WT	RT	
JP,	- 1	3	6	5	2		Criteria. Burst Time
SJY -P2	-2	4	10	8	9		Mode: Non-Pasemptive
V-P3	1	2	3	2	0	0	-
-P4	14	4	14	10	6		TAT= CT-AT
Crowle (	hat [	S 18	100	2	-		WT = TAT - BT
0100111	1	(1/					
		BP	-				
· Ca		4					
1							<b>■</b> SOFFSCRIPET
	SJE -P2 -P2 -P4	SJF -P2 -2 -P3 -1 -P4 4 Grant Chart &	SJF -P2 -2 4 -P2 -2 4 -P4 4 4 Grantt Choot P3	SJF -P2 -2 4 10 -P2 -2 4 10 -P3 1 2 3 -P4 4 14 Grantt Chart P3 P1 P2 0 1 3 6 10	SJF -P2 -2 4 10 8 -P2 -2 4 10 8 -P3 1 2 3 2 -P4 4 14 10 Grant Chart P3 P1 P2 P4	SJF -P2 -2 4 10 8 4 -P2 -2 4 10 8 9 -P4 4 14 10 6 Grant Chart P3 P1 P2 P4 0 1 3 6 10 14	SJF -P2 -2 4 10 8 4  -P2 -2 4 10 8 9  -P4 4 14 10 6  Grant Chart P3 P1 P2 P4  0 1 3 6 10 14

Process No France Burst Completion TAT WT RT Time Time Time To 4  SRIF - P2 1 32 10 4 3 0  P1 P3 2 -40 13 11 7  P4 4 10 5 1 0	Cariforia. Burst Time"  Mode: Passemptive  TAT = CT - AT  WT = TAT - BT
Grant Chart P1 P2 P2 P2 P4 P1 P1 P1 P1 P3  [P]  O 1 2 3 4 5 6 7 8 9 13  P1 P2 P3 P1 P2 P3 P1 P3 P1 P1 P1 P1 P1 P3  P1 P2 P3 P1 P3 P1 P3 P1 P3 P3 P4 P1 P4	WT = TAT - BT  RT = & CPU first time - AT?





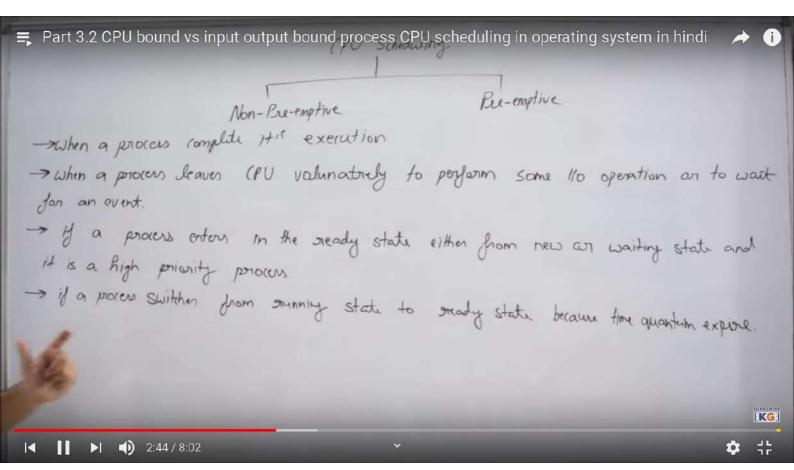


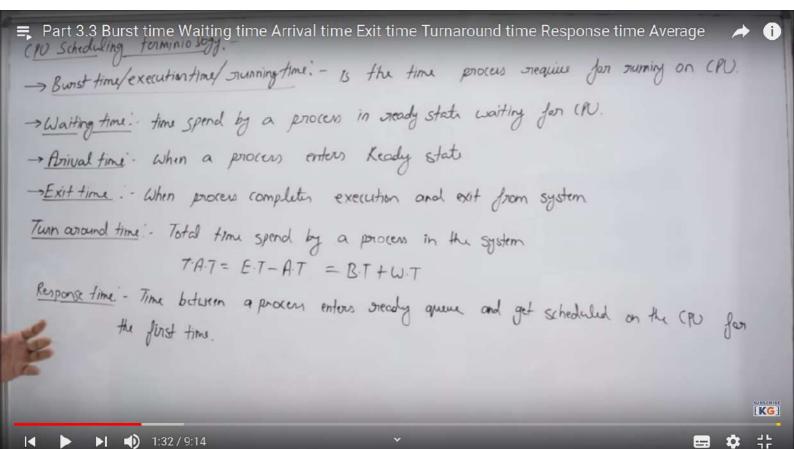
Pariority Parocess No	Assiral Time	Burst   Time		TAT	WT	Parionity Scheduling
-10 - 91	0	54	12	12	7	Carillain "Pariparity"
20 × PZ	+ 1	43	8	7	3	Coniteria: "Porioonity" Mode: Pasemptive
30 P3	2	Zto	4	2	0	
40 - 24	4	20	5	(	0	TAT= CT-AT WT = TAT-BT
Higher the no higher the Po	P. P.	2 P3 P3 2 3 s	P4	P2 P,   8 R	AVJTA1 = 22 = 5.5  AVJTA1 = 22 = 5.5	

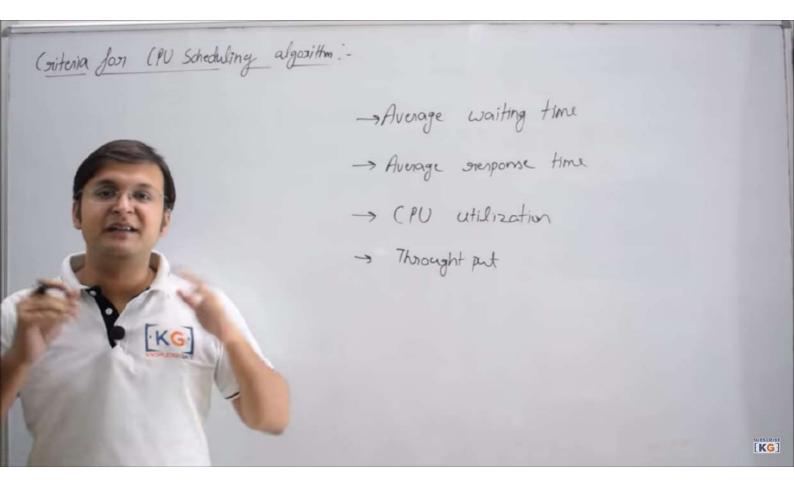
CPU Scheduling :-- A priorers execution consist of a cycle of CPU execution and I/o execution. -> Normally every process begins with CPU burst that may be followed by I/o burst, then another (PU burst and then 1/0 burst and so on eventually in the Sort will and up on CPU burst. (PU bound pricess: There are those porcesses which require most of time on (PU I/o bound process: There are those perocess which require most of the time on I/o devices On peripherals. Conclusion: A good (PU scheduling idea should choose the mixture of both so that both

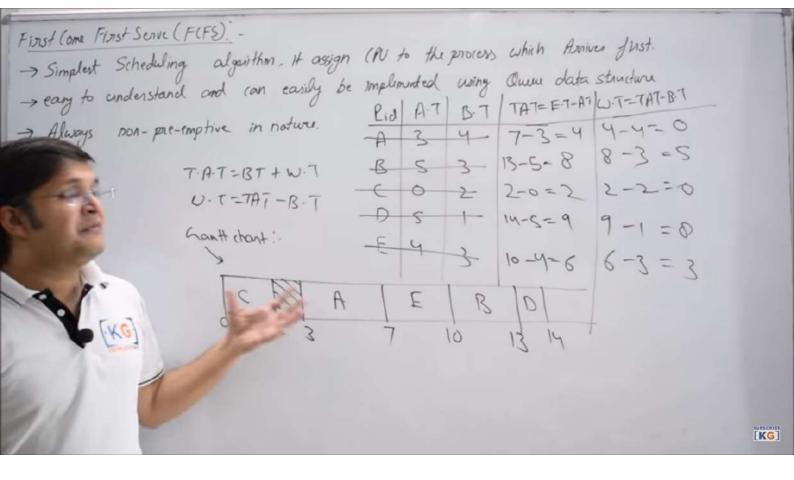
[KG]

To devices and (PU can be willised efficiently









Shantest Job First (non-pre-emptive) Shantest Remaining time	e-emption)	Please check my new channel (					
→ Out of all available process, (PU is assigned requirement (no privarity, no seniority)  → If there is a fix, F(FS is used to kneak tix  → (an be used both with non-pru-emptive and pru-emptive approach  → pre-emptive version (SRTF) is calso called as optimal as it gurantee minimal average waiting time.	Pid Pi PL	AT 3	B.T	smallest	bunst til	ne	
						[]	(G)

