# High-Speed Stereo Visual SLAM For Low-Powered Computing Devices

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Abstract—We present an accurate and GPU-accelerated Stereo Visual SLAM design called Jetson-SLAM. It exhibits frameprocessing rates above 60FPS on NVIDIA's low-powered 10W Jetson-NX embedded computer and above 200FPS on desktopgrade 200W GPUs, even in stereo configuration and in the multiscale setting. Our contributions are threefold: (i) a Bounded Rectification technique to prevent tagging many non-corner points as a corner in FAST detection, improving SLAM accuracy. (ii) A novel Pyramidal Culling and Aggregation (PyCA) technique that yields robust features while suppressing redundant ones at high speeds by harnessing a GPU device. PyCA uses our new Multi-Location Per Thread culling strategy (MLPT) and Thread-Efficient Warp-Allocation (TEWA) scheme for GPU to enable Jetson-SLAM achieving high accuracy and speed on embedded devices. (iii) Jetson-SLAM library achieves resource efficiency by having a data-sharing mechanism. Our experiments on three challenging datasets: KITTI, EuRoC, and KAIST-VIO, and two highly accurate SLAM backends: Full-BA and ICE-BA show that Jetson-SLAM is the fastest available accurate and GPU-accelerated SLAM system (Fig. 1).

#### SUPPLEMENTARY MATERIAL

Code: https://github.com/ashishkumar822/Jetson-SLAM

Video: See attachment.

#### I. INTRODUCTION

A centimeter-accurate local positioning system is crucial for complex robotic and flight autonomy systems to execute navigation, control, and visual servoing tasks precisely [1]. Visual odometry (VO) can be employed for this purpose, but it discards older environmental observations and lacks global consistency [2]. This causes pose-estimation drifts over time, although an agent navigates in the same area.

In contrast, visual SLAM offers drift-free localization and mapping, which enables the precise execution of autonomous tasks. In this context, *Stereo visual SLAM* is particularly interesting due to its high metric accuracy and low-cost sensor demands. However, its compute-intensive frontend (feature detection-extraction-matching, stereo-matching) and backend (graph optimization, loop-closure, localization and mapping) quickly exhaust low-powered devices. Also, the limited computing power of these devices forces a SLAM system to drop intermediate frames [3], resulting in reduced frame rate and tracking failure [2].

This situation becomes difficult due to shared computing resources in the presence of co-existing modules such as data acquisition, control, grasping, and compute-intensive deep networks. It can easily cause a catastrophic system failure, e.g., the divergence of the control system from the desired trajectory due to delays in position feedback. For these

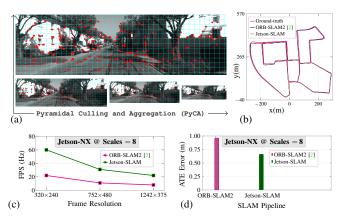


Fig. 1: (a) Output of Jetson-SLAM's GPU-accelerated and resource-efficient Frontend–Middle-end design, (b) the output trajectory, (c) Frames-Per-Second benchmarking on Jetson-NX embedded computer, and (d) SLAM performance on a KITTI sequence.

reasons, UAV-based autonomous manipulation [1] executed SLAM on a remote computer. [4] explored network computing for SLAM but is unsuitable for isolated robotic systems.

Although there have been efforts to speed up VO systems, SLAM still remains untouched. For instance, recent [3] only benchmarks runtime of existing VO systems [5], [6], [7] onto Jetson devices. In VO, restricting feature count by dividing an image into grids and picking one feature per grid is commonly used. [8] is such an approach for GPU. However, its design is limited only to monocular VO, preventing its use in SLAM. Its unconfigurable scale factor of two in a multiscale setting reduces the image resolution aggressively, resulting in fewer features which are further trimmed to only one feature pergrid irrespective of the number of scales. It leads to inadequate feature points in SLAM, causing tracking failures (see video). Moreover, it is inefficient for smaller grids when deployed onto embedded computing devices.

ORB-SLAM2 [2], ICE-BA [9] are highly accurate SLAM systems. However, their complex CPU-only workload turns them slower and exhausts the low-powered devices. SLAM-Core [10] is CPU-efficient SLAM but is not open source and is not benchmarked alongside deep networks. Despite these limitations, a high-speed SLAM system is the need of the current time with a huge scope in modern autonomy systems.

In visual SLAM, the frame rate is mainly governed by the frontend load, which varies with the image resolution and doubles in stereo mode (of our interest). Also, the number of features yielded by the frontend affects the backend load. Thus we develop a GPU-accelerated frontend from scratch which produces a sufficiently smaller number of impactful features at high-speed by harnessing the on-chip GPU of the embedded computers. However, in this endeavor, the

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scarcity of GPU cores appears as a bottleneck that limits the maximum achievable speed, and an inefficient use of the cores degrades the GPU throughput. We tackle this issue via our algorithmic and system development contributions, resulting in a high-speed, accurate, and resource-efficient GPU-accelerated SLAM system available to date, called *Jetson-SLAM*. Our contributions are:

- a) Bounded Rectification: prevents misclassification of non-corners as corners in FAST features [11], and improves SLAM accuracy by producing impactful corners (Sec. II-A).
- b) Pyramidal Culling and Aggregation (PyCA): It yields high-quality multiscale features via our Multi-Location Per-Thread (MLPT) culling, and Thread Efficient Warp-Allocation (TEWA) to deliver high speeds (2000 FPS) and high computing efficiency even in the scarcity of GPU cores (Sec. II-C).
- c) Frontend-Middle-end-Backend Design of Jetson-SLAM: We develop a new SLAM component called Middle-end that houses stereo-matching, feature matching, feature-tracking, and performs data-sharing to avoid CPU-GPU memory-transfer overhead of duplicating-and-accessing intermediate results needed across SLAM components (Sec. III).

Despite we contribute in the frontend and system design, the middle-end and backend performance also gets boosted. It turns Jetson-SLAM efficient and accurate while reaching above 60FPS @432 × 240, even at eight scales in stereo mode on Jetson-NX alongside VGG [12] deep neural network. The high speed minimizes tracking failures during camera rotations (video), and facilitates developing autonomous UAVs which still rely on external positioning systems [1].

Next, we discuss our algorithmic contributions (Sec. II), and system development contributions (Sec. III). Experiments are presented in Sec. IV, with conclusions in Sec. V.

# II. METHODOLOGY

## A. Bounded Rectification for Corner Detection

In FAST [11] detection, the number of consecutive dark  $(N_d)$  and bright pixels  $(N_b)$  are computed for a segment around each image pixel, known as Bresenhem circle [11] of radius 3 and a length  $N_{seg} = 16$  pixels. If there exists  $P_{min}$  brighter or darker pixels, the center pixel is labeled as a corner (Eq. 1).

$$L_p = egin{cases} ext{bright,} & I_c - I_p < -arepsilon \ ext{similar,} & -arepsilon < I_c - I_p < arepsilon \ ext{dark,} & I_c - I_p > arepsilon \end{cases}$$

$$L_c = \begin{cases} \text{Corner}, & N_b, \ N_d \ge P_{min} \\ \text{Non-corner}, & \text{otherwise} \end{cases} \tag{2}$$

where,  $L_p$  is the pixel label,  $I_c$  and  $I_p$  are the intensities of the center pixel and any pixel on the segment respectively.  $\varepsilon$  is often set to 20, and  $L_c$  denotes the center pixel label.

We note that the above process misclassifies many noncorner points as corners. Such points are essentially statistical outliers having a shot-noise-like or tiny blob-like appearance. Interestingly, they satisfy Eq. 1, 2, but can not be used as map points due to their highly similar appearance which confuses the stereo-matcher. For instance, all segment pixels in Fig. 2a are bright and  $N_b > P_{min}$ , yet it is not a corner. Fig. 2b shows a



Fig. 2: (a) A non-corner but all bright pixels on its Bresenhem circle [11], (b) Real-image examples of such points tagged as a corner. Each rectangle ( $\square$ ) denotes a pixel in a  $7 \times 7$  patch of the image.

real-world case. This issue needs to be resolved since the error in the frontend propagates to the other SLAM components. Thus we aim to discard such points at the detection stage itself because it reduces the number of features reaching the stereo-matcher which lowers the matching time. To this end, we propose a key enhancement called *Bounded Rectification* which not only improves the corner quality but also improves the metric accuracy of Jetson-SLAM significantly.

*Rectification:* we propose to rewrite Eq. 2 which performs upper and lower bounded rectification (Eq. 3).

$$L_c = \begin{cases} \text{Corner, } P_{min} \le N_b, N_d \le P_{max} \\ \text{Non-corner, otherwise} \end{cases}$$
 (3)

This formulation rejects the cases similar to Fig. 2a by upper and lower bounding  $N_b$  or  $N_d$  because for the center pixel to be a corner, all of its surrounding pixels can not be bright or dark simultaneously. This can be assured *iff*  $P_{max} < 16$ , which is the drawback of Eq. 2.  $P_{min}$  is generally set to 9 whereas  $P_{max}$  can be set anywhere  $\in (P_{min}, 16)$ . We use  $P_{max} = 13$  in our case which does not restrict corner diversity but can be adjusted based on the nature of the visual scenes.

Bounded rectification-based FAST detection outputs a Corner Response Matrix (CRF-matrix, Sec. III-.1), which is utilized by our PyCA technique. Next, we brief GPU terminologies for better grasping of the upcoming text.

## B. GPU Fundamentals

NVIDIA GPUs comprise *streaming multiprocessors* (SM), each having multiple GPU *cores*, and an on-chip *shared memory* with low memory access cost (MAC). Off-chip *global memory* is also present but has a higher MAC. A GPU performs computing in *warp* consisting of 32 *threads* that are concurrently executed on an SM. A *block* comprises many such warps which all are executed on the same SM even if the other SMs are sitting idle. It is so because the threads of a block residing onto an SM may need to communicate with each other, and if warps are executed on different SMs, communication can only be achieved via global memory which has higher MAC, in contrast to the shared memory which has lower MAC but is inaccessible to the other SMs.

## C. Pyramidal Culling and Aggregation (PyCA)

PyCA detects robust features at high speeds (in  $\mu$ S) on GPU via its Feature Culling (FC) and Pyramidal Feature Aggregation (PFA) steps. FC harvests the strongest corners by suppressing the weaker ones, mimicking a culling behaviour, while PFA harvests robust features from the output of FC applied at multiple scales. PyCA remains aware of GPU core scarcity, which, if left unaddressed, reduces the throughput.

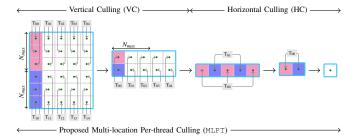


Fig. 3: Feature Culling (FC) for a  $6 \times 5$  cell.  $T_{ij}$  is a CUDA-thread of CUDA kernel [13]. A '·' indicates the corner strength of a pixel.



Fig. 4: (a) coalesced, and (b) non-coalesced memory access. '□' denotes contiguous memory block, and a '□' denotes  $i^{th}$  warp thread. In coalesced access, 32 threads read in one machine-cycle, whereas in non-coalesced access, the memory transactions are serialized [13].

- 1) Feature Culling (FC): It divides the CRF-matrix into non-overlapping cells of a size  $(c_h, c_w)$  pixels, and then performs vertical, and horizontal culling in each cell (Fig. 3).
- a) Vertical Feature Culling: In this step, a cell is traversed vertically, and the maximum response is recorded for each column. To achieve that, we propose Multi-Location Per Thread (MLPT) culling in which we divide the vertical cell dimension  $c_h$  into chunks (Multi-Location), and then process each of them with a single thread (Per-Thread). The total number of threads required in this process is given by:

$$N_t = \min(1, \lceil c_h/N_{max} \rceil), \tag{4}$$

where  $N_{max}$  is the maximum location that a single thread processes. It is adjustable per the needs. Based on our analysis, it can be set in the range [1, 10] for common cell sizes.

Now each of the  $N_t$  threads stores its result in shared memory which is utilized by the first thread  $(T_{0j})$  of each column. The thread  $T_{0j}$  finds the strongest response among  $N_t$  values and stores the result for horizontal feature culling.

For the above operation,  $\log_2$ -reduction [14] can also be used, however, it processes only two locations per thread, thus requiring a huge number of threads per cell which incurs GPU kernel-launch overhead. Moreover, it is repeated  $\lceil \log_2(c_h) \rceil$  times which increases the number of warps required and thus increased runtime to process multiple cells. Also, culling an entire column using one thread becomes slower and inefficient for larger cells in a scenario of core scarcity.

In contrast, MLPT avoids these issues by using fewer threads regardless of the cell size and also conforms to memory coalescing (Fig. 4). This can be verified via Table I, that  $\log_2$ -reduction requires more warps which induce overhead, even for one cell on Jetson-NX-like embedded devices having core scarcity (Table III). Since multiple cells exist in an image, MLPT can run faster by the order of milliseconds.

b) Horizontal Feature Culling: Now feature culling is performed in the horizontal direction over  $c_w$  responses, stored in the shared memory by the previous step. In this case, memory is consecutive, therefore memory coalescing can not

Table I. MLPT vs  $\log_2$ -reduction.  $N_w$ : number of warps.

Cell-size	Culling	Nmax	$N_t$	Total	$N_W$	Time (µS		
Celi-size	Scheme	wmax		Threads	1VW	RTX-2070	Jetson-NX	
20 × 32	• log <sub>2</sub>	_	10	320	10	4.9μS	11.4µS	
20 × 32	• MLPT	5	4	160	5	4.6μS	8.7µS	
50 × 32	• log <sub>2</sub>	_	25	800	25	3.2µS	20.4μS	
30 × 32	• MLPT	5	4	320	10	2.9μS	16.3μS	
100 × 32	• log <sub>2</sub>	_	50	1600	50	3.8µS	14.0µS	
100 × 32	• MLPT	10	10	320	10	2.6μS	10.6μS	

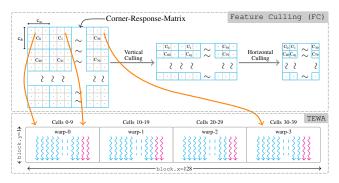


Fig. 5: Illustration of FC + (TEWA) scheme. FC is applied over the CRF-Matrix which produces the strongest corner in a cell. A ' $C_i$ ' is a cell, and a ' $\stackrel{\downarrow}{\downarrow}$ ' and a ' $\stackrel{\downarrow}{\downarrow}$ ' denote a working and an idle/wasted thread in a warp respectively for a  $3 \times 3$  cell-size.

be achieved, but memory transactions can be minimized. To achieve that, a thread in MLPT always accesses an element at a stride of  $N_{max}$  instead of a consecutive location, unlike vertical culling. This combines the memory transactions of  $N_t$  threads of a warp into one. The remaining process is the same as the previous i.e. MLPT performed over  $c_w$  locations which produce the strongest corner in the cell if present.

c) Thread Efficient Warp-Allocation (TEWA): A naïve way to allocate GPU for FC is to assign a block size equal to the cell size. However, a job requiring less than 32 warp threads (W) leads to a wastage of leftover threads since they do not involve in the computations but are still part of a warp. This leads to poor throughput on Jetson devices due to core scarcity. The multiscale scenario becomes more challenging due to smaller cells at lower scales. For instance, a block consists of 9 threads for a  $3 \times 3$  cell which if executed in a warp, will lead to a wastage of 23 threads, indicating severely low warp-efficiency, defined as:

$$\eta_w = \frac{N_{ta}}{WN} \tag{5}$$

where,  $N_{ta}$ ,  $N_w$ , W are the number of active threads, number of warps required, and threads per warp respectively.

Hence, we propose a thread-efficient warp allocation scheme (TEWA) that offers high warp efficiency regardless of the cell size. In this scheme, we assign multiple cells to a single thread block by virtually partitioning the warps into chunks, each chunk handling one cell. We fix x dimension (block.x) of the thread block to 128, and find the maximum number of the cells that can be fit into block.x, while y dimension (block.y) of the block is set to  $N_t$ . Together both of these numbers are utilized to launch the GPU kernels. It might be the case that a few threads in the block might still sit idle because the warp-size W is not an integer multiple of cell-size, however, the wastage is minimal in TEWA.

Fig. 5 depicts the TEWA design, where, FC for the entire CRF-matrix occurs in one CUDA kernel-launch call that

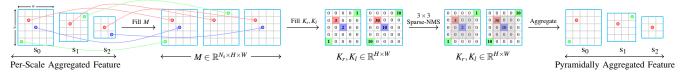


Fig. 6: Pyramidal Feature Aggregation (PFA). Steps of a single  $5 \times 5$  image at scale factor 1.2 and three scales. A ' $\circ$ ' indicates a survived point in the PFA process, a ' $\circ$ ' indicates a suppressed point, and a ' $\circ$ ' indicates an unaffected point.

Table II. Thread-Efficient Warp allocation (TEWA) efficiency (Eq. 5).

Cell-size	Warp allocation Scheme	Nta	$N_W$	$\eta_w$
3 × 3	<ul> <li>Single block per cell</li> </ul>	9	1	28%
3×3	• TEWA	30	1	94%
5 × 5	<ul> <li>Single block per cell</li> </ul>	25	1	78%
3 × 3	• TEWA	30	1	94%
7 × 7	<ul> <li>Single block per cell</li> </ul>	49	2	76%
1 × 1	• TEWA	28	1	88%

avoids additional launch overhead. Moreover, multiple cells are processed in a single warp which reduces the number of warps and cores required. This leads to high throughput in FC contrary to the naïve allocation (Table II), eventually reducing the runtime and latency, even in GPU core scarcity.

2) Pyramidal Feature Aggregation (PFA): It is the final step of PyCA that processes the features obtained by performing FC at each scale of the image pyramid in a multiscale setting. PFA reduces the overall feature count, which lowers the feature and stereo-matching calculations, thus lowering the overall runtime without affecting the SLAM accuracy.

PFA is motivated by two reasons: *First*, the multiscale setting is crucial in SLAM to obtain robust features which govern tracking, localization, and mapping accuracy. Hence, features that project to a common pixel at the native scale (s<sub>0</sub>), need to be prioritized because of their uniqueness and consistency across scales. *Second*, the projected features may fall into the vicinity of each other, which confuses the feature-matcher and stereo-matcher, and also increases the computation time of the frontend (feature extraction), middle-end (stereo matching, tracking) and backend (optimization).

PFA is carried out in three steps (Fig. 6). Firstly, we project all keypoints to  $s_0$ , i.e.  $x_0 = x_n \times \zeta^n$ , and copy their responses in a 3D matrix  $M \in \mathbb{R}^{N_s \times H \times W}$  initialized with zeros. Here  $x_0$  denotes  $s_0$  correspondent of a coordinate  $x_n$  at  $n^{th}$  scale with scale-factor  $\zeta$ , and  $N_s, H, W$  denote the number of scales, image height and width respectively at the  $s_0$ . Secondly, we compute two metrics for each keypoint, i.e. the sum of the corner responses across scales if a keypoint is detected at multiple scales  $(k_r)$ , and the total number of levels at which it is detected  $(k_l)$ . These scores are stored as two matrices  $K_r, K_l \in \mathbb{R}^{H \times W}$ . Finally, we perform non-maximal suppression (NMS) over the  $K_r, K_l$  matrices, but only sparsely in a  $q \times q$  window (q = 3 adjustable) around each keypoint, saving a lot of computations. The keypoint is suppressed if its  $k_r$  and  $k_l$  scores are smaller than any keypoint in the window.

PFA is crafted such that it runs on GPU while avoiding CPU-GPU memory transfer. On the contrary, if run on CPU, its operation unnecessarily consumes CPU and requires CPU-GPU memory transfer, because the input to PFA resides on GPU. It also keeps the CPU occupied, and reduces the memory-transfer bandwidth due to small-sized data transfer, a point of consideration for Jetson devices.

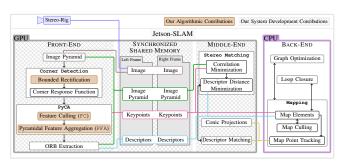


Fig. 7: Overall Jetson-SLAM design, highlighting our contributions.

#### III. SYSTEM INTEGRATION

In this section, we describe our system development contributions i.e. our new Frontend-Middle-end Jetson-SLAM design (Fig. 7), and strategic integration to optimize information flow. These are crucial to achieving resource efficiency despite the frontend achievements because SLAM components now are multi-device residents (CPU and GPU)

- 1)  $\mu$ -Sec. Efficient FAST Detection: We use two 16-bit integers  $B_b$  and  $B_d$  whose each bit denotes one of the 16 locations on the Bresenhem path (Sec. II-A), and is computed via Eq. 1. A '1' bit corresponds to  $L_p = \texttt{bright}$  in  $B_b$  and  $L_p = \texttt{dark}$  in  $B_d$ , whereas a '0' bit signifies  $L_p = \texttt{similar}$ . To speed up the process, we construct a lookup table where all the 16-bit combinations are pre-calculated to be a corner or a non-corner based on the proposed bounded rectification technique (Eq. 3). We use the sum of absolute differences ( $|I_c I_p|$ ) over the circle as the corner response [15].
- 2) Streamlined MultiScale Detection & Extraction: Our frontend is dedicated to high-speed SLAM, however, it can be used as a VO frontend which only performs detection. In contrast, SLAM computes descriptors as well for the map elements, making SLAM slower than VO. The multiscale setting is more compute-intensive, and the scarcity of GPU cores prevents the concurrent execution of multiple jobs. Thus we employ CUDA-streams [13] for faster execution such that when GPU is released for one scale, its CPU work begins, while at the same time, GPU is allocated to another scale.

For extraction, ORB descriptor [16] is used due to its speed and uniqueness but existing implementations extract serially for each scale [15], [17]. Since we have the multiscale key points ready, we perform the extraction for all scales at once. To do so, we parallelize Gaussian filtering via CUDA streams which is essential for robustness [2]. Then the ORB extraction which is a quite time-consuming step and prevents memory coalescing, is performed at once for all scales, leading to high-speed multiscale detection and extraction (Fig. 15).

3) Middle-end: Despite the accuracy, stereo visual SLAM [2] poses high computing demands for Jetson-like devices. In

Table III. GPU devices specifications.

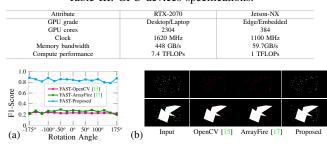


Fig. 8: (a) Repeatability analysis of Bounded Rectification. (b) Top: only non-corners. Bottom: non-corners and corners. Bounded rectification suppresses non-corners while the baselines [15], [17] fail. Green and red circles are ground truth, and detections.

addition, unlike VO, conic projections of the map points in feature tracking are also time-consuming [2]. Hence to attain higher throughput, we parallelize both of them which now forms the middle-end. However, naïvely doing so results in inefficiency, because stereo matching requires the descriptors and images to be present in the GPU memory. Since they are required by many SLAM components, creating multiple copies is not desirable for a resource constraint platform.

Thus, we design an information flow that facilitates sharing of the data structures between the frontend and the middle-end (Fig. 7). It saves memory consumption by preventing duplication which in turn avoids the overhead of CPU-GPU memory transfer. Achieving this objective is a programmatically complex task that we tackle via synchronized shared memory, as discussed below.

4) Synchronized Shared Memory (SSM): We adopt synchronized memory primitives from [18], and on top of which, we build synchronized shared memory (SSM) that wraps CPU-GPU transfers and memory allocation/de-allocation calls. Since feature count across frames keeps varying, stereomatching and tracking demand variable memory. In such cases, SSM reduces dynamic memory allocation/de-allocations calls by performing them only when the requested memory exceeds the current size. Also when CPU-GPU memory is accessed, SSM on its own transfers the underlying data to the destination device, reducing the framework's complexity.

## IV. EXPERIMENTS

We evaluate our contributions on a desktop GPU: NVIDIA RTX-2070 (200W) and an embedded device: Jetson-NX (10W). Please see Table III for the GPU specifications.

## A. Bounded Rectification For Corner Detection (Sec. II-A)

Due to the lack of a labeled corner dataset, we use synthetic data following [19]. We perform rotation transformation as it is the most challenging case [19] to analyze Repeatability (F1-score) of bounded rectification. Fig. 8a shows that bounded rectification drastically improves the corner quality while outperforming [15], [17]. Fig. 8b shows a qualitative analysis.

## B. Pyramidal Culling and Aggregation (PyCA) (Sec. II-C)

PyCA is a two-phased technique, however due to the lack of aligned baselines, we evaluate its feature culling (FC), and pyramidal feature aggregation (PFA) steps separately.

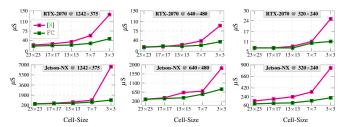


Fig. 9: Evaluation of Feature Culling (FC) procedure of PyCA.

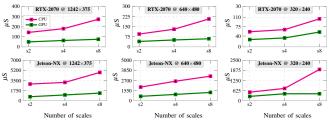


Fig. 10: PFA Runtime on CPU and GPU, scales=8, scale factor=1.2.

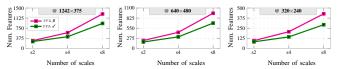


Fig. 11: Effect of PFA on features across scales and resolutions.

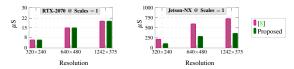


Fig. 12: GPU kernel Performance for CRF Computations in FAST.

1) Feature Culling (FC): [8] is loosely comparable with PyCA in FC mode only. We report wall-time (obtained via NVIDIA profiler) for which the computing resources are occupied by FC or [8]. Fig. 9 shows the comparison at various resolutions, common cell sizes, and different GPUs.

Noticeably, FC has a roughly linear timing profile w.r.t the cell size, whereas [8] grows exponentially. Also, the runtime gap is significant in smaller cells, irrespective of the GPU. It indicates computing resources consumption by FC only for a small duration, even in core scarcity. This advantage is attributed to our resource-efficient MLPT and TEWA schemes.

2) Pyramidal Feature Aggregation PFA: Due to the lack of matching baselines, we study the runtime of PFA on CPU and GPU, and we analyze its effect on the number of features across resolutions and scales. In the runtime analysis, we observe a huge gap in the CPU and GPU modes (Fig. 10), thanks to the parallelizable and sparse-NMS design of PFA. While the other analysis (Fig. 11) shows that PFA reduces the number of features lowers the Jetson-SLAM middle-end and backend runtime by  $\sim 2-4$ ms on high-resolution KITTI images, without affecting the SLAM accuracy (Fig. 16).

# C. µ-Second Efficient CRF Computations (Sec. III-.1)

Fig. 12 compares time to compute CRF-matrix via bounded rectification against [8]. On the desktop GPU, similar runtime is observed due to many cores (2304). On Jetson-NX with only 384 cores, ours runs 50% faster regardless of the resolution, thanks to our simplified GPU kernel that avoids

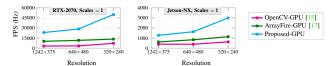


Fig. 13: FAST detection. Proposed vs open-source baselines.

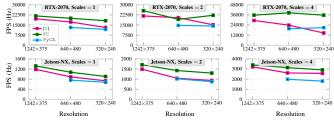


Fig. 14: Multi-scale FAST detection. PyCA@FC vs a loosely comparable [8]. PyCA@FC+PFA is shown for reference.

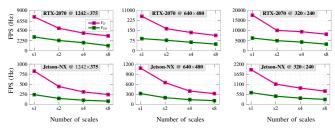


Fig. 15: Throughput of our frontend across resolutions, scales and devices. ' $F_D$ ': Detection rate, ' $F_{DE}$ ': Detection-Extraction rate.

warp divergence and guarantees coalesced access to image data. This contrasts to [8], which performs additional address extraction steps in the look-up tables, leading to warp divergence and slower speeds in case of GPU core scarcity.

## D. High-Speed FAST Detection over GPU (Sec. III-.1)

Fig. 13, 14 compares frame-rate of our PyCA-based FAST detection with single-scale [17], [15] approaches, and a multi-scale approach [8] (comparable with PyCA in FC mode only). Noticeably, PyCA is faster than the baselines with predominant gains on Jetson-NX. It indicates fewer computing resources needed by PyCA and for a short duration only.

#### E. High-Speed Multiscale Detection-Extraction (Sec. III-.2)

We present a frame-rate analysis of FAST detection and ORB extraction across resolutions and scales. The timings include image upload to GPU, image-pyramid, CRF, PyCA, ORB extraction, and keypoint download to CPU.  $F_D$  and  $F_{DE}$  denote the detection and detection-extraction frame rates.

Fig. 15 shows that detection at a high resolution of  $1242 \times 375$  can run at 1000FPS for a single scale and at 250FPS for eight scales. The detection-and-extraction can run at 250 FPS for a single scale and 80FPS for eight scales on Jetson-NX. For a smaller resolution of  $320 \times 240$  (popular in UAV), our method runs at 2000FPS for single scale detection and at 800FPS for detection while 250FPS for detection-and-extraction, even for eight scales which is huge. It fulfills our goal of using the computing resources for a short duration which is a key requirement of the modern robotic autonomy solutions having many sub-systems [1].

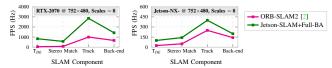


Fig. 16: Frame-rates of SLAM components on EuRoC [21]. Notice that effect of frontend efficiency also transfers to the backend.



Fig. 17: SLAM frame-rate and ATE on KITTI [20]. 'T.F.': tracking failure. Jetson-SLAM uses [2] backend. ATE is same for all GPUs due to identical settings.

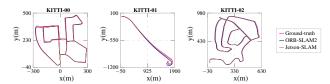


Fig. 18: **Zoom in.** Trajectory output on KITTI sequences.

## F. SLAM-Focused Evaluation of Jetson-SLAM (Sec. III)

We evaluate Jetson-SLAM with two backends: Full-BA [2], ICE-BA [9], and three datasets: KITTI [20], EuRoC [21], KAIST-VIO [3]. KITTI dataset is collected via a self-driving test bed, while EuRoC and KAIST-VIO are via a UAV indoors. These datasets have events of saturation [20], severely low lighting and low texture [21], and rapid rotations [3], and thus sufficient to rigorously analyze a frontend and a SLAM system. Following [2], we report RMSE Absolute Trajectory Error (ATE) and frame rate.

- a) Throughput Analysis of SLAM Components: Fig 16 shows that Jetson-SLAM with Full-BA backend is 85% faster on RTX-2070 GPU, whereas 65% faster on Jetson-NX @752 × 480 resolution over eight scales at scale-factor of 1.2, showing major achievement of this work. Interestingly, the backend still uses CPU and has not been altered to run faster, nonetheless, PyCA improves the backend's speed, justifying that PyCA produces robust features. The overall frame rate is bottlenecked by the back end, especially on Jetson-NX, which opens up future possibilities to speed up the backend.
- b) Evaluation on KITTI [20]: Fig. 17 shows that Jetson-SLAM performs better in seq. KITTI-00, and have a marginally higher error in other sequences that can be traded for speed, and occurs due to the reduced number of features. We also test [8] with Full-BA backend, but observe tracking failures (see video) due to its incapability to produce sufficient map points. Overall, Jetson-SLAM achieves real-time performance with an average speed-up of 80% on RTX-2070 and 67% on Jetson-NX at eight scales and high resolution @1242 × 375. Fig. 18 compares trajectories, indicating that Jetson-SLAM remains close to the ground truth while being faster.
- c) Evaluation on EuRoC [21]: Fig. 20 compares Jetson-SLAM with [8] which faces tracking failures (T.F.) in some sequences [8]. Jetson-SLAM does not fail in any sequence while runs 50% faster with marginal error increment in MH01.

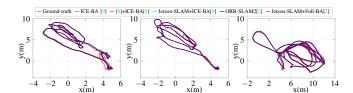


Fig. 19: Zoom in. EuRoC Trajectories for various SLAM pipelines.



Fig. 20: Frame rate and ATE on EuRoC, with Full-BA [2] backend.

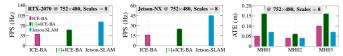


Fig. 21: Frame rate and ATE on EuRoC, with ICE-BA [9] backend.

Table IV. SLAM Performance on EuRoC against VO/VIO/SLAM pipelines. Top-3 approaches are highlighted in bold face.

Approach		RMSE ATE (m)	
Approach	MH01	MH02	MH03
VINS-Stereo [22]	0.54m	0.46m	0.33m
MSCKF-Stereo [23]	0.42m	0.45m	0.23m
<ul> <li>VINS-Stereo + IMU [22]</li> </ul>	0.24m	0.18m	0.23m
• ICE-BA [9]	0.05m	0.04m	0.10m
• [8]+ICE-BA [9]	0.16m	0.06m	0.16m
SVO-GTSAM [24]	0.05m	$0.03 {\rm m}$	0.12m
KIMERA-VIO-FULL [25]	0.04m	0.07m	0.12m
KIMERA-RPGO [25]	0.08m	0.09m	0.11m
<ul> <li>Jetson-SLAM+ IMU + ICE-BA [9]</li> </ul>	0.07m	0.04m	0.07m
<ul> <li>Jetson-SLAM+ Full-BA [2]</li> </ul>	0.04m	0.03 m	0.03m

Table V. SLAM evaluation on KAIST-VIO [3] on Jetson-NX. Baselines results are borrowed from [3].

	KAIST-VIO Sequence										
Approach	circle		infinite		square		rotation				
	normal	fast	head	normal	fast	head	normal	fast	head	normal	head
VINS-Fusion [22]	0.06m	0.12m	0.08m	0.05m	0.09m	0.12m	0.17m	0.07m	0.19m	0.11m	0.28m
<ul> <li>MSCKF-Stereo [23]</li> </ul>	0.12m	0.19m	0.21m	0.32m	0.17m	0.60m	0.10m	0.30m	0.30m	0.10m	0.29m
<ul> <li>KIMERA-VIO [25]</li> </ul>	0.12m	0.07m	0.28m	0.05m	0.14m	1.08m	0.17m	0.19m	1.57m	0.17m	0.74m
• VINS-Fusion + IMU [22]	0.11m	0.10m	0.13m	0.08m	0.08m	0.12m	0.21m	0.13m	0.20m	0.16m	0.10m
• VINS-Fusion + GPU [22]	0.09m	0.13m	0.11m	0.09m	0.05m	0.14m	0.12m	0.11m	0.15m	0.12m	0.11m
ORB-SLAM2 [2]	0.09m	0.11m	0.13m	0.08m	0.10m	0.12m	0.09m	0.09m	0.16m	0.17m	0.21m
<ul> <li>Jetson-SLAM</li> </ul>	$0.014 \mathrm{m}$	$0.017 \mathrm{m}$	0.12m	0.017m	0.016m	0.09m	0.016m	0.017m	$0.04 \mathrm{m}$	0.07m	0.09m

We also show qualitative analysis of the trajectories in Fig. 19, indicating a high overlap of Jetson-SLAM trajectories with ground truth, regardless of the backend.

- d) ICE-BA backend on EuRoC: Jetson-SLAM achieves lowest error (Fig. 21) reflecting that PyCA produces high quality features which turns Jetson-SLAM faster and accurate.
- e) Comparison with existing VO/VIO/SLAM pipelines on EuRoC: Table IV shows that Jetson-SLAM achieves the lowest error on all sequences with [2] backend while remaining among Top-3 approaches with [9] backend.
- f) Evaluation on KAIST-VIO: Heading movement is the most challenging aspect of this dataset, which makes it difficult for SLAM systems to handle without using an Inertial-Measurement-Unit (IMU). Table V shows analysis on all the 11 sequences. Noticeably, Jetson-SLAM achieves the lowest error except circle\_head, where it remains among Top-3 with a marginal ATE difference. Jetson-SLAM even without using an IMU, outperforms several baselines relying on IMU. The primary reason is the capability of Jetson-SLAM to process every frame at high speed. Fig. 22 shows trajectory analysis, indicating that Jetson-SLAM closely overlaps with ground truth.

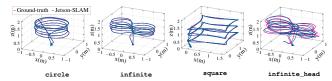


Fig. 22: Zoom in. Trajectories of Jetson-SLAM on KAIST-VIO [3].

Table VI. Effect of bounded rectification on SLAM performance.

Approach	Dataset	Sequence	RMSE ATE (m)  Bounded Rectification		
••		•	Х	/	
Jetson-SLAM +ICE-BA [9]	EuRoC [21]	MH01 MH02 MH03	0.11m 0.07m 0.16m	0.07n 0.04n 0.07n	
Jetson-SLAM +FULL-BA [2]	KAIST-VIO [3]	infinite_fast	0.10m	0.09n	

Table VII. Effect of PyCA cell-size on SLAM for challenging infinite\_head sequence of KAIST-VIO. GPU: RTX-2070.

Cell Size	Average Feature Count	RMSE ATE (m)	FPS (Hz)
15 × 15	1800	0.20m	52
$20 \times 20$	1385	0.13m	62
25 × 25	925	0.10m	90
32 × 32	600	0.09m	200

Table VIII. Effect of number of scales on SLAM for seq. infinite\_head [3]. GPU: RTX-2070. 'X': Tracking Failure.

Number of scales	Cell Size	Average Feature Count	RMSE ATE (m)	FPS (Hz)
2	32 × 32	×	Х	Х
2	10 × 10	1200	0.09m	66
4	32 × 32	×	×	X
4	20 × 20	800	0.13m	125
6	32 × 32	700	0.11m	166
8	32 × 32	600	0.09m	200

## G. SLAM-Focused Ablation Study of Jetson-SLAM

- a) Bounded Rectification: Table VI shows the analysis, indicating that bounded rectification significantly lowers the SLAM error when used in the FAST detection process.
- b) PyCA Cell-Size: Table VII shows that smaller cell size at the native-scale leads to a large number of features because smaller cells become even smaller at lower resolutions in the multiscale setting. It impacts the middle-end and backend performance due to stereo and feature matching ambiguities, as evident from  $15 \times 15$  cell having the highest feature count and the highest error. On the contrary, the cell-size  $32 \times 32$  results in  $8 \times 8$  cells at the  $8^{th}$  scale with a scale factor of 1.2 but have the lowest feature count, lowest ATE, and lowest runtime. This demonstrates the capability of our PyCA based frontend to yield fewer but high-quality features.
- c) Effect of Number of Scales: Table VIII shows that too few scales (i.e. 2) at large cell-size results in insufficient points which leads to SLAM failure. Smaller cell size helps but increases the number of features and hence the runtime. On the contrary, more scales with larger cell-size results in sufficiently fewer key points, lower ATE, and a high frame-processing rate of Jetson-SLAM.

# H. Jetson-SLAM Resource Utilization Analysis

Jetson-SLAM utilizes roughly 15% CPU, 40% GPU, and 10% RAM on Jetson-NX, which is quite low, owing to PyCA, MLPT, TEWA, and data-sharing. It allows other sub-systems to utilize GPU, such as deep networks, discussed next.

#### I. Jetson-SLAM Co-Existing with Deep Neural Networks

Modern autonomy requires deep networks to be coexisting with other sub-systems. We show that Jetson-

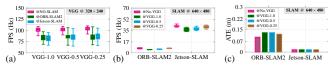


Fig. 23: Effect of SLAM on the FPS of a deep network VGG [12] on Jetson-NX, stereo-mode, eight scales @640 × 480, infinite\_fast sequence [3], and (b)-(c) Effect of VGG onto the FPS and accuracy of ORB-SLAM2 [2] vs Jetson-SLAM.

SLAM marginally affects the runtime of the neural network without sacrificing its own runtime and accuracy, a representative achievement of our work.

To verify that, we choose VGG [12] deep network, popular in robotics applications, and construct its three variants: VGG-1.0, VGG-0.5, and VGG-0.25. The variant VGG-1.0 has five stages with {2,2,2,4,4} layers, and {32,64,128,256,256} channels, while the others are scaled w.r.t the 1.0 variant.

Fig. 23a depicts the effect of ORB-SLAM2 and Jetson-SLAM onto the frame rate of VGG. Despite using GPU, Jetson-SLAM incurs a frame-rate drop similar to the CPU only ORB-SLAM2, indicating GPU efficiency of Jetson-SLAM. This experiment was conducted at a resolution of  $640 \times 480$  which is quite high, thus for smaller resolutions, Jetson-SLAM will result in negligible frame-rate drop.

Fig. 23b, 23c shows how VGG affects SLAM performance. Interestingly, ORB-SLAM2 faces a drop in frame rate which is already running below 10FPS and faces higher ATE error due to its failure to process the frames in time. On the contrary, Jetson-SLAM does not face a drop in ATE but has a minimal drop in frame rate that is still well-above real-time (30FPS).

This experiment shows the utility of Jetson-SLAM to develop complex UAV autonomy solutions. We use Jetson-SLAM in a UAV  $@432 \times 240$  with VGG-1.0, and do not observe FPS drop in VGG or Jetson-SLAM.

# V. CONCLUSION & FUTURE WORK

We present a resource-efficient and accurate GPUaccelerated Jetson-SLAM for low-powered computing devices. We proposed Bounded Rectification to prevent non-corners from being classified as corners in the FAST corner detection process, and Pyramidal Culling and Aggregation PyCA which yields high-quality features at very high speeds in multiscale and stereo setting. PyCA is based on our Feature Culling (FC), Pyramidal Feature Aggregation (PFA), Multi-Location Per-Thread (MLPT) culling, and Thread Efficient Warp Allocation (TEWA) techniques. We also design a concept of Middle-end in visual SLAM and develop a Jetson-SLAM library that utilizes synchronized shared memory to achieve resource efficiency. Jetson-SLAM exhibits a very high frameprocessing rate, suitable for modern robotic autonomy systems comprising several sub-systems. Jetson-SLAM outperforms many prominent SLAM pipelines by a large margin even in multi-scale and stereo settings on Jetson devices.

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