VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY NAGPUR

GRADE CARD

Name : DARAVATH SRIDHAR

Enrolment No.: 13MT05MVD004

Branch : V L S I DESIGN Degree : MASTER OF TECHNOLOGY

Course Title Cr Gr Course Title Cr Gr

AUTUMN 2013

SCDA	Credit EGP	SGPA	CGPA	Credit	EGP	CG	PA
PHL401	VLSI TECHNOLOGY	(DC)				6	FF
ECP510	VLSI TECHNOLOGY	LAB (DE)				2	ВВ
ECP433	DIGITAL IC DESIGN	LAB (DC)				2	ВВ
ECP432	ADV. VLSI DESIGN	LAB (DC)				2	вс
ECP431	DEVICE SIMULATIO	N LAB (DC)			2	ВВ
ECL433	DIGITAL IC DESIGN	(DC)				6	CD
ECL432	ADV. VLSI DESIGN	(DC)				6	FF
ECL431	PHYSICAL ELECTR	ONICS (DC	:)			6	CD

	SGPA Credit			EGF	•	SGPA CGPA			(Credit		EGP	CG	PA		
30	J. 7	· [32.0	0	122.0	00	3.81		C	JFA	2	20.00	1	22.00	6.	10
DE	2	DC	18	НМ		О	С	Ţ	DE	2	DC	18	НМ		ос	
ΑU		ES		BS			tal 20		ΑU		ES		BS		Total	20

RE-EXAM AUTUMN 2013

PHL401 VLSITECHNOLOGY (DC) 6 CD CGPA FGP FGP Credit SGPA Credit **SGPA CGPA** 6.00 30.00 5.00 26.00 152.00 5.85 DE -- DC 6 HM DE 2 DC 24 HM AU -- ES -- BS -- Total 6 Total 26 ΑU -- ES -- BS

Note: This grade card is exclusively for internal use

Abbreviations: Cr - Credits, Gr - Grade, Au - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points, SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - Repeat the Course (This Statement is subject to correction, if any)

Date: 09-April-2014 Asst. Registrar, Examination Cell

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VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY NAGPUR

GRADE CARD

Name : PALLE RAVIKUMAR

Enrolment No.: 12MT05MVD012

Branch : V L S I DESIGN Degree : MASTER OF TECHNOLOGY

Course Title Cr Gr Course Title Cr Gr

AUTUMN 2012

ECL431 PHYSICAL ELECTRONICS (DC) 6	6	FF
ECL432 ADV. VLSI DESIGN (DC) 6	6	CC
ECL433 DIGITAL IC DESIGN (DC) 6	6	FF
ECP431 DEVICE SIMULATION LAB (DC) 2	2	AB
ECP432 ADV. VLSI DESIGN LAB (DC)	2	AB
ECP433 DIGITAL IC DESIGN LAB (DC) 2	2	вв
ECP510 VLSI TECHNOLOGY LAB (DE) 2	2	AB
PHL401 VLSI TECHNOLOGY (DC) 6	6	FF

	JF C	טוכ	V L	SIIL	אוו וכ	OLOG) I L/	יט (טר	.)						_	AD
Pŀ	HL4	101	VL:	SITE	CHN	OLOG	Y (DC)							6	FF
	90	ìΡΑ		Credi	it	EGP	'	SGPA	CC	€PA	(Credit		EGP	CG	PA
	30) F A	\	32.00	0	106.0	00	3.31) F A	1	14.00	1	06.00	7.	57
DE	Ē	2	DC	12	нм		ОС		DE	2	DC	12	НМ		ОС	
Αl	J		ES		BS		Tota	al 14	ΑU		ES		BS		Total	14

SPRING 2013

_			
ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	6	FF
ECL435	MEMS (DE)	6	CD
ECL503	ANALOG IC DESIGN (DC)	6	FF
ECL504	VLSI SYSTEM DESIGN (DC)	6	FF
ECL505	NANO ELECTRONICS (DC)	6	CC
ECP435	MEMS (DE)	2	BB
ECP503	ANALOG IC DESIGN (DC)	2	BB
ECP505	NANO ELECTRONICS (DE)	2	ВВ
·			

ECP5	05	NA	NO EL	_ECT	ron	ICS	(DE)							2	BB
SG	DΛ		Credi	- 1	EGP		SGPA	~	ЭPА		Credit		EGP	С	GPA
36	ГА		36.0	0	114.0	0	3.17	C	JPA		38.00	2	44.00	(6.42
DE 1	0	DC	8	НМ		ос		DE	12	DC	26	НМ		ОС	
AU -	-	ES		BS		Total	18	ΑU		ES	·	BS		Tota	J 38

RE-EXAM AUTUMN 2012

ECL431	PHYSICAL ELECTRONICS	(DC)	6	FF
ECL433	DIGITAL IC DESIGN (DC)		6	DD
PHL401	VLSI TECHNOLOGY (DC)		6	FF
·				

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0	GP/		Cred	lit	EGP	,	SGPA	~	3PA	Crec	lit	EGP	C	GPA
	_		18.0	- :	24.0	0	1.33	- 01	א וכ	20.0	-	130.00		.50
DE		DC	6	HM		oc		DE	2	DC 18	3	MH	ОС	-
ΑU		ES		BS		Tota		ΑU		ES		BS	Total	20

RE-EXAM SPRING 2013

ECL409 RADIO FREQUENCY CIRCUIT DESI	IGN (DE) 6	DD
ECL503 ANALOG IC DESIGN (DC)	6	FF
ECL504 VLSI SYSTEM DESIGN (DC)	6	FF

SCD4	Credit	EGP	SGPA	CCDA	Credit	EGP	CGPA
SGFA	18.00	24.00	1.33	CGFA	44.00	268.00	6.09
DE 6 D	C H	M	oc	DE 18	DC 26	HM	oc
AU E	S B	s - 1	Total 6	AU	ES	BS 1	Total 44

AUTUMN 2013

ECD501	PROJE	CT P	HASE - I	(DC)				6	CC
ECL412	ADVAN	NCED	DIGITAL	SIGNAL F	PROCESSIN	IG (DE)		6	FF
ECL431	PHYSI	CAL E	ELECTRO	ONICS (D	C)			6	DD
ECL507	DESIG	N FO	R TESTA	BILITY YII	ELD AND RE	ELIABILIT	Υ	6	CD
	(DE)								
PHL401	VLSI T	ECH	NOLOGY	(DC)				6	FF
SGPA	Cr	edit	EGP	SGPA	CGPA	Credit	EGP	CG	PA
SGPA	30	.00	90.00	3.00	CGPA	62.00	358.00	5.	77

SGPA	Credit	EGP	SGPA	CCDA	Credit	EGP	CGPA	
301 A	30.00	90.00	3.00	COLA	62.00	358.00	5.77	
DE 6 DC	12 HI	и с	С	DE 24	DC 38	HM	oc	
AU ES	S B	S To	tal 18	AU	ES	BS	Total 62	

RE-EXAM AUTUMN 2013

FIL4UI	VLSI IECHI	NOLOG I	(DC)				O	ГГ
CCDA	Credit	EGP	SGPA	CCDA	Credit	EGP	CGP	Α
SGFA	6.00	0.00	0.00	CGPA	44.00	268.00	6.09	9

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