Ashish Tibrewal

ashishtibrewal.github.io | ashishtibrewal | in tibrewalashish 💌 ashish.tibrewal@hotmail.com | 📞 +44 (0) 7841 367262 | 🗥 125a Fountainbridge, Edinburgh

Education

University of Edinburgh

Edinburgh, UK

MSc Artificial Intelligence | Distinction (On track) | Expected August 2019

Sep 2018 - Present

Relevant Courses: Machine Learning & Pattern Recognition, Machine Learning Practical, Extreme Computing, Probabilistic Modelling and Reasoning, Reinforcement Learning, Foundations of Natural Language Processing, Algorithmic Game Theory

University of Bristol Bristol, UK 2012 - 2016

MEng Computer Science & Electronics | Upper Second Class Honours (2.1)

Assam, India

The Assam Valley School Indian School Certificate (Equivalent to U.K. A Levels) | Grade: 91%

2010 - 2012

Indian Certificate of Secondary Education (Equivalent to U.K. GCSE) | Grade: 89%

2004 - 2010

Experience

HCL Technologies

New Delhi, India

Software Engineer | Business Services AI/ML Team

- Oct 2017 Jul 2018
- Researched and developed AI/ML products (PoCs and MVPs) for several HCL clients
- Designed signature localisation and offline verification system using deep learning techniques (YOLO and SigNet) for cheque, invoice, and form processing with an accuracy of 70%
- Researched techniques for one-shot learning and forgery detection using Siamese Networks
- Involved in the entire data pipeline including data sourcing/collection, generation, annotation, cleaning, preprocessing, and iterative training, tuning, and model evaluation, data augmentation for training YOLO, and documentation

Renishaw plc

Wotton-under-Edge, UK

Jan 2015 - Sep 2015

- Research Engineer Intern | Group Engineering Team
 - Researched and built a prototype of a passive non-contact 3D scanner
 - Designed, developed, and analysed a serial communication and PI control system, including low-level design synthesis
 - Designed an application to interface with the scanning system along with APIs to support EmguCV & MS SQL Server
 - Optimised performance by implementing a multi-threaded design, and integrated support for MATLAB and GNU Octave to run image processing scripts using OLE Automation
 - Drafted comprehensive documentation covering design and implementation details including software documentation. Involved in frequent code/design reviews and presentations with senior members of the team

Hidden Brains InfoTech

Ahmedabad, India

Software Analyst Intern | Planning, Analysis & Prototyping Team

Aug 2014 - Sep 2014

- Analysed client proposal documents, drafted software requirements specification (SRS), scope of work, business requirement, and database documents
- Designed UML models for several website and mobile application development projects
- Improved existing SRS documents to minimise development required reducing delivery time and costs

Skills

Programming (Proficient): C, C#, Java, Python, MATLAB, Bash, LATEX

Programming (Working knowledge): C++, SQL, HTML, CSS, JavaScript, Haskell, PHP, VHDL, Verilog

Machine Learning: Probabilistic Modelling, Data Visualisation, Hadoop (MapReduce), Deep Learning (CNN, RNN) Software Engineering: Agile (Scrum & Kanban), DevOps, TDD, CI/CD, Git, UML, Web & Mobile Development **Languages:** English (Fluent), Hindi (Fluent), Assamese (Native)

Interests: Coding, Reading, Photography, Sports (Lawn Tennis, Squash & Football), Cooking/Baking, Travelling

Awards & Achievements

Selected for 3 Day Startup (3DS), Edinburgh	2018
Selected for Facebook Analytics Academy, London	2018
Dean's International Intern Scholarship Scheme (Nomination & honourable mention)	2014
Barry Thomas Scholarship for Computer Science	2012
Lawn Tennis Team Captain at the Assam Valley School	2010 - 2012
Major Colours for Lawn Tennis and Photography, and Minor Colours for Squash	2008 - 2011
Delegate to the International Exchange Programme, Sydney	2010
Represented Assam at the Junior National Squash Championships Aimer	2007