

📧 ashishtibrewal.github.io | 🌐 ashishtibrewal | in tibrewalashish
✉ ashish.tibrewal@hotmail.com | ☎ +44 (0) 7841 367262 | 🏠 125a Fountainbridge, Edinburgh

Education

University of Edinburgh

MSc Artificial Intelligence | Distinction (Expected)

Core Modules: Machine Learning & Pattern Recognition, Machine Learning Practical, Extreme Computing, Probabilistic Modelling and Reasoning, Reinforcement Learning, Foundations of Natural Language Processing

Edinburgh, U.K.

Sep 2018 – Present

University of Bristol

MEng Computer Science & Electronics | Upper Second Class Honours (2:1)

Research Project: Disaggregated Data Centres – Simulation & Analysis of Resource Allocation Algorithms

Bristol, U.K.

2012 – 2016

The Assam Valley School

Indian School Certificate (Equivalent to U.K. A Levels) | Grade: 91%

Indian Certificate of Secondary Education (Equivalent to U.K. GCSE) | Grade: 89%

Assam, India

2010 – 2012

2004 – 2010

Experience

HCL Technologies

Software Engineer | Business Services AI/ML Team

New Delhi, India

Oct 2017 – Jul 2018

- Researched and developed AI/ML products (PoCs and MVPs) for several HCL clients
- Designed signature localisation and offline verification system using deep learning techniques (YOLO and SigNet) for cheque, invoice, and form processing with an accuracy of 70%
- Researched techniques for one-shot learning and forgery detection using Siamese Networks
- Involved in the entire data pipeline including data sourcing/collection, generation, annotation, cleaning, preprocessing, and iterative training, tuning, and model evaluation, data augmentation for training YOLO, and documentation

Renishaw plc

Research Engineer | Group Engineering Team

Wotton-under-Edge, U.K.

Jan 2015 – Sep 2015

- Researched and built a prototype of a passive non-contact 3D scanner
- Designed, developed, and analysed a serial communication and PI control system, including low-level design synthesis
- Designed an application to interface with the scanning system along with APIs to support EmguCV & MS SQL Server
- Optimised performance by implementing a multi-threaded design, and integrated support for MATLAB and GNU Octave to run image processing scripts using OLE Automation
- Drafted comprehensive documentation covering design and implementation details including software documentation. Involved in frequent code/design reviews and presentations with senior members of the team

Hidden Brains InfoTech

Software Analyst Intern | Planning, Analysis & Prototyping Team

Ahmedabad, India

Aug 2014 – Sep 2014

- Analysed client proposal documents, drafted software requirements specification (SRS), scope of work, business requirement, and database documents
- Designed UML models for several website and mobile application development projects
- Improved existing SRS documents to minimise development required reducing delivery time and costs

Skills

Programming: C, C++, C#, Java, Python, MATLAB, SQL, HTML, CSS, JavaScript, Haskell, Bash, \LaTeX

Machine Learning: Predictive Modelling, Data Visualisation, Hadoop (MapReduce), Deep Learning

Software Engineering: Agile (Scrum & Kanban), DevOps, TDD, CI/CD, Git, UML, Web & Mobile Development

Languages: English (Bilingual fluency), Hindi (Bilingual fluency), Assamese (Native fluency)

Interests: Coding, Reading, Photography, Sports (Lawn Tennis, Squash & Football), Cooking/Baking, Travelling

Awards & Achievements

Selected for Facebook Analytics Academy, London

2018

Dean's International Intern Scholarship Scheme (Nomination & honourable mention)

2014

Barry Thomas Scholarship for Computer Science

2012

Lawn Tennis Team Captain at the Assam Valley School

2010 – 2012

Major Colours for Lawn Tennis and Photography, and Minor Colours for Squash

2008 – 2011

Delegate to the International Exchange Programme, Sydney

2010

Represented Assam at the Junior National Squash Championships, Ajmer

2007