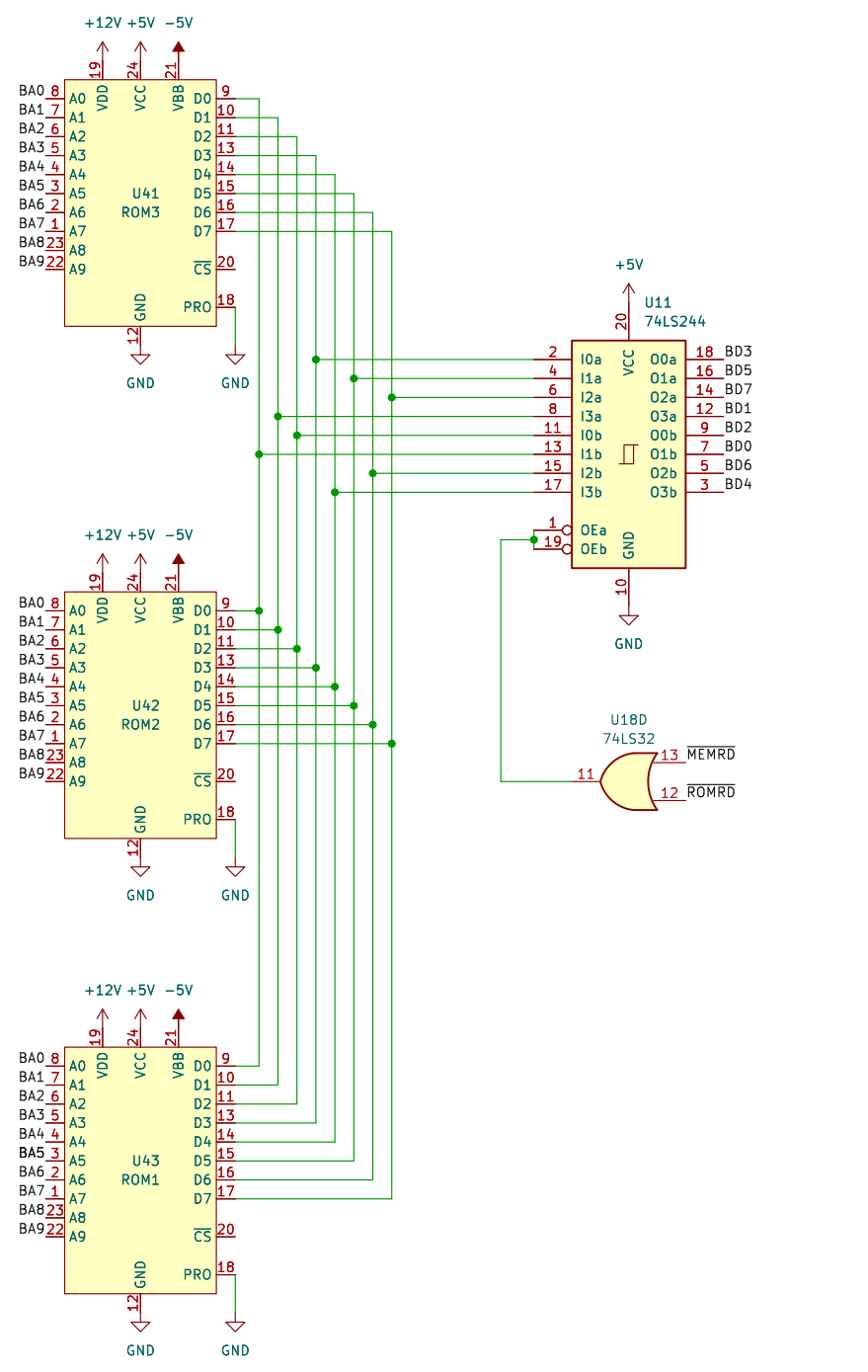
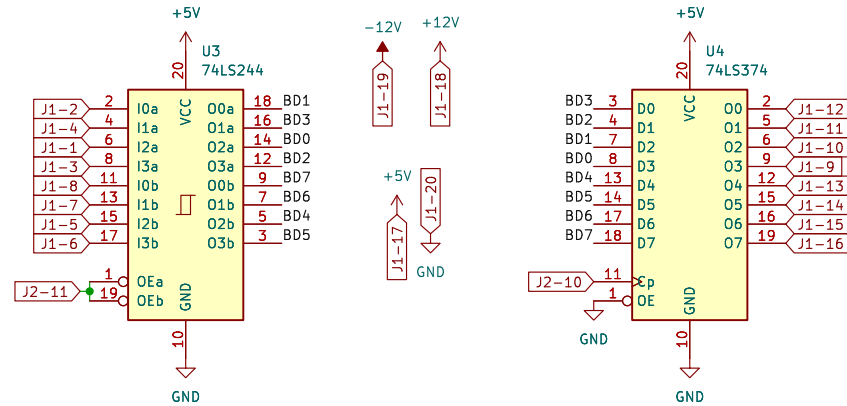


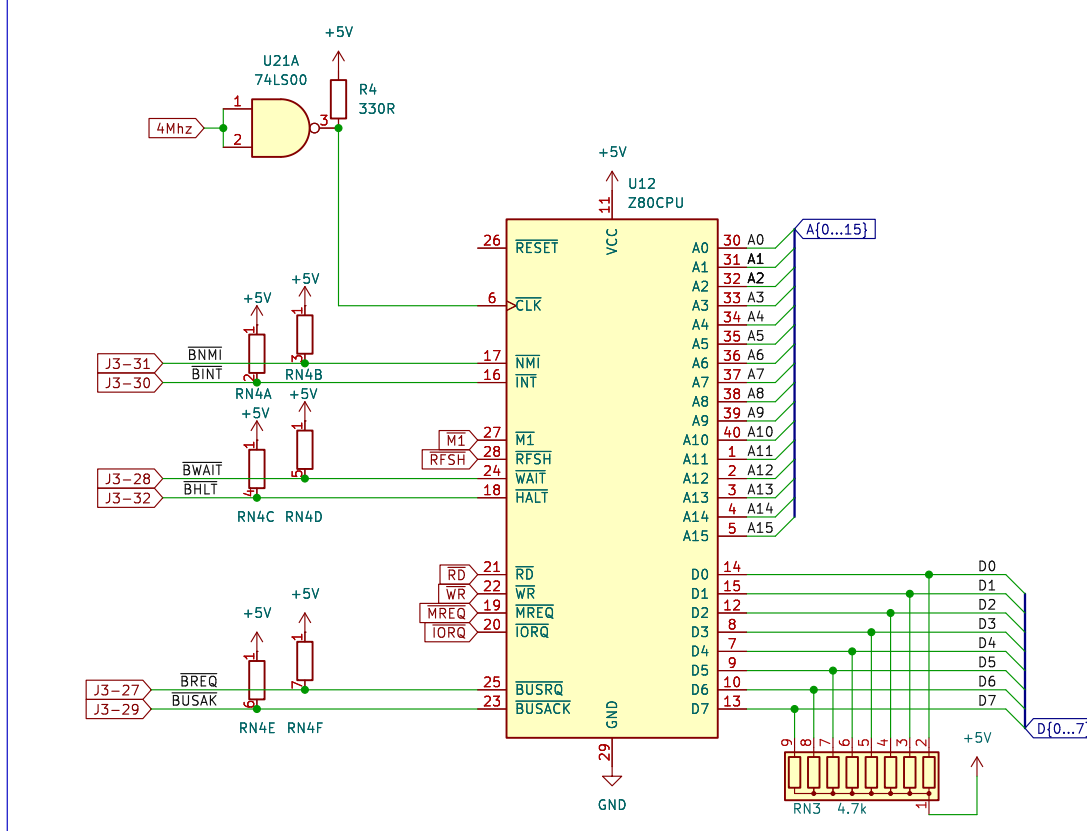
## ROM



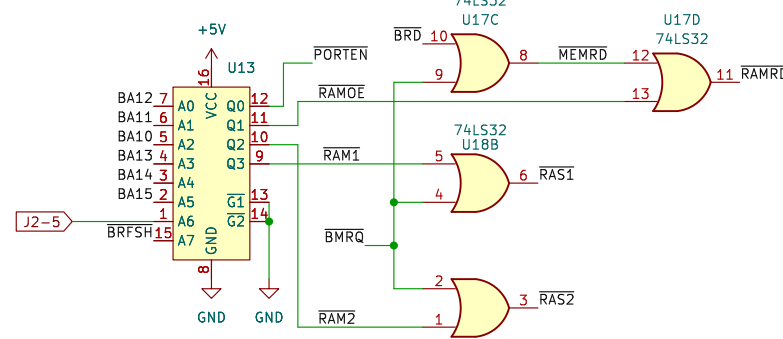
## User Port



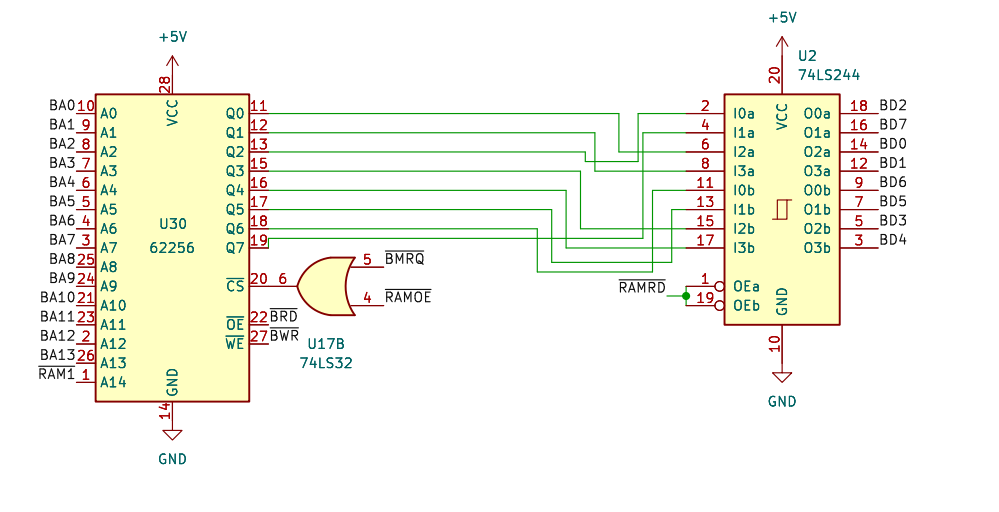
## Z80



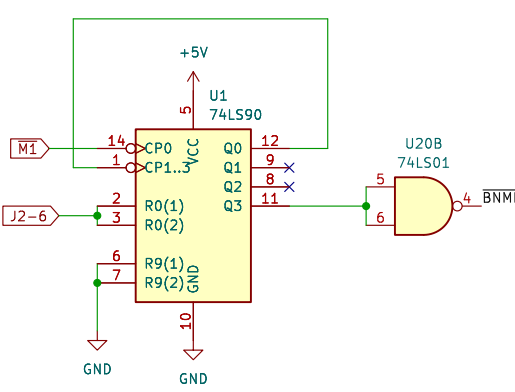
## Memory Map



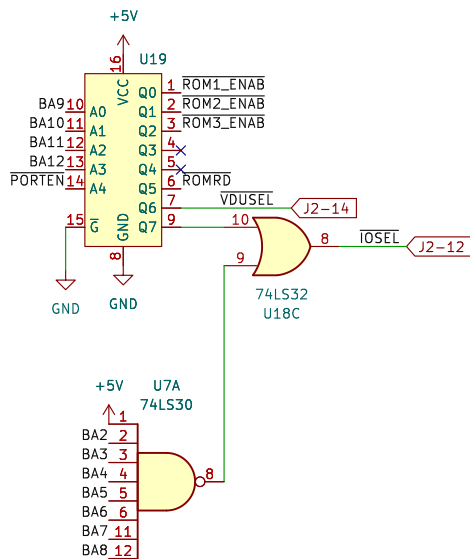
## 32K RAM



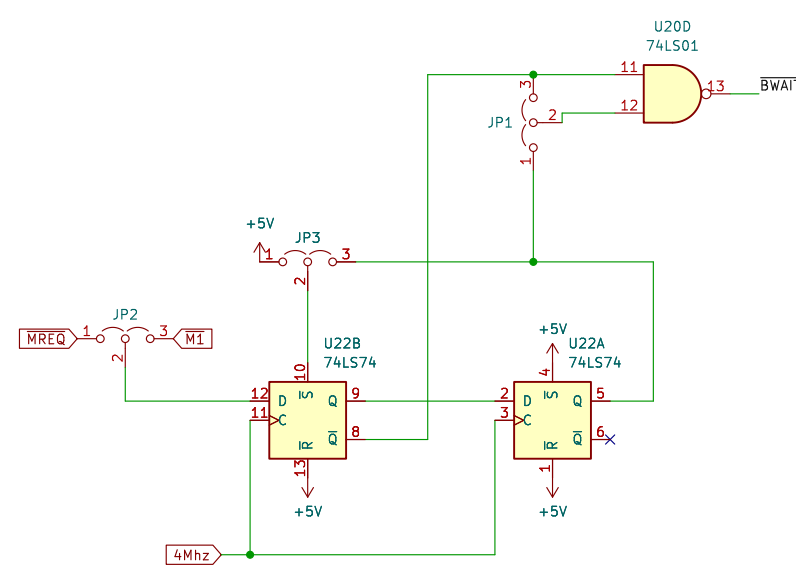
## Interrupt



## Port Map

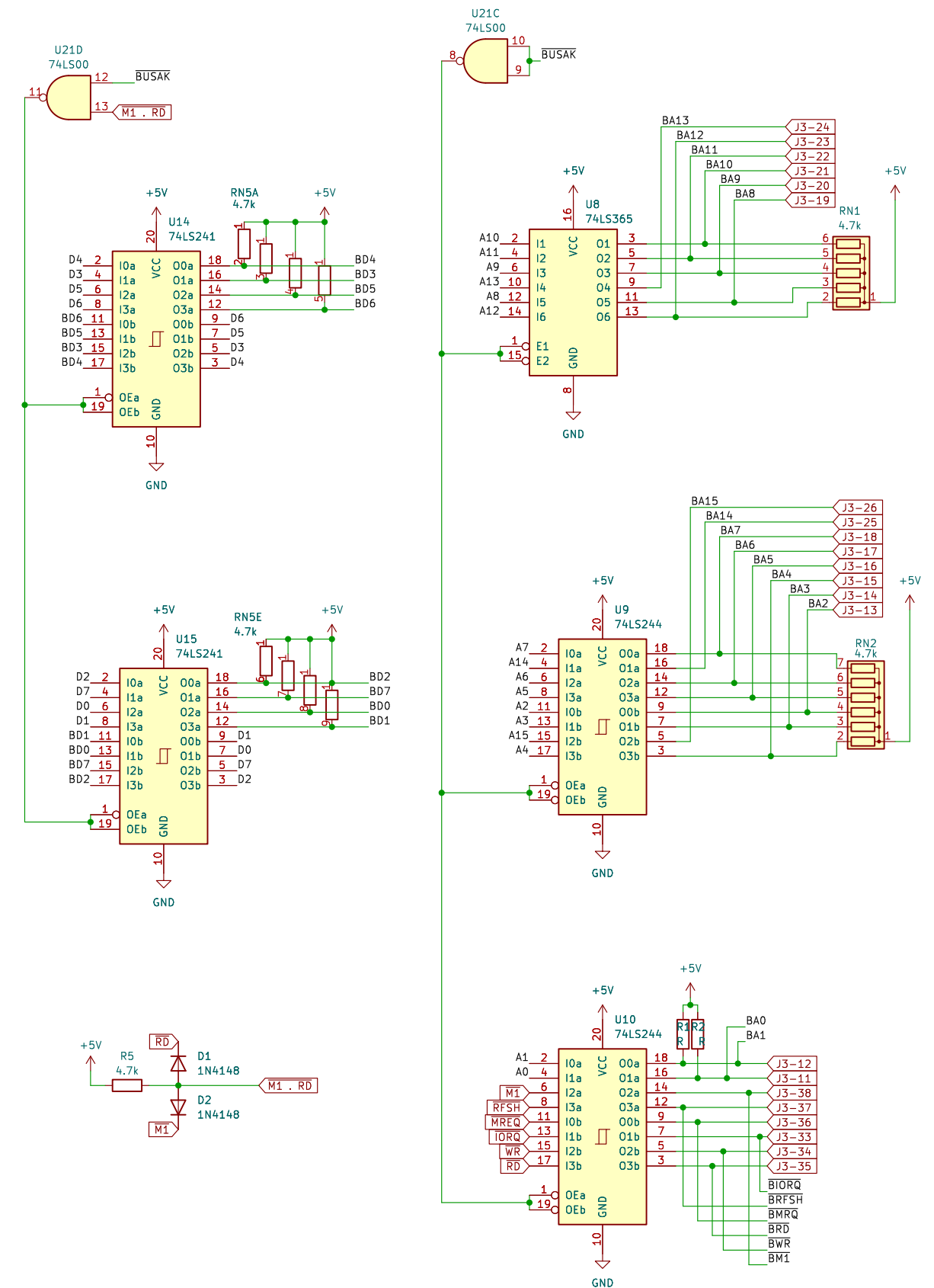


## MEM WAIT



Note:  
Use jumper position 1 & 2 for MRQ  
Use jumper position 2 & 3 for M1

## Bus Buffers



## Clock

