

# PSX Modchip

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Si5351A

ATtiny9

X322527AMB4SI

PB0 - TPI DATA / SDA

PB1 - TPI CLCK / SCL

PB2 - INT $\phi$

PB3 - PAL / DUAL

SO-High + SUBB

SO-High + SUBA

SO-Low + Don't Care

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Si5351A

X322527MMB4SI

VDD	1	8	GND
PAL - PA6	2	7	NC / CLK
MODE - PA7	3	6	RESET / UPDI
PAL - SDA	4	5	SCL

ATTiny402

VDD	1	14	GND
→ SQCK - PA4	2	13	PA3 - LED →
→ SUBQ - PA5	3	12	PA2 - MODE ←
→ GFCK - PA6	4	11	PAL - NTSC ←
← DATA - PA7	5	10	RESET / UPDI ←
← BDI2 - PB3	6	9	SCL →
→ BALB - PB2	7	8	SDA ↔

ATTiny404

~~VDD~~ - ~~VDD~~, ~~CLK~~, ~~VDD~~, ~~LOW~~, ~~PA0~~, ~~ACND~~  
 GPU - ~~CLK~~, ~~SUBQ~~, ~~NTSC~~, ~~VDD~~, ~~GND~~, ~~LED~~  
 ROM - ~~SQCK~~, ~~SUBQ~~, ~~GFCK~~, ~~DATA~~, ~~BDI2~~, ~~BALB~~, ~~LED~~, ~~LED~~  
 PAL - ~~VDD~~, ~~GND~~  
 SIO - ~~VDD~~, ~~TXD~~, ~~RXD~~, ~~GND~~, 4x Handshake

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SBC_P1.2	1	16	P1.1_LED
SBQ_P1.3	2	15	P1.0_NC
GWC_P1.4	3	14	P3.7_SDA
DAT_P1.5	4	13	P3.6_SCL
D02_P5.4	5	12	INT1_SEL
VCC	6	11	INT0_PAL
A18_P5.5	7	10	TXD_USB
GND	8	9	RXD_USB

STC15W204S