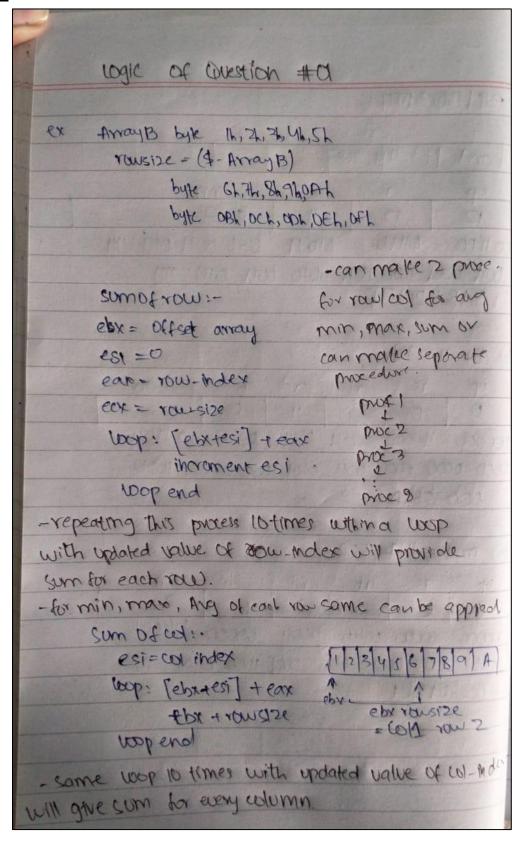
COAL ASSIGNMENT # 04

TASK# 01:

Logic:



Code:

```
TITLE Assignment4 (Test.asm)
Include irvine32.inc
.data
tableB byte 100 dup(?)
RowSize byte 10
RowIndex byte?
ColIndex byte?
counter dword?
sum dword?
TotalSum dword?
sumColumn dword?
maximumRow dword?
maxTable dword 0
maximumColumn dword?
minimumRow dword?
minTable dword 100
minimumCol dword?
averageRow dword?
averageCol dword?
averageTable dword?
message byte "Row# ",0
message2 byte "Sum of Overall Table: ",0
colon byte ": ",0
message3 byte "Col# ",0
message5 byte "Maximum of Overall Table: ",0
message7 byte "Minimum of Overall Table: ",0
message9 byte "The Average of Overall Table: ",0
MessageSumRow byte "THE SUM OF EACH ROW!",0
MessageSumCol byte "THE SUM OF EACH COLUMN!",0
MessageMaxRow byte "THE MAXIUMUM OF EACH ROW!",0
MessageMaxCol byte "THE MAXIUMUM OF EACH COLUMN!",0
MessageMinRow byte "THE MINIMUM OF EACH ROW!",0
MessageMinCol byte "THE MINIMUM OF EACH COLUMN!",0
MessageAvgRow byte "THE AVERAGE OF EACH ROW!",0
MessageAvgCol byte "THE AVERAGE OF EACH COLUMN!",0
prototypes of all the functions
;Note: made different procedures for them for easy understanding
sumRow proto array: ptr byte, rSize: byte, rIndex: byte
sumCol proto array: ptr byte, rSize: byte, cIndex: byte
MaxRow proto array: ptr byte, rSize: byte, rIndex: byte
MaxCol proto array: ptr byte, rSize: byte, cIndex: byte
```

```
MinRow proto array: ptr byte, rSize: byte, rIndex: byte
MinCol proto array: ptr byte, rSize: byte, cIndex: byte
AvgRow proto array: ptr byte, rSize: byte, rIndex: byte
AvgCol proto array: ptr byte, rSize: byte, cIndex: byte
.code
main PROC
; initializing array from 1-100
mov esi, offset tableB
mov eax, 1
mov ecx, 100
11: mov [esi], al
       inc al
        inc esi
loop 11
mov RowIndex, 0
mov ecx, 10
; sum of rows
mov edx, offset MessageSumRow
call WriteString
call crlf
12: mov counter, ecx
       mov edx, offset message
       call WriteString
        movzx eax, RowIndex
        call WriteDec
        mov edx, offset colon
       call WriteString
        invoke sumRow, addr tableB, RowSize, RowIndex
        mov eax, sum
        add TotalSum, eax
                                       ; calculating the sum of overall table
       inc RowIndex
        mov ecx, counter
loop 12
: sum of columns
call crlf
call crlf
mov edx, offset MessageSumCol
call WriteString
call crlf
mov ecx, 10
mov ColIndex, 0
```

```
13: mov counter, ecx
       mov edx, offset message3
       call WriteString
       movzx eax, ColIndex
       call WriteDec
       mov edx, offset colon
       call WriteString
       invoke sumCol, addr tableB, RowSize, ColIndex
       mov ecx, counter
       inc ColIndex
       call crlf
loop 13
; maximum of Rows
call crlf
call crlf
Mov edx, offset MessageMaxRow
call writeString
call crlf
mov RowIndex, 0
mov ecx, 10
14: mov counter, ecx
       mov edx, offset message
       call WriteString
       movzx eax, RowIndex
       call WriteDec
       mov edx, offset colon
       call WriteString
       invoke maxRow, addr tableB, RowSize, RowIndex
       mov eax, maxTable
       cmp maximumRow, eax
       JLE next
       mov eax, maximumRow
       mov maxTable, eax
       next:
               inc RowIndex
               mov ecx, counter
loop 14
; maximum of columns
call crlf
call crlf
mov edx, offset MessageMaxCol
call writeString
call crlf
```

```
mov ecx, 10
mov ColIndex, 0
15: mov counter, ecx
       mov edx, offset message3
       call WriteString
       movzx eax, ColIndex
       call WriteDec
       mov edx, offset colon
       call WriteString
       invoke maxCol, addr tableB, RowSize, ColIndex
       mov ecx, counter
       inc ColIndex
       call crlf
loop 15
;minimum of row
call crlf
call crlf
Mov edx, offset MessageMinRow
call writeString
call crlf
mov RowIndex, 0
mov ecx, 10
16: mov counter, ecx
       mov edx, offset message
       call WriteString
       movzx eax, RowIndex
       call WriteDec
       mov edx, offset colon
       call WriteString
       invoke minRow, addr tableB, RowSize, RowIndex
       mov eax, minTable
       cmp minimumRow, eax
       JGE next3
       mov eax, minimumRow
       mov minTable, eax
                                              ; min of overall table
       next3:
               inc RowIndex
               mov ecx, counter
loop 16
;minimum of columns
call crlf
call crlf
mov edx, offset MessageMinCol
```

```
call writeString
call crlf
mov ecx, 10
mov ColIndex, 0
17: mov counter, ecx
        mov edx, offset message3
       call WriteString
        movzx eax, ColIndex
       call WriteDec
        mov edx, offset colon
       call WriteString
       invoke MinCol, addr tableB, RowSize, ColIndex
       mov ecx, counter
       inc ColIndex
       call crlf
loop 17
; Average of Rows
call crlf
call crlf
mov edx, offset MessageAvgRow
call WriteString
call crlf
mov RowIndex, 0
mov ecx, 10
18: mov counter, ecx
       mov edx, offset message
       call WriteString
       movzx eax, RowIndex
       call WriteDec
        mov edx, offset colon
       call WriteString
       invoke AvgRow, addr tableB, RowSize, RowIndex
       inc RowIndex
        mov ecx, counter
loop 18
; Average of Columns
call crlf
call crlf
mov edx, offset MessageAvgCol
call WriteString
call crlf
mov ecx, 10
```

```
mov ColIndex, 0
19: mov counter, ecx
       mov edx, offset message3
       call WriteString
       movzx eax, ColIndex
       call WriteDec
       mov edx, offset colon
       call WriteString
       invoke AvgCol, addr tableB, RowSize, ColIndex
       mov ecx, counter
       inc ColIndex
loop 19
call crlf
mov edx, offset message2
call WriteString
mov eax, TotalSum
call WriteDec
call crlf
mov edx, offset message9
call WriteString
mov eax, TotalSum
mov edx, 0
mov bx, 100
div bx
mov averageTable, eax
call WriteDec
call crlf
mov edx, offset message7
call WriteString
mov eax, minTable
call WriteDec
call crlf
mov edx, offset message5
call WriteString
```

mov eax, maxTable call WriteDec call crlf

call dumpregs

main ENDP

```
sumRow proc, array: ptr byte, rSize: byte, rIndex: byte
mov ebx, array
movzx ecx, rsize
movzx eax, rIndex
mul ecx
add ebx, eax
mov eax, 0
mov esi, 0
11: movzx edx, byte ptr[ebx+esi]
       add eax, edx
       inc esi
loop 11
mov sum, eax
call WriteDec
call crlf
ret
sumRow endp
sumCol proc, array: ptr byte, rSize: byte, cIndex: byte
mov ebx, array
mov eax, 0
movzx esi, cIndex
movzx edi, rSize
movzx ecx, rsize
11: movzx edx, byte ptr[ebx+esi]
       add eax, edx
        add ebx, edi
loop 11
mov sumColumn, eax
call WriteDec
sumCol endp
MaxRow proc, array: ptr byte, rSize: byte, rIndex: byte
mov ebx, array
movzx ecx, rsize
movzx eax, rIndex
```

```
mul ecx
add ebx, eax
mov eax, 0
mov al, [ebx]
mov esi, 0
11: movzx edx, byte ptr[ebx+esi]
       cmp edx, eax
       JLE next
       mov eax, edx
       next:
               inc esi
loop 11
mov maximumRow, eax
call WriteDec
call crlf
ret
MaxRow endp
MaxCol proc, array: ptr byte, rSize: byte, cIndex: byte
mov ebx, array
mov eax, 0
movzx esi, cIndex
movzx edi, rSize
movzx ecx, rsize
11: movzx edx, byte ptr[ebx+esi]
       cmp edx, eax
       JLE next
       mov eax, edx
       next:
               add ebx, edi
loop 11
mov maximumColumn, eax
call WriteDec
ret
MaxCol endp
MinRow proc, array: ptr byte, rSize: byte, rIndex: byte
mov ebx, array
movzx ecx, rsize
```

```
movzx eax, rIndex
mul ecx
add ebx, eax
mov eax, 0
mov al, [ebx]
mov esi, 0
11: movzx edx, byte ptr[ebx+esi]
       cmp edx, eax
       JGE next
       mov eax, edx
       next:
               inc esi
loop 11
mov minimumRow, eax
call WriteDec
call crlf
ret
MinRow endp
MinCol proc, array: ptr byte, rSize: byte, cIndex: byte
mov ebx, array
mov eax, Offffffh
movzx esi, cIndex
movzx edi, rSize
movzx ecx, rsize
11: movzx edx, byte ptr[ebx+esi]
       cmp edx, eax
       JGE next
       mov eax, edx
       next:
               add ebx, edi
loop 11
mov minimumCol, eax
call WriteDec
ret
MinCol endp
AvgRow proc, array: ptr byte, rSize: byte, rIndex: byte
mov ebx, array
movzx ecx, rsize
movzx eax, rIndex
mul ecx
```

```
add ebx, eax
mov eax, 0
mov esi, 0
11: movzx edx, byte ptr[ebx+esi]
       add eax, edx
       inc esi
loop 11
mov edx, 0
mov bx, 10
div bx
mov averageRow, eax
call WriteDec
call crlf
ret
AvgRow endp
AvgCol proc, array: ptr byte, rSize: byte, cIndex: byte
mov ebx, array
mov eax, 0
movzx esi, cIndex
movzx edi, rSize
movzx ecx, rsize
11: movzx edx, byte ptr[ebx+esi]
       add eax, edx
       add ebx, edi
loop 11
mov edx, 0
mov bx, 10
div bx
mov averageCol, eax
call WriteDec
call crlf
ret
AvgCol endp
END main
```

Output:

```
C:\WINDOWS\system32\cmd.exe
                                                                                                                                  X
THE SUM OF EACH ROW!
Row# 0: 55
Row# 1: 155
Row# 2: 255
Row# 3: 355
Row# 4: 455
Row# 5: 555
Row# 6: 655
Row# 7: 755
Row# 8: 855
Row# 9: 955
THE SUM OF EACH COLUMN!
Col# 0: 460
Col# 1: 470
Col# 2: 480
Col# 3: 490
Col# 4: 500
Col# 5: 510
Col# 6: 520
Col# 7: 530
Col# 8: 540
Col# 9: 550
THE MAXIUMUM OF EACH ROW!
Row# 0: 10
Row# 1: 20
Row# 2: 30
Row# 3: 40
Row# 4: 50
Row# 5: 60
Row# 6: 70
Row# 7: 80
Row# 8: 90
Row# 9: 100
THE MAXIUMUM OF EACH COLUMN!
Col# 0: 91
Col# 1: 92
Col# 2: 93
```

```
C:\WINDOWS\system32\cmd.exe
                                                                                                                                                                                 X
                                                                                                                                                                         THE MAXIUMUM OF EACH COLUMN!
THE MAXIUML
Col# 0: 91
Col# 1: 92
Col# 2: 93
Col# 3: 94
Col# 4: 95
Col# 5: 96
Col# 6: 97
Col# 7: 98
Col# 8: 99
Col# 9: 100
THE MINIMUM OF EACH ROW!
Row# 0: 1
Row# 1: 11
Row# 2: 21
Row# 3: 31
Row# 4: 41
Row# 5: 51
Row# 6: 61
Row# 7: 71
Row# 8: 81
Row# 9: 91
THE MINIMUM OF EACH COLUMN!
Col# 0: 1
Col# 1: 2
Col# 2: 3
Col# 3: 4
Col# 4: 5
Col# 5: 6
Col# 6: 7
Col# 7: 8
Col# 8: 9
Col# 9: 10
THE AVERAGE OF EACH ROW!
Row# 0: 5
Row# 1: 15
Row# 2: 25
```

```
COUNT OF THE AVERAGE OF EACH ROW!

ROW# 0: 5

ROW# 1: 15

ROW# 2: 25

ROW# 3: 35

ROW# 3: 35

ROW# 3: 55

ROW# 3: 55

ROW# 3: 99

THE AVERAGE OF EACH COLUNN!

COL# 9: 95

THE AVERAGE OF EACH COLUNN!

COL# 9: 95

THE AVERAGE OF EACH COLUNN!

COL# 1: 47

COL# 2: 48

COL# 3: 49

COL# 4: 50

COL# 3: 49

COL# 4: 50

COL# 3: 49

COL# 4: 50

COL# 3: 51

COL# 3: 51

COL# 3: 52

COL# 3: 53

COL# 3: 53

COL# 3: 54

COL# 3: 55

Sum of Overall Table : 50

Sum of Overall Table : 10

EAX-000000004 EEX-00270064 ECX-00000000 EDX-003750C1

ESI-000000000 EDI-000000000 EDP-0035750C1

ESI-000000000 EDI-000000000 EDP-00000000 EDP-0035750C1

ESI-000000000 EDI-000000000 EDP-00000000 EDP-00000000 EDP-00000000 EDP-00000000 EDP-000000000 EDP-00000000 EDP-000000000 EDP-00000000 EDP-00000000 EDP-00000000 EDP-000000000 EDP-000000000 EDP-00000000 EDP-000000000 EDP-0000000000 EDP-000000000 EDP-0000000000 EDP-000000000 EDP-000000000 EDP-000000000 EDP-0000000
```

TASK#02:

Machine	Number of General	Architectural	<u>Year</u>	<u>Difference</u>
	Purpose Registers	<u>Style</u>		
Arm64/A64	31 (including stack pointer and zero register)	Register- Register	2011	The Arm64/A64 is a 64 bit architecture that is different than previous architectures before 2004 because: all of its addresses are assumed to be 64 bits, and set of conditional instructions have been reduced down to cover branches. Dedicated zero register is available for most instructions.
AVR32	32 (16 on reduced architecture)	Register- Register	2006	It's an updated version of rev 2 and allows operations on up to 3 operands which was not possible in previous architectures before 2005 it was just up to 1 or 2 operands before RISC was introduced.
Elbrus (native VLIW)	8-64	Register- Register	2014	It's a 64 bit, updated version of Elbrus-4S version that supports the architecture type of Register – Register. Previously the machines and architectures such as RX (2000) consisted memorymemory type which is a more complex architecture of CISC than RISC.
ESI- RISC	8/16/32/72	Register- Register	2009	It's a 16/32 bit RISC architectural type that supports up to general purpose registers. The way it's different from previous machines and architectures is because of its increased general purpose

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				registers, previously there exists 1, 3, 7, 8, 15, 32 general purpose registers initially which reduces the computational and calculation time.
Mico32	32	Register- Register	2006	It's a 32 bit machine architecture that follows register- register architectural type. It's different than previous because before 2004 instruction encoding styles were mostly variable not fixed. Such as MIPS (1981), but Misco32 entails fixed instruction encoding styles
OpenRisc	16 or 32	Register- Register	2010	OpenRisc architecture is designed in such a way that it supports pipelining, can execute multiple instructions in less clock cycle, it saves time exponentially and increases the processor's performance allowing multiple instructions to be executed at the same time without collisions.
RISC-V	32 (including "zero")	Register- Register	2010	It's a 2.2 version of RISC architecture, it supports little endian-ness which differentiates it from the previous architectures in a way that they used to support biendian-ness or big endian-ness which had their limitations, but since RISC was made to make instructions simpler, its little endian-ness adds on to its compatibility.