X86 INSTRUCTION ENCODENG

Ashmal Anis 1912-0305 sector

(1) XOR WAR32, 7979FFOIL 83 00110 110 01FF7979 = 833601FF7979

23 00 001 111 = 2305

(3) ADD DWURD PIR [EBY +EDÎ+70080], EDX 01 10010001 80006700 = 019180000700

9 MULFBX F7 11 100 011 = F7F3

D DIN VAR32 F7 0011 0110 =F736

10 br Dx, 0818 81+02 4—1808 = 66831808

- 7) MOV [FBX +13 146528], 79765612] C7 16 000 111 28 65 1413 12567679 = C7 872865 1413 12567679
- (8) CMP DLIDH 38 11 110 010 -38F2
 - @ PUSH 96876044 = 50 44608790
 - (6) TUC EST (9) -46

MIPS INSTRUCTION ENCODING

(1) SII \$10,\$1,20

00	YS	7+	rd	sh	fun	
6	10	3	2	20	0	1

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00031500

3 nor \$50, \$55,\$t9

op	rs	nt	rd	sh	fun	
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0000 0011	0 10/1 10	000 100	0000 00	1110 OIC	1931	
02 B	98027				#12.3 W	

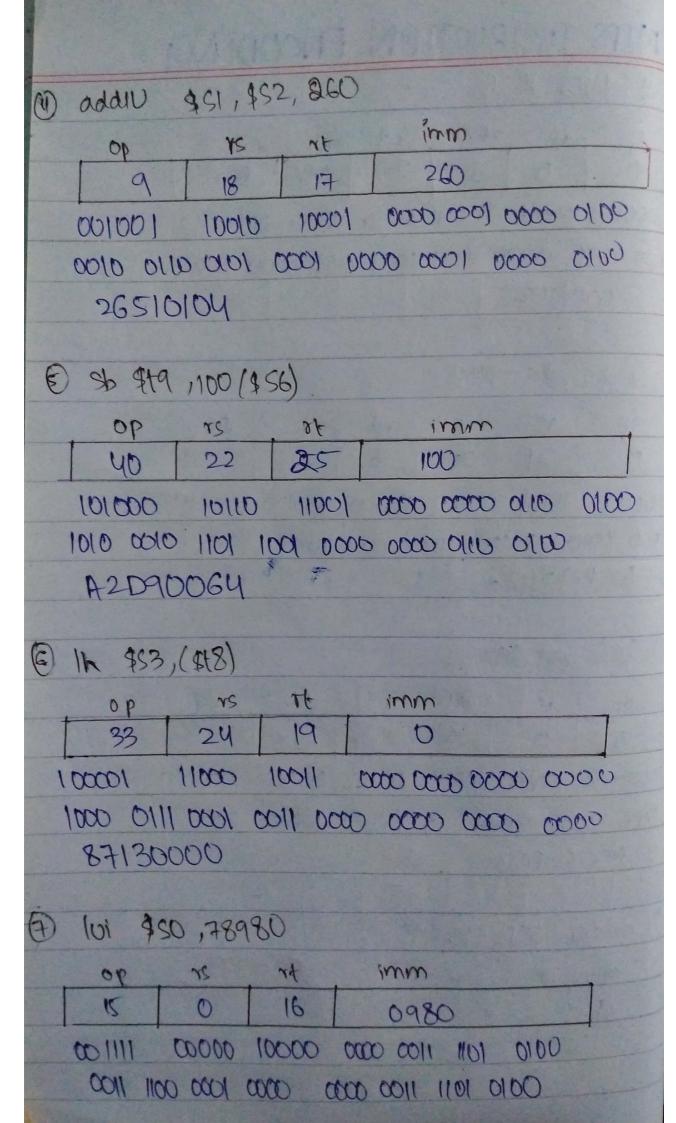
(3) Sta Sal, \$02,12

op	rs	rt	rd	sk	fun
0	0	6	5	12	3

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0000 0000 0000 0110 0010 1011 0000 0011

00062803



3C1003D4

(8) and \$t3, \$t4, 9087

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WILL	100	100 1011	0010	1100	0111	1111	
		2375					Der 1*

6) dn \$t3,\$V1

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THE REAL PROPERTY.	monou	00000	11000	11010	00000	011010
						1 1010
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(6) multu \$12,\$02

Op	75	rt.	rd,	Sh	-fun
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0000	0000 00	000 011	-	1000 01	
	31819			-	410