Ashok S. V. Dibbada

TRAINEE DECISION SCIENTIST · DATA ANALYST · CODING ENTHUSIAST

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Job Experience _____

Mu Sigma Business Solutions Pvt. Ltd.

Bengaluru,India

TRAINEE DECISION SCIENTIST

Sept. 2015 - Present

- Worked in teams across multiple time zones to design and deliver long term solutions for complex business problems
- · Has experience in fields of , Regression & Cluster Analysis, Video Analytics, Image Processing and web-app development

Education _____

Indian Institute of Technology- Guwahati

Assam, India

B.Tech. IN ELECTRONICS AND COMMUNICATION ENGINEERING

Jul. 2011 - Apr. 2015

· Pursued ECE major and completed course work in Information theory, Communication Networks and Signal Processing areas

Bhashyam Public School & Junior College

Andhra Pradesh, India

June 2008 - May 2011

CLASS X, INTERMEDIATE (CLASS XI & XII)

• Studied Maths, Physics & chemistry, scored 95.8% in intermediate (class XI & XII) and secured 91.3% in class X

Skills ____

- Languages: Python, C++, R, SQL, JAVA(J2EE), MATLAB
- Statistical Algorithms: Linear Regression, Logistic Regression, AP & K-Means Clustering, LSA
- Image & Video Analytics: Neural Networks (CUDA), Contour detection & Analysis, OCR, MHT tracker, Image pre processing & enhancing techniques
- Web development: Spring MVC, REST API, Angular JS, D3. is, HTML, Bootstrap
- Tools & Databases: Excel, MS SQL Server

Projects _____

Visualization Portal Mu Sigma

Developer Nov. 2016 - Mar. 2017

- Built a web application for US based home improvement retailer to visualize and monitor price sensitivity model results and simulate
 price sensitivity scores
- Designed middle layer architecture using JAVA Springs and front end with angular.JS
- Created graphical components in d3.js for visualization purposes

Video Analytics Mu Sigma

Data Analyst

June 2016 - Sept. 2016

- Worked with US based home improvement retailer in detecting and tracking of customers from in-store CCTV footage
- Developed an ensemble neural network model for human face detection and MHT for tracking across video frames
- Developed a click tool in python for tagging customer wait and check out time for model validation

Parts Harmonization Mu Sigma

Data Analyst March 2016 - May 2016

- Enabled decisions for a USA based technology conglomerate in procurement optimization. Identified duplicates at part level on the basis of key part features and descriptions for a given part
- Developed a desktop application to extract the important part features from images containing part related information. Identified the correlation between P.O. Cost and various part features
- · Created an excel based reporting framework highlighting the optimised procurement options of specified features for a given part

Academic Work

Software and Hardware implementation of Power Line Communication

IIT-Guwahati

BACHELOR THESIS PROJECT - UNDER DR. KALPANA DHAKA AND DR. AMITABH CHATTERJEE OF EEE DEPT.

July 2014 - Apr. 2015

- · Worked on implementing power line communication as an effective tool box for communication purposes
- For software interface, the idea is to establish a power line channel scenario and transmit the data bytes through OFDM transmission model. The software implementation has been done in MATLAB
- Implemented the hardware interface by establishing communication between two computers connected with arduino micro controllers (transmitter and receiver) through power line channel using PLC modems

Toolbox for drawing leaf outlines

IIT-Guwahati

SUMMER INTERNSHIP - UNDER GUIDANCE OF DR.P.K.BORA, EEE DEPT.

May 2014 - July 2014

- Did literature review about shape descriptors and edge detection of images
- Developed an application in MATLAB which enables user to manually select points on the image to form an outline using spline interpolation
- Performed edge detection on leaf images using various edge detectors

Implementation of Re-configurable printed antenna

IIT-Guwahati

COURSE WORK PROJECT - UNDER GUIDANCE OF DR.K.R.SINGH AND DR.R.K.SONKAR OF EEE DEPT.

Jan. 2014 - Apr. 2014

- · Implemented tunable micro strip antenna as per the input so that it could be used at multiple input frequencies
- Implemented circuit design in CircuitCam and simulated the circuit using HFSS-9
- Implemented hardware interface by printing an antenna on FR4 epoxy with SMA connectors and used varactors to make the antenna tunable to input frequencies

Digital Speedometer IIT-Guwahati

Course work project - under guidance of Dr.Amith Sethi, EEE dept.

Sep. 2012- Nov. 2012

May 2012 - July 2012

• Implemented a digital speedometer using 8085 microprocessor and combinatorial circuits to calculate wheel speed

Awards & Achievements

INDUSTRY

2016	$\textbf{Impact Award}, \ \text{For simplifying the video tagging process and reduced the time taken significantly}$	Mu Sigma
2016	Spot Award , In recognition of improvement shown within few weeks into my first project	Mu Sigma

ACADEMIC

2011-2015 MCM Scholarship, Full fee wavier throughout B.Tech on merit cum means basis	
2009-2011 BTS Scholarship, Full fee wavier for class XI,XII based on performance in state wide talent test	
2011 Top 250 , Listed in top 250 of NSEP & NSEC and selected for InPhO and InChO	Bhashyam
2011 Top 1% , Stood among top 1% in IITJEE-2011,AIEEE-2011	Bhashyam

Relevant Course Work

- Information Theory & Coding
- Error Control Codes
- Communication Networks
- Probability & Random Processes
- Digital Signal Processing

- Algorithms & Data Structures
- Parallel Computing
- Embedded Systems
- Microwave Engineering & Lab
- Digital Communications & Lab

Extracurricular Activity

Technothlon IIT-Guwahati

CITY REPRESENTATIVE

· Successfully conducted Technothlon in my home town and was responsible for a participation of 500 teams

National Service SchemeIIT-GuwahatiMemberSep. 2011 - Apr. 2013

· Visited nearby villages to spread awareness and participated in several cloth collection and cleanliness drives

APRIL 15, 2017 ASHOK S. V. DIBBADA · RÉSUMÉ