PLL

Report (Stage-2)

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1.For input frequency (f_in) = 5MHz

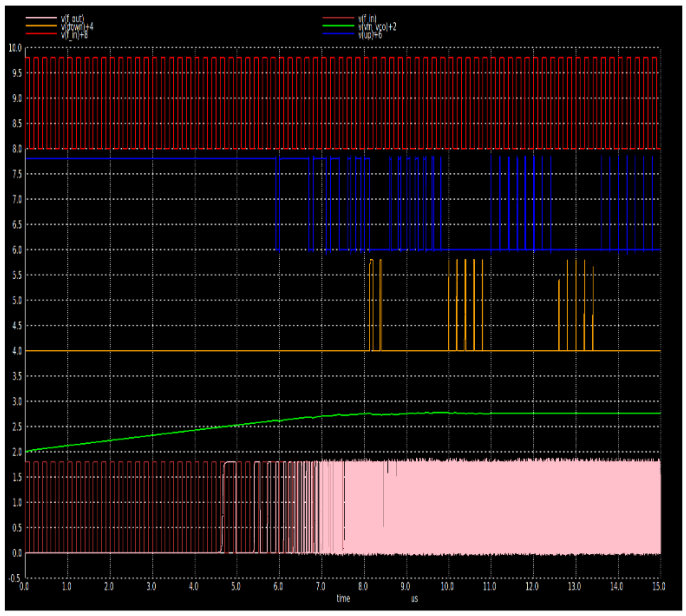

Fig:Waveforms at each node of PLL

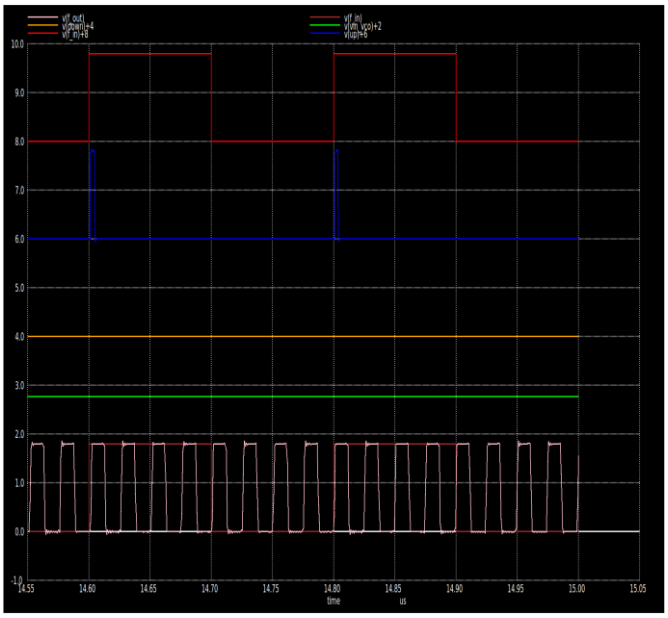

Fig: Waveforms at each node of PLL(magnified)

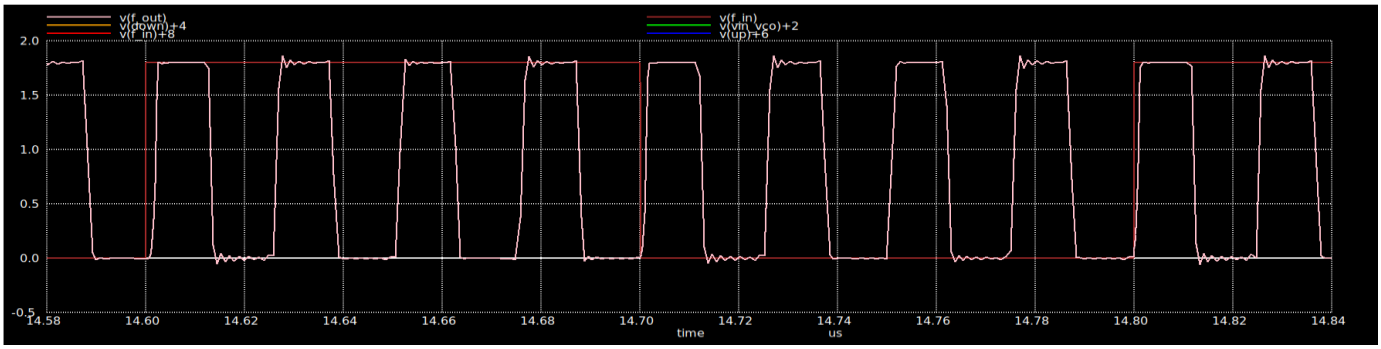

Fig:Waveform showing input and output comparison

Result:

Input Frequency (f_in)	**5MHz**
Output Frequency(f_out)	**40MHz**

2.For input frequency (f_in) = 10MHz

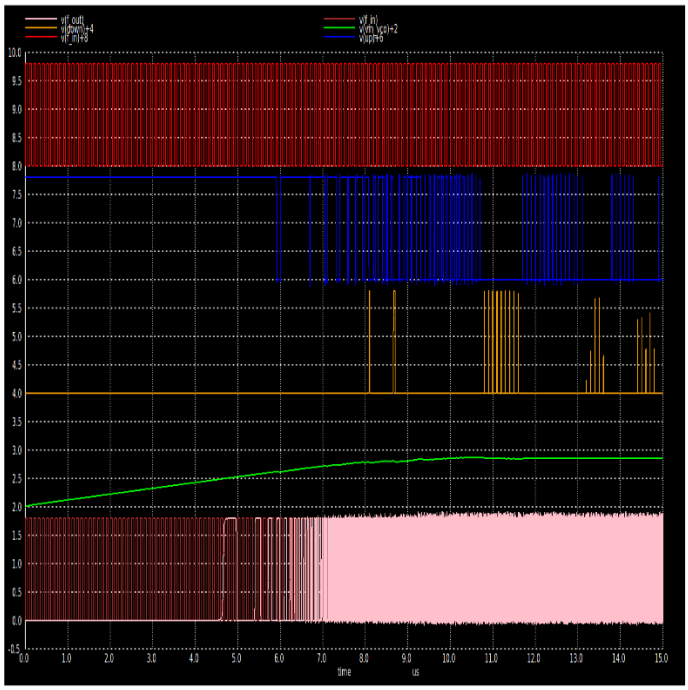

Fig:Waveforms at each node of PLL

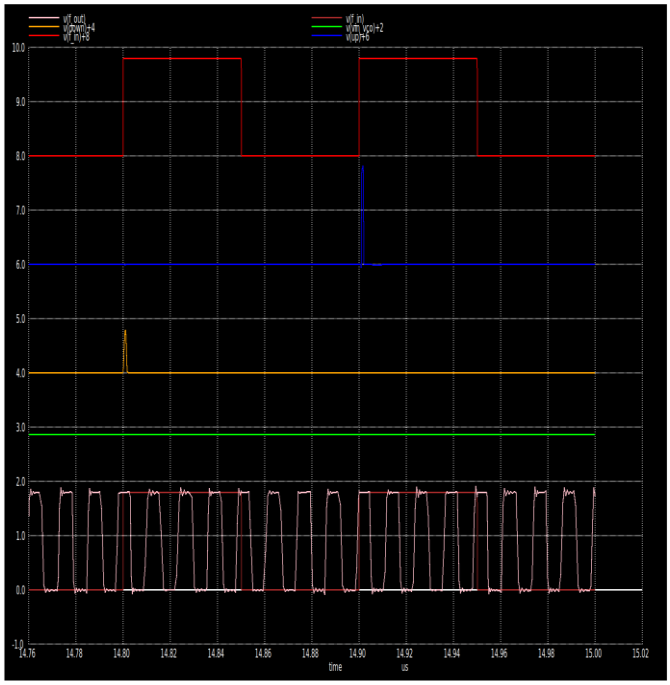

Fig: Waveforms at each node of PLL(magnified)

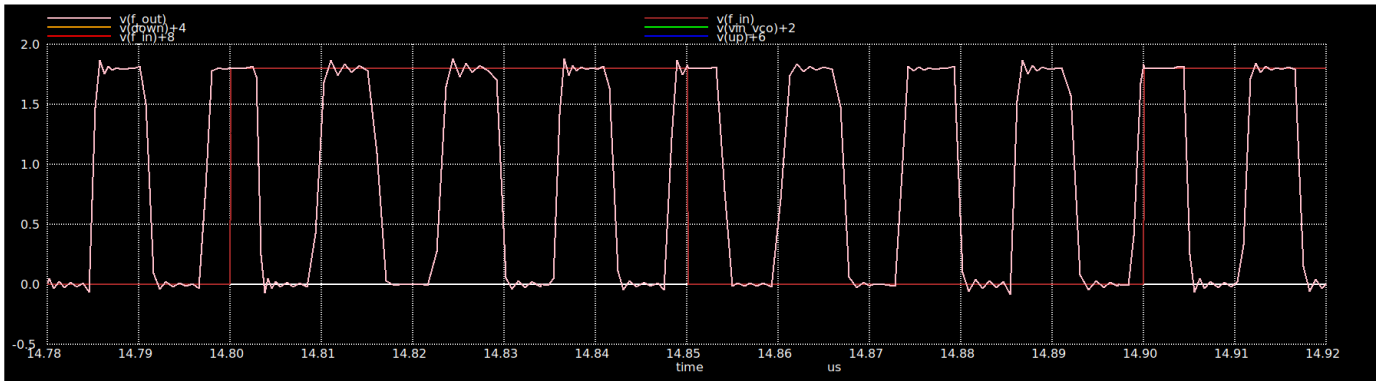

Fig:Waveform showing input and output comparison

Result:

Input Frequency (f_in)	**10MHz**
Output Frequency(f_out)	**80.64MHz**

2.For input frequency (f_in) = 12MHz

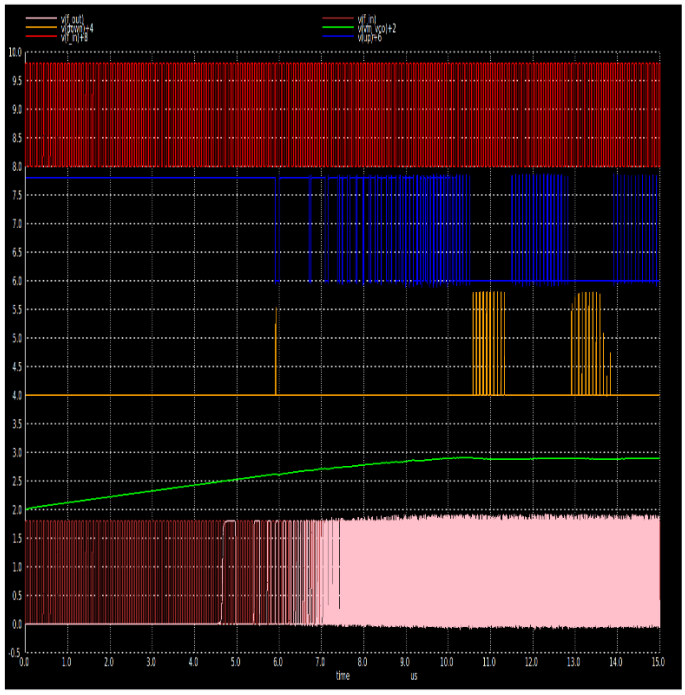

Fig:Waveforms at each node of PLL

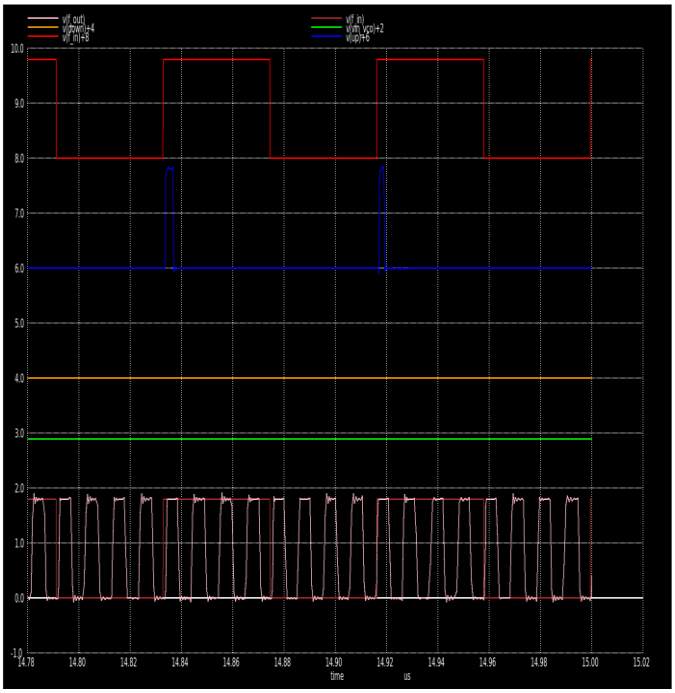

Fig: Waveforms at each node of PLL(magnified)

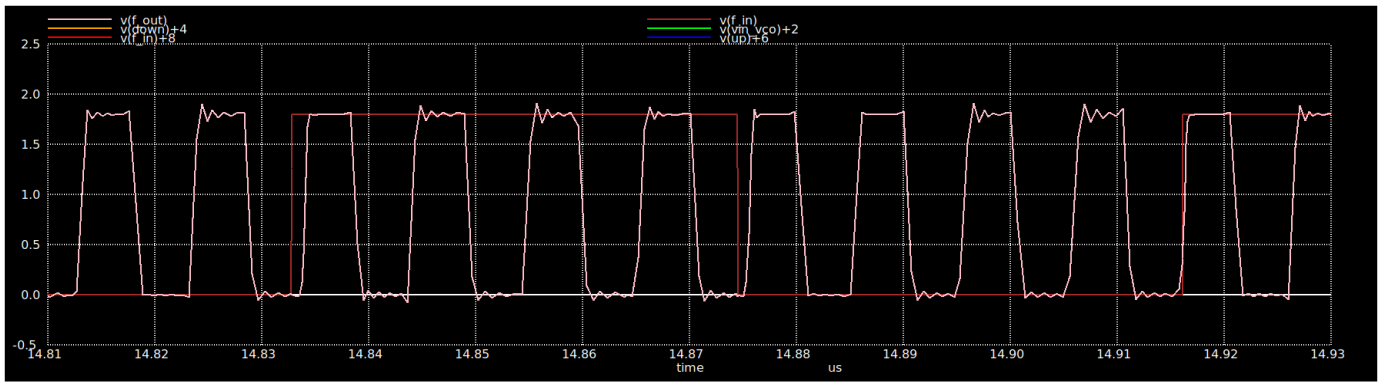

Fig:Waveform showing input and output comparison

Result:

Input Frequency (f_in)	**12MHz**
Output Frequency(f_out)	**96.153MHz**