PLL

Report (Stage-2)

Submitted by:Venkata Ashok Kumar

1.For input frequency (f_in) = 5MHz

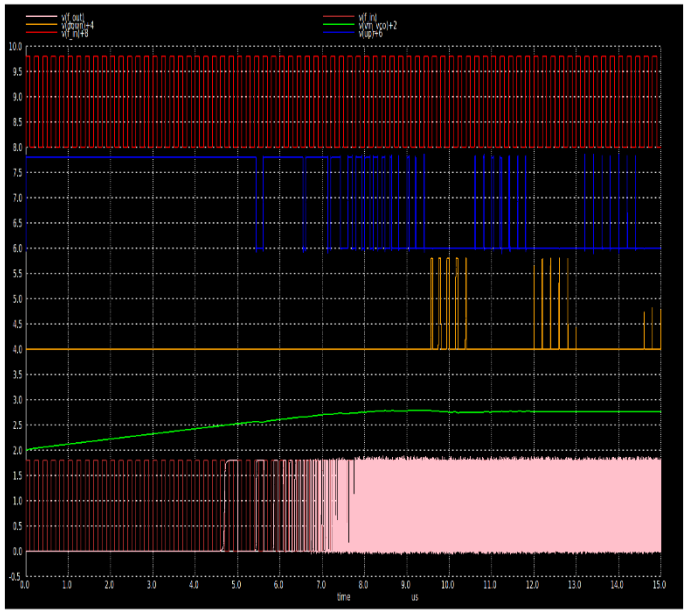

Fig:Waveforms at each node of PLL

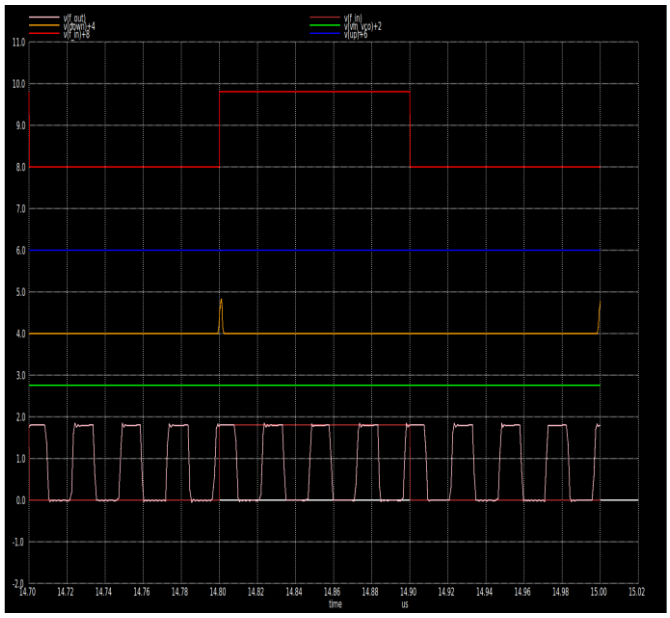

Fig: Waveforms at each node of PLL(magnified)

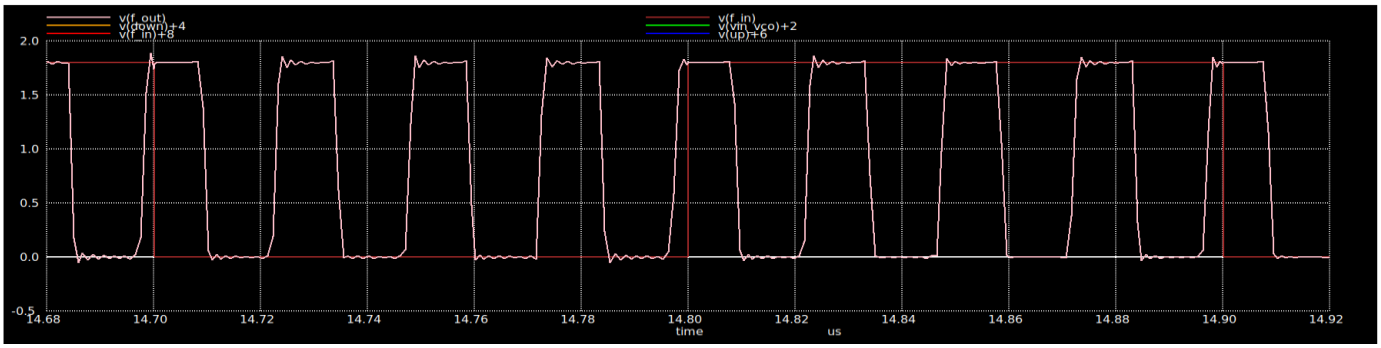

Fig:Waveform showing input and output comparison

Result:

Input Frequency (f_in)	**5MHz**
Output Frequency(f_out)	**40.16MHz**

2.For input frequency (f_in) = 10MHz

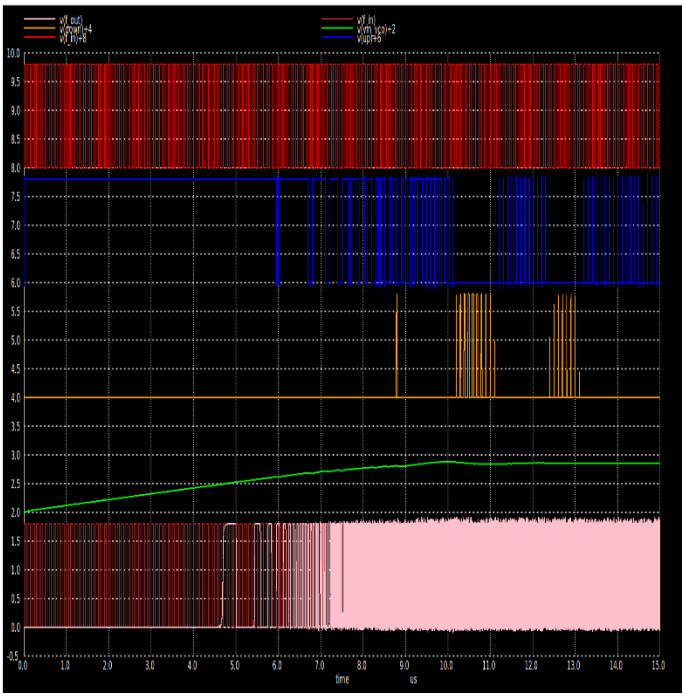

Fig:Waveforms at each node of PLL

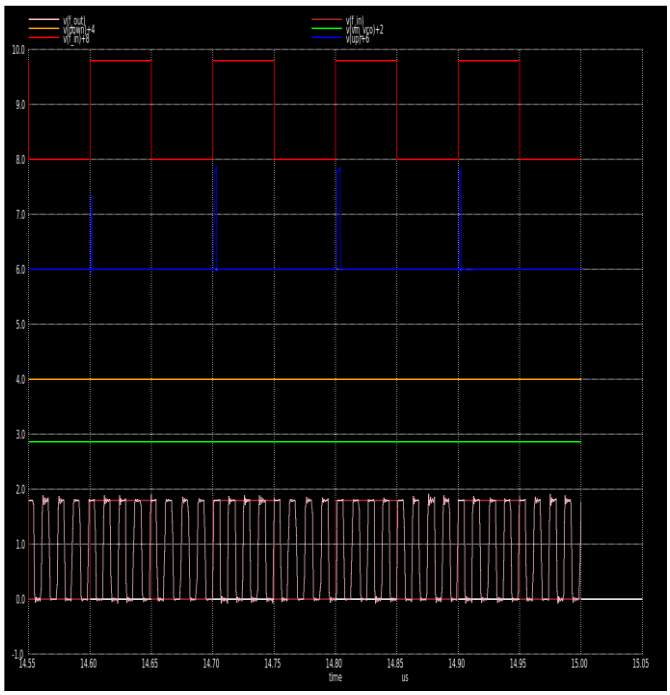

Fig: Waveforms at each node of PLL(magnified)

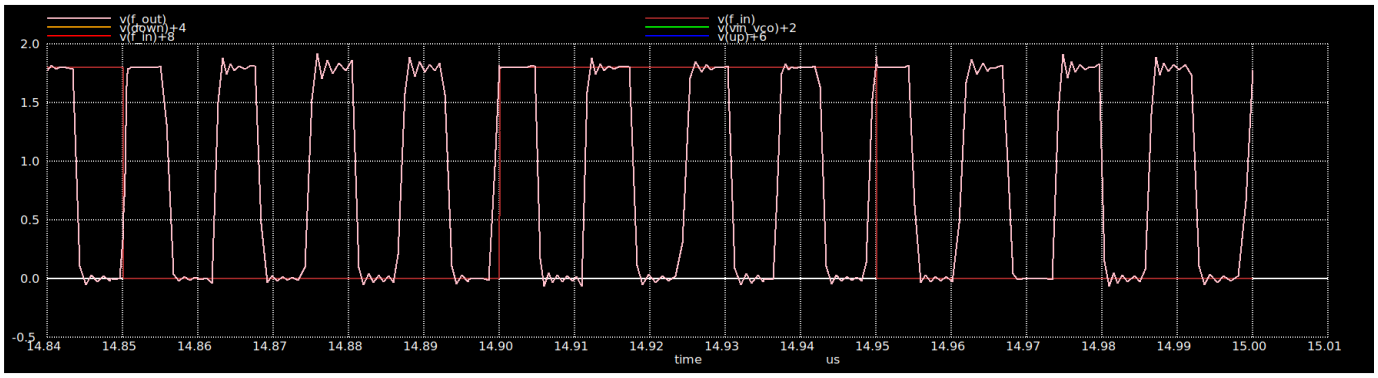

Fig:Waveform showing input and output comparison

Result:

Input Frequency (f_in)	**10MHz**
Output Frequency(f_out)	**81.8MHz**

2.For input frequency (f_in) = 12MHz

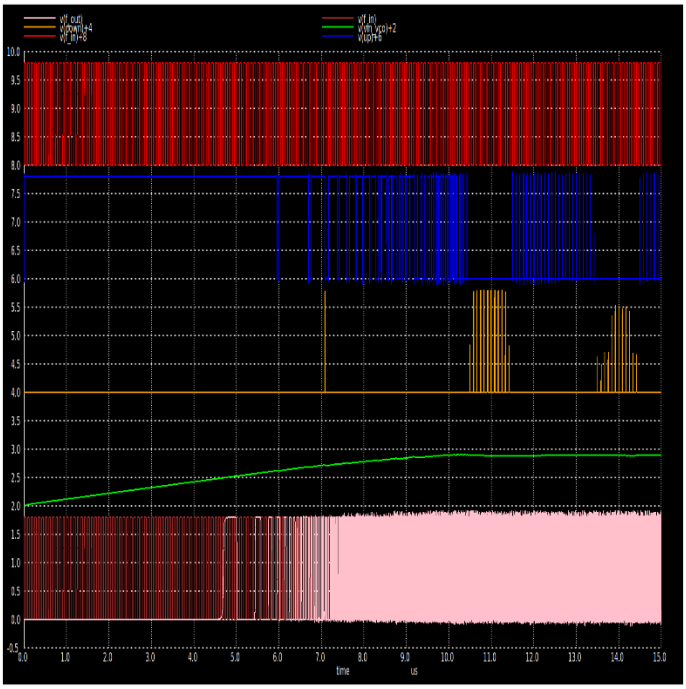

Fig:Waveforms at each node of PLL

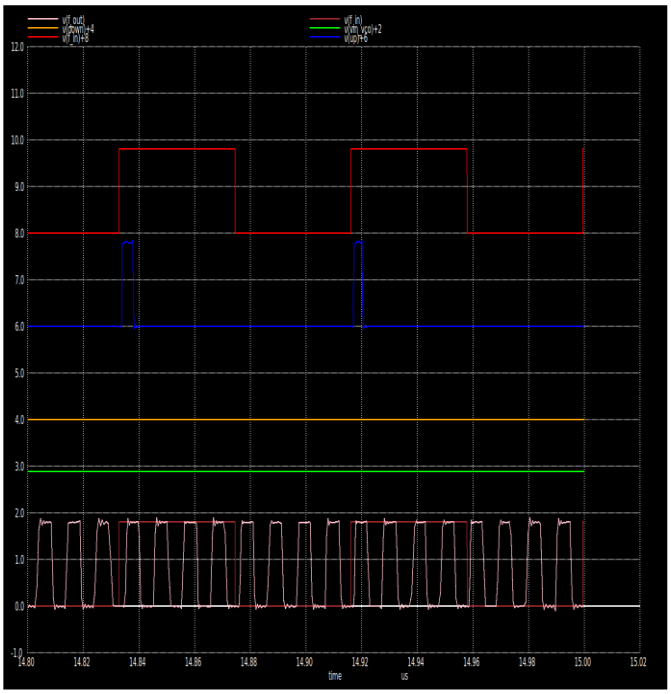

Fig: Waveforms at each node of PLL(magnified)

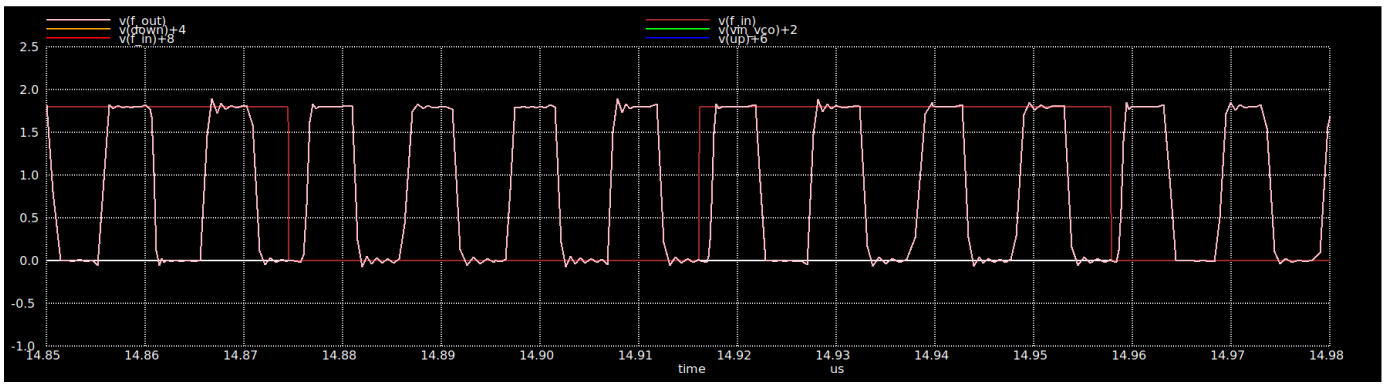

Fig:Waveform showing input and output comparison

Result:

Input Frequency (f_in)	**12MHz**
Output Frequency(f_out)	**97.08MHz**