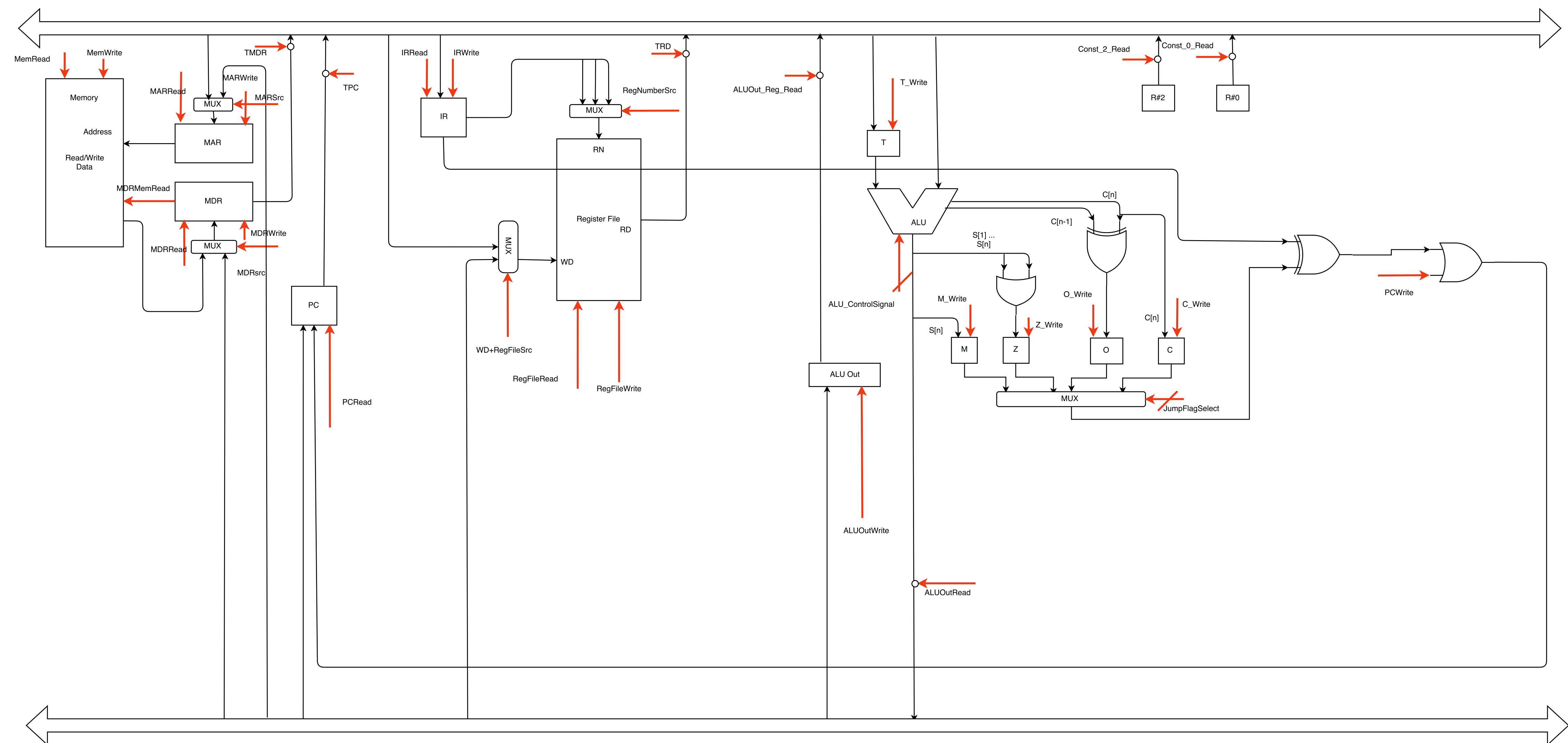


CPU Datapath design



RegBank Internal Structure

