ASSIGNMENT - 1

Question no:1 Group no: 4

Members : Ashrujit Ghoshal (14CS10060) Sayan Ghosh (14CS10061)

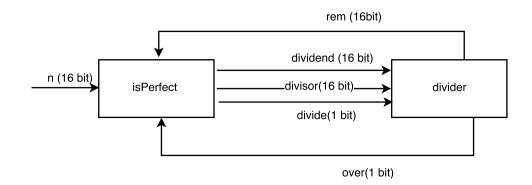
Ports for isPerfect module :

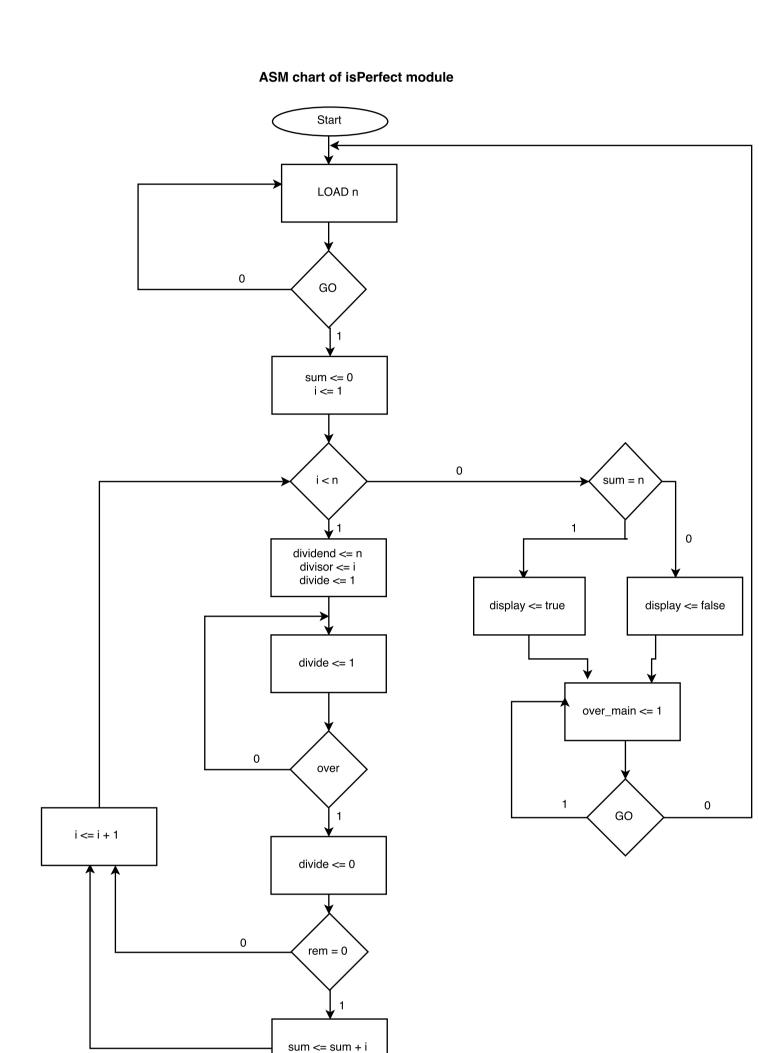
Input : n[15:0] and GO Output : display and over_main

Ports for divide module :

Input : dividend[15:0], divisor[15:0], divide Output :rem[15:0] and over

Connection between the modules





ASM chart of divider module

