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Education

Degree/Certificate	Institute/School,City	CGPA/%
Bachelor & Masters of Technology (Computer Science)	Indian Institute of Technology, Kanpur	8.0/10.0
Bachelor of Technology (Computer Science)	Indian Institute of Technology, Kanpur	6.1/10.0
Masters of Technology (Computer Science)	Indian Institute of Technology, Kanpur	8.0/10.0
Current CGPA	Indian Institute of Technology, Kanpur	8.0/10.0
All India Senior School Certificate Examination-XII	Senior Seconder School Sector -X ,Bhilai	73%
All India Secondary School Examination-X	Kendriya Vidyalaya Sarni,MP	83.6%

Projects

- **Summer internship**
National Des telecommunication University
– basic concepts of watermarking
– implement watermarking on the video
– research paper in the conferance in callifornia
Paris, France
may. 2007 - July. 2007
- **Extension of Nachos**
Prof. Manindra Agarwal, Department of Computer Science, IIT Kanpur
– Worked in group of three as a part of Operating Systems Course
– implemented some features like System Calls, Scheduling Algorithms, Multiprogramming and Virtual Memory.
- **UDP-Chat system**
Guide: Dr. Bhaskar Raman, CSE Department, IIT Kanpur (Fall 2006)
– Developed a chat system with functionalities of conferencing and file sending.
– Designed a protocol to implement reliability, flow control and congestion control over transport layer in User Datagram Protocol (UDP) using socket programming.
- **Implementation of SDLX Processor**
Dr. Rajat Moona, CSE Department, IIT Kanpur Spring 2005
– implementation of SDLX processor having Instruction-fetch & Register-decode unit,ALU, Register file and MEM unit with delayed branch semantics
– Work was done on Spartan-3(FPGA board)in Verilog.
- **Oberon Compiler**
Dr. Sanjeev Kumar Agarwal, CSE Department, IIT Kanpur Spring 2007
– As a part of course curriculum of Compiler Design, I with two of my colleges, are developed a compiler for Oberon language in java with almost all features
– We have completed lexical , Syntax Analyzer and Semantic analyzer. We have also proposed to implement Code Optimizer with Intermediate Language Generation and Code Generation.
- **PHP based webchat**
Prof. T. V. Prabhakar, CSE Department, IIT Kanpur Fall 2007

- implemented with the software engineering concepts using UML diagrams.
- It is modular and follow the MVC design pattern

• **Peer to Peer chat**

Prof. T. V. Prabhakar, CSE Department, IIT Kanpur *Spring 2008*

- it is decentralised p2p chat using java's JXTA technology.
- Bare minimum chat with peer table and discovery .

• **Solver for Very Large System of Equations**

Prof. Phalguni Gupta, Department of Computer Science, IIT Kanpur *Spring 2008*

- in a group of two, successfully developed a solver for a large system of equations of order 1000x1000 where each of the coefficients being of the order of 500 digits in a finite field using LU decomposition
- It is for clusters and mesh connected computers, with GF2 field and both real numbers

• **Offline biometric signature Verification**

Prof. Phalguni Gupta, Department of Computer Science, IIT Kanpur *Fall 2008*

- in a group of two, successfully developed a signature verification system using SVM which can used for scanned images of signature.
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• **Similarity Search in time series database**

Prof. Arnab Bhattacharya, Department of Computer Science, IIT Kanpur *Fall 2008*

- I have made this as single person , the aim of the course project is to study search methods for long time series databases.
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• **motion detector**

Prof. R. K. Rathore, Department of Mathematics , IIT Kanpur *spring 2006*

- in a group of two, we have made a software which can detect moving object in video taken in still camera position by background subtraction algorithm .
- It was part of the course project of "Digital Image processing"

• **Prefetching & Caching protocols for Wide Area Wireless data dissemination**

R. K. Ghosh, CSE Department, IIT Kanpur *Fall Of 2007*

- Problem is in prediction of future data access and it can be severe if the Mobile user is going from one hot-spot to another.
- Idea is to potentially predict what user might need in near future and prefetch and cache the data
- low bandwidth network that can result in really high latency. So, correct prediction of future data access is highly necessary
- we use users speed, location, route, history to predict future data access.

• **Concurrent vi**

R. K. Ghosh, CSE Department, IIT Kanpur *Spring 2008*

- to make vi a concurrent editor of linux
- to implement locks in the data section
- code was submitted on source forge and get much appreciation from opensource community as it was totally new feature in the vi

• **Medical Records**

Sumit Ganguly,, CSE Department, IIT Kanpur *Fall 2006*

- it was part of the database course and was developed using LAMP architecture.

- show statics of which area of india is affected by which disease in which year
- data mining over medical data

- **network Analyser**

- *Sumit Ganguly,, CSE Department, IIT Kanpur*

Fall 2007

- It was part of the datastreaming course and developed using pcap library ,in linux
- it is based on randomised fast map,count min ,count sketches.
- it can during run time shows the statics of the packet flow and bandwidth utilisation by different ips.

- **dual Stepler**

- *Amit Ray, Dept. Of Huminty & Social Science,, IIT Kanpur*

Spring 2008

- to make single stepler able to use two kind of pins
- It is a project in which we have to design inovative things , we secured A grade in this.

Awards

Secured All India Rank 416 in IIT-JEE exam in 2004 in general category.

Secured All India Rank 1200 in the AIEEE 2004 with state rank 12.

Cleared the first phase and secured the Top 1% in National Physics Olympiad .

NSEP stands for National Standard Examination in Physics. It is conducted by Indian Association of Physics Teachers (IAPT). About 200 top students in NSEP are selected to appear for the Stage II examination, i.e. INPhO. These students get a certificate of merit each.

Research & Technical Capability

- I worked on watermarking videos using dct transformation during Internship Achievemnt:- I sugest some improvement and new method for the same which result in the paper published in the "Electronic Imaging 2008".I received the appreciation and invitation for the conferance in callifornia , from the professor Mihai mitrea ,whose email* is attached herewith.
- Currently I am doing my masters thesis under professor Phalguni Gupta, The title of my thesis is "Onlinre signature verification " , which is MHRD sponsered project.
- I spent the summer vacation of 2006 after my second year, working at SIMORTEL,in campus ,a SIDBI incubated company. I worked on a GPS based software to track the buses in city which is more likely to be used in future years, written in C.

Positions of Responsibility

- Elected as techncl assistance(TA) in cs220(Computer Organisation) course.helped the course students in implemntationof ALU , Regfile, Ram , CPU in FPGA Board.
- Mentor the winter project under the sunclub,lead the group of 3 people and helped them in completing the project.

Skills

Languages: C/C++, L^AT_EX, Java, C, C++, Java, C#, Php, ruby, shell scripting, python, UML, Verilog, IA32 Assembly.

Operating Systems: Linux (Redhat , Ubuntu ,Fedora), Solaris, Windows 95/98/NT/2000/XP

Applications and IDE: Mathematica, MatLab, Visual Studio, Eclipse, Netbeans, GCC debuger, OpenOffice, MS Office XP , Ruby on Rails, LAMP, Apache Web Server, Mysql, Oracle.

Lab Skills and Experiance: FPGA Board , Mechanical Lab , Electronics Lab.

Miscellaneous: application of software engineering concepts, strong verbal and written communication skills, excellent troubleshooting and debugging skills, exceptional problem solving skills, good team spirit.

Interests And Extra Corecellar

Academic: syatems, os, network, databases , new web based technologies , microcontrollers.

Sports: swimming, I was in Institute swimming team during 1st year at IITK.

Computers: Currently maintain two official ubuntu Linux packages, Mozilla beta tester, enjoy using and learning Linux systems .

Musical: Playing synthesizer

technology: reading latest technology news using Zdnet. C# coders.

Other: Reading novels

Courses

Relevant: 1. System related:-

Distributed Systems , Operating Systems , Computer Organisation , Fundamental of electronics , Principles Of DataBase Systems , Compiler Design

2. Theoretical:-

Advance Algorithms , , Data Structures , Parallel and semi Numerical Algorithm

Theory of Computation , , Numerical Methods In Engineering, Linear Algebra

Discrete Mathematics

3. Network related:-

Mobile Computing , Computer Networks , Data Streaming

4. Software :-

Principles Of Programming Languages , Software Engineering , Internet technologies ,

Programming Tools & Techniques, Fundamentals Of Computing

other courses: rest are from grad sheet.

Attachment

***email:** Dear Dr. Rolin, Dear Professor Prteux,

Please, find attached some good news: our paper including some of Ashutosh results has been accepted for Electronic Imaging 2008. Of course, this means for Ashutosh some more experiments to do but I am quite confident he will confirm the very good impression he gave us. With my highest consideration, Mihai