

# Undecided

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# Chapter 1

## Abstract

Intermediate languages in compilers glue two different compiler construction phases. Being machine independent makes it possible for an IR structure to transform from one form to another to allow different patterns of reasoning, and different stages of transformation. In this thesis the proposal of a logical language based IR is explored by inserting it in a incremental compiler pipeline as a mid-level IR. This IR lies in between the source code and the LLVM IR, enabling Logical programming analysis techniques to be used for optimisation before the usual LLVM optimisation. Using this compilation pipeline we build an incremental compiler which is focused on LP analyses and reusing IR structures already built in previous instances compilation of a target.



## Chapter 2

# Introduction

This thesis explores using a logical intermediate representation (IR) in an incremental compiler pipeline for a new declarative and imperative mixed paradigm source language. This logical IR is called LPVM, and its simple clausal structure enables logical programming analysis and easier optimisations. It lies middle of the compilation pipeline, before an LLVM generation stage, making it a middle level IR. Using this representation, we can also make the compiler incremental and have a lazy build system which tries to avoid re-compilation as much as it can. Even though having multiple IR stages and forms adds extra work in the compilation process (and compiler construction), we show a simple transformation of LPVM to LLVM, so that the compiler is able to target all the machines LLVM can realistically.

The source language is called Wybe. It's a new language which aims to unify the good parts of declarative and imperative languages. Having a mixture of paradigms makes it a good fit for a compilation pipeline which involves a mixture of paradigms as well.

The data structure of an Intermediate representation is an important factor in deciding what optimisation passes are going to be useful. Different compilers usually have their own IR, which maybe only slightly different from other IRs. The actual form is really a compiler construction choice. There are efforts to build a universal IR, like the LLVM project, but a reasonably complex language would have it's own unique requirements which can't be accounted for in a single universal IR or form. The IR generation stage in a compiler pipeline goes through multiple optimisation passes. There is no restriction on the form of the IR as it moves through these passes. In fact having multiple IR forms, which gradually transform from being closer to the source language to a more machine dependent form is quite common. Multiple forms opens up multiple approaches to optimisations.

Mid level IRs, quit simply lie in between some high level IR or source

code and a low level IR. We insert LPVM into the compilation pipeline as a mid level IR between the source code and the LLVM IR. In a way, our target code is the LLVM IR. Even though LLVM code generation comes with it's own set of curated and tested optimisations, its main use here is to avoid the need to account for every architecture. Thus we can focus on maximising the usefulness of LPVM. We also show how the clausal form of LPVM can be easily translated to the more imperative block style of LLVM.

The Wybe compiler wants to be lazy and incremental. It wants to avoid as much recompilation as it can. The object files Wybe compiler builds have enough information to be used in place of a source file, while at the same time provide inalienable code which could only have been obtained from the source code. The simple clausal semantics of LPVM is the key to this. To be more incremental, the compiler maps and tracks the source code semantic structures to LPVM clauses. For re-compilation, it tries to load the already compiled clauses stored in the object files for any trivial changes of the source.

Another focus of the Wybe compiler is to have a build system ingrained into the compiler. We try to mimic the gnu make utility with some simplifications, and hence our Wybe compiler is usually run through the command 'wybemk' (Wybe make). Instead of having a separate make file, the Wybemk command just takes a target name, infers the dependency chains and the list of files to compile and link and makes the target. We wanted to have the ability to link in foreign source object files and not just Wybe source files.

Why use a logical IR?

## Chapter 3

# Literature Review

### 3.1 Horn Clauses as an Intermediate Representation

The paper describes a new form of IR using a logical programming structure, now called LPVM. The wybe compiler presents an implementation of this clausal form and as such LPVM is a core part of its pipeline. This thesis provides the final code generation stage for LPVM and wybe.

LPVM is an attempt to make program analysis and transformation easier. It's an alternative to the common imperative IR forms while maintaining a similar set of program analysis methods. The paper compares LPVM against the other current options for an IR, like the three address code and SSA, discussing the drawbacks of these common IRs. The focus is on solving these drawbacks at a more fundamental level rather than relying on further transformations. The motivation outlined in the paper is having a simple limited structure in analysis, while drawing in existing logical programming reasoning. The limited set of primitives, additionally enables the wybe compiler to be more efficient by embedding a LPVM transformation in the object file. This would have been inefficient with a more complex structure.

The Three Address Code (TAC) IR and its refinement, the Static Single Assignment (SSA) form, are popular IRs used in compiler constructions. They are simple enough to be universal and are quite extensible to accommodate different source language semantics due to their little restrictions. A SSA based IR will generally be laid out as basic blocks and branching instructions connecting them, like a graph. The SSA refinement requires all variable names to be unique in a block. This makes it easier to track variable lifelines. But this also requires a virtual function called phi to choose the value of the same variable coming in from two different blocks. The paper points out that this is inadequate for determining the control flow path. While the phi function can tell us which blocks are providing the values for the same variable during analysis, it won't tell us the condition that was

evaluated for that decision. This can be done by looking into all the predecessor blocks that provided the value. Another solution to this, as pointed out by the paper, is using the Gated Single-Assignment (GSA) form which augments the existing phi function to capture the block entering condition.

Another drawback described is the SSA form being biased to forward analysis. The phi function at the top of a block is helpful for analysing that block locally. A predecessor block (to the block with phi) just knows about its branching destination, and not about the phi function choosing between its variables and other blocks. A transformation to the standard SSA form, called the Static Single Information (SSI), provides a solution to this problem by adding another virtual function to the end of the blocks which describes the variables that will be compare with other blocks in the subsequent block.

So far every drawback of SSA has been addressed can be solved by creating an extension to the SSA structure. While these are perfectly feasible, they are additional complexities. In the case of LPVM these problems are solved at the basic structural level, without any special functions. During analysis, the special functions of SSA have to be resolved with extra work. An analysis of a block containing phi node involves revisiting the analysis work done for the predecessor blocks. This is a result of SSA's naming rules. Even though SSA enforces unique names for every declaration, alternate blocks can still have the same variable names. A more functional translation of SSA avoids this, but still exhibits the forward bias problem.

The LPVM IR is a restricted form of a logical language. It does not feature non-determinism seen commonly in a LP. Therefore all input parameters have to be bound to a value before calling that procedure. It also requires fixing the mode of a parameter. In LP, a procedure can have a parameter which can behave as an input or an output. LPVM requires this behaviour to be explicit and fixed for every parameter for easier reasoning. These restrictions makes LPVM surprisingly easy to read.

At the top level LPVM form there are only procedures. In the form presented in the paper a procedure consists multiple *Horn Clauses*. A horn clause with its *Head* describing its parameters and procedure name, and the sequence of goals as the body can be seen as a procedure itself. Since LPVM wants to be deterministic, only one of the clauses with the same name evaluated for given inputs. That clause can be seen as the entire procedure itself with the *Head* as the procedure signature, without bothering about the usual backtracking in LP. The parameters to a clause also needs its modes to be explicitly defined: either as an input or an output. There may be two alternative *Clauses* of the same name having switched up modes for the same parameters. Since determinism will select only one of them as the procedure, single modedness is preserved.

The *Goals* are simple body statements. Logical statements in SSA result in block cause branching. These statements are represented as guard goals



$x0 = x1 + x3$	$wybe.int. + (x1, x3, ?x0)$
$if (x0 > 0) left right$	$wybe.int. > (x0, 0, ?tmp1)$
	$case tmp1 of$
	$0 : left..$
	$1 : right..$

Figure 3.1: SSA statement and their equivalent LPVM goals

<i>Proc</i>	$\rightarrow$	<i>Clause*</i>
<i>Clause</i>	$\rightarrow$	<i>Proto Body</i>
<i>Proto</i>	$\rightarrow$	<i>Name Param*</i>
<i>Param</i>	$\rightarrow$	<i>Name Type Flow</i>
<i>Body</i>	$\rightarrow$	<i>Prim * Fork</i>
<i>Prim</i>	$\rightarrow$	<i>PrimCall   PrimForeign</i>
<i>Fork</i>	$\rightarrow$	<i>Var Body*</i>

Figure 3.2: LPVM Implementation Data Type

in LPVM. When a guard goal is encountered, a new clause will be created with the same goal sequence until that guard, but will follow the complementary evaluation of the guard after that. In the implementation however, the procedure body or the clause body or the sequence of goals are put in a tree data structure. Instead of having somewhat duplicated sequence of goals in creating an alternate clause, the guard goals instead create branching child nodes, each with further sequence of goals. This is similar to the layout of basic blocks within a function in SSA, which makes it easier to translate the LPVM to any SSA based IR later for code generation.

At the end of conversion to LPVM, the IR form only contains procedures with multiple clauses. Alternate branches of conditionals are incorporated in their own clauses, chosen by the evaluation of a guard statement. Unconditional jumps are procedural calls and loops are flattened to recursive procedure calls. Every procedure will also define all its input and output parameters explicitly in its signature. Since output variables are specified here, there is no need of a return instruction. The only variables that would exist in the clause bodies are now the specified input and output variables, and temporaries between them. This declarative paradigm greatly simplifies a lot of analyses. This also helps determine the purity of a procedure.

## 3.2 Current Implementation of LPVM

In the LPVM implementation used by the Wybe compiler every goal is like a primitive procedure call. At the most basic level these are similar

to primitive machine instructions. This again simplifies code generation. As mentioned above, instead of having duplication of clause bodies the guard goals currently result in a fork of bodies. Generation of two complementary clauses similar to the LPVM specifications in the paper is trivial from this tree structure.

All loops and conditionals are un-branched by generating new procedures which are involved in recursion. The calls are tail calls and hence the code generation will have to ensure tail call optimisation is enabled. Simple conditionals like if conditions, just generate a fork in the body. The alternate destinations, say *Body A* and *Body B* are stored in a list in the *Fork*. Since the *Fork* is at the end of a clause *Body*, the remaining body (after the If statement) is placed in a new generated procedure. This generated procedure is called at the end of *Body A* and *Body B*.

## Chapter 4

# Wybe Build System

Wybemk is the wybe compiler and wybe make system joined together in one utility. The wybemk compiler just needs the name of a target to build, and it will decide the building and linking order. Targets include architecture object files, LLVM bitcode files, or a final linked executable.

Similar to the `gnu make` utility, it does not want to rebuild any object file which is already built. Taking it one step further, the object files wybemk makes store useful LPVM information generated from the source code. While a normal object file does present its name table, allowing the functions stored in it to be called as extern functions, these functions are not inlineable. While source code for functions can be stored in a section of the object file, it is easier to just pass the original source file around with the object file. The LPVM structure of a parsed source file however is simpler in its structure. A body of a function is simply a list of clauses. Storing this information in the object file enables inlining in at the LPVM generation stage. Inlining also presents a wider view for analysis as we can look at the body of the functions we want to call externally.

Storing a serialised form of LPVM in the object file is quite similar to the LLVM bitcode files, which stores the LLVM IR in a byte file. To keep this option open, since we use LLVM as the final stage, we also present a wrapped LLVM bitcode structure which stores both the LLVM and LPVM IR in it. Such a file can be linked in by wybemk or by the LLVM compiler, which loads different information from the file.

### 4.1 Storing structures in Object files

Object files store relocatable object code which is the compiled code generated by the LLVM compiler in the wybe compiler. Even though different architectures have their own specification of the object file format, they are modelled around the same basic structure. Object files defines segments, which are mapped as memory segments during linking and loading. The

segment TEXT usually contains the instructions. An object file also lists the symbols defined in it, which is useful for the linker to resolve external function calls from another module being linked. Avoiding all the common segment names, it is possible to add new segments to the object file (at the correct bytes), which do not get mapped to memory. Using such a segment we can attempt storage of some useful serialised meta-data in the object file.

To emulate the behaviour of `gnu make`, or to avoid rebuilding an object file, it is quite trivial to check the file creation date and reach a decision. However this hinders a lot of LPVM level optimisation. Since there is no need to compile a source module if it has a newer object file for it, we can't do any inter-module analysis with it. We can get the symbol names and possibly the generated object code for the function names, but those are not useful for LPVM analyses. Storing generated LPVM code in the object files would circumvent this. The simple semantics of LPVM is an excellent candidate for storage in object files without being disruptive. Even when the source code file is present, loading the LPVM code from the object file is faster than engaging the parser and generating LPVM from the parse tree.

LLVM also generates bitcode files. These files store a serialised form of the generated LLVM IR. The LLVM compiler can transform these to the machine local object file too. Being a simple bitstream format, it is possible to include other serialised metadata into this file as long as the LLVM compiler can find its required IR. Storing LPVM in these files is a feasible alternative to the machine local object file formats. LLVM bitcode files can be used for code generation on any valid target, much like the Java bytecode.

## **4.2 LPVM as a stored structure**

The simplicity of LPVM makes it efficient...

## **4.3 Loading Inlienable LPVM**

Talk about object file structures?