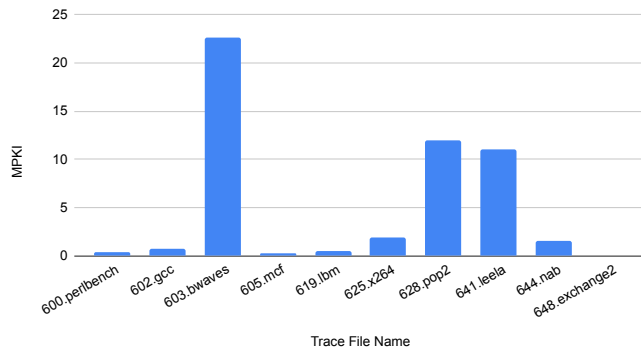
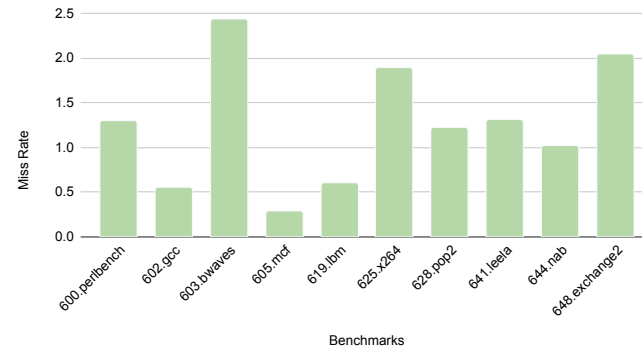


LRU Replacement Policy										
Benchmark	LLC Accesses	LLC Hits	LLC Misses	IPC	CPU Cycles	Instructions Retired	MPKI	%Miss Rate	%Hit Rate	# Miss Latency
600.perlbench	547,609	201,194	346,415	1.291	774,619,423	1,000,000,003	1.97	63.26%	36.74%	176.2
602.gcc	8,541,902	179,351	8,362,551	0.5471	1,827,779,844	1,000,000,004	0.3258	97.90%	2.10%	155.8
603.bwaves	368,341	167,218	201,123	2.438	410,102,110	1,000,000,003	0.6822	54.61%	45.39%	196.4
605.mcf	31,349,076	18,230,551	13,118,525	0.2811	3,557,252,132	1,000,000,000	22.64	41.85%	58.15%	171.6
619.lbm	58,415,264	25,916,328	32,498,936	0.5918	1,689,788,855	1,000,000,002	0.2108	55.63%	44.37%	487.9
625.x264	1,750,156	678,537	1,071,619	1.892	528,636,605	1,000,000,004	0.4896	61.23%	38.77%	219.3
628.pop2	9,596,767	6,014,783	3,581,984	1.221	819,239,135	1,000,000,001	1.863	37.32%	62.68%	272.6
641.leela	123,213	61,958	61,255	1.312	762,165,994	1,000,000,004	11.97	49.72%	50.28%	229.5
644.nab	215,467	16,163	199,304	1.018	981,912,945	1,000,000,000	11.07	92.50%	7.50%	157.3
648.exchange2	154	0	154	2.047	488,596,845	1,000,000,002	1.547	100.00%	0.00%	167.3

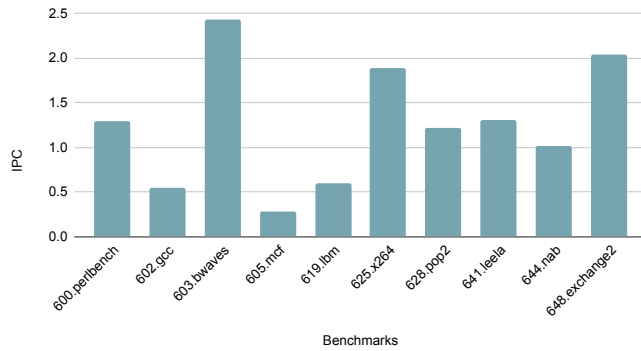
MPKI vs. Benchmark



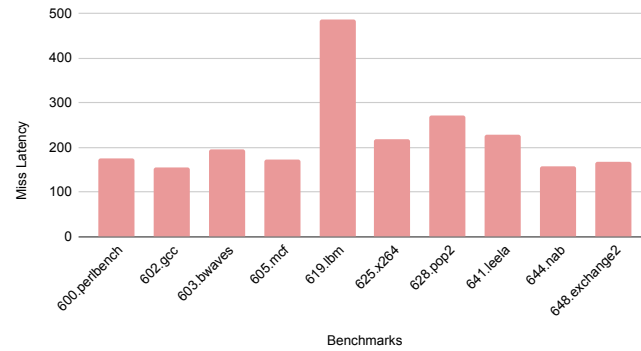
Miss Rate vs. Benchmark



IPC vs Benchmarks

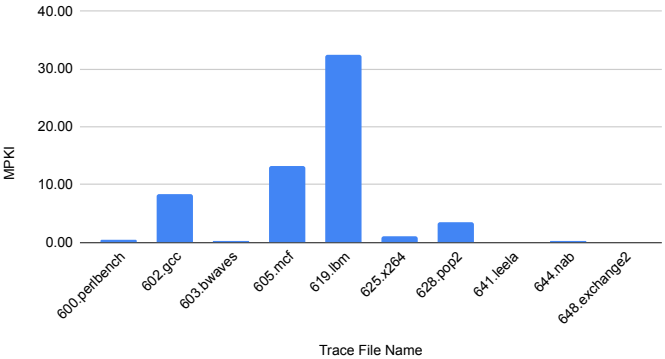


Miss Latency vs Benchmars

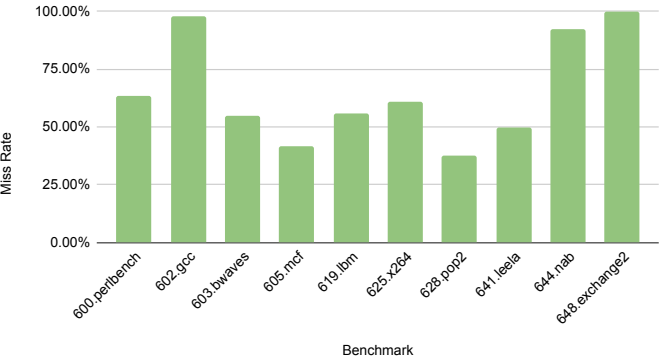


SEG-LRU																		
Benchmark	#	LLC Accesses	#	LLC Hits	#	LLC Misses	#	IPC	#	CPU Cycles	#	Instructions Retired	#	MPKI	% Miss Rate	% Hit Rate	#	Miss Latency
600.perlbench		547613		201188		346425		1.291		774607423		1000000003		0.35	63.26%	36.74%		176.2
602.gcc		8541902		179361		8362541		0.5471		1827775804		1000000004		8.36	97.90%	2.10%		155.8
603.bwaves		368341		167218		201123		2.438		410102110		1000000003		0.20	54.60%	45.40%		196.4
605.mcf		31349100		18231575		13117525		0.2811		3557095050		1000000000		13.12	41.84%	58.16%		171.6
619.lbm		58416519		25917577		32498942		0.5918		1689724475		1000000002		32.50	55.63%	44.37%		487.8
625.x264		1750431		680041		1070390		1.892		528504540		1000000004		1.07	61.15%	38.85%		218.9
628.pop2		9576846		5996522		3580324		1.22		819788004		1000000001		3.58	37.39%	62.61%		273.1
641.leela		123169		61925		61244		1.312		762172205		1000000004		0.06	49.72%	50.28%		228.7
644.nab		215467		16164		199303		1.018		981912945		1000000000		0.20	92.50%	7.50%		157.3
648.exchange2		154		0		154		2.047		488596845		1000000002		0.00	100.00%	0.00%		167.3

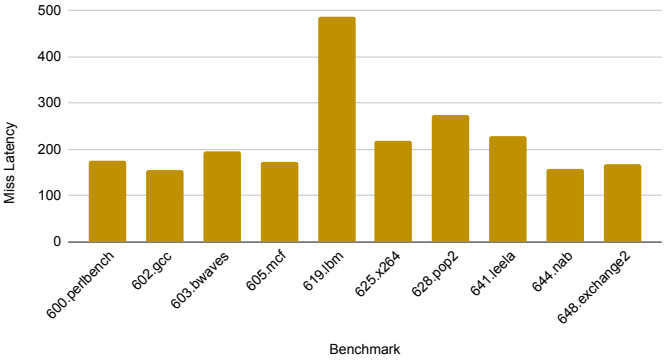
MPKI vs. Benchmark



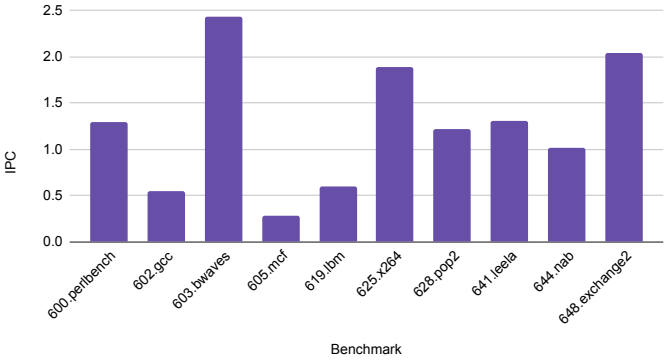
Miss Rate vs. Benchmark



Miss Latency vs. Benchmark



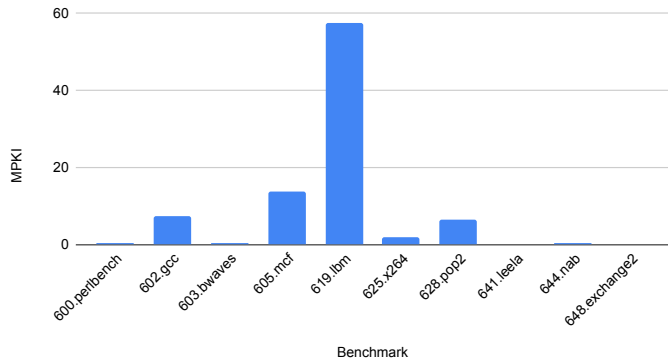
IPC vs. Benchmark



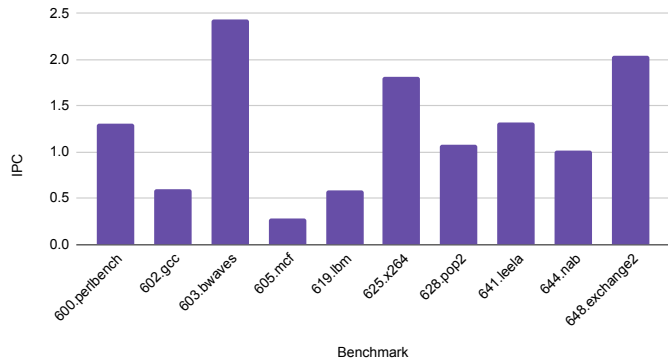
SHIP Replacement Policy

Benchmark	LLC Accesses	LLC Hits	LLC Misses	IPC	CPU Cycles	Instructions Retired	MPKI	%Miss Rate	%Hit Rate	Miss Latency Cycles
600.perlbench	547856	200116	347740	1.302	767987058	1000000003	0.347739999	63.47%	36.53%	151.5
602.gcc	8544191	1110885	7433306	0.5987	1670393124	1000000004	7.43330597	87.00%	13.00%	154.1
603.bwaves	368341	2297	366044	2.44	409776170	1000000003	0.3660439989	99.38%	0.62%	110.1
605.mcf	31349068	17652290	13696778	0.2821	3544295489	1000000000	13.696778	43.69%	56.31%	165.9
619.lbm	58416124	828008	57588116	0.5911	1691895098	1000000002	57.58811588	98.58%	1.42%	276.7
625.x264	1750114	22964	1727150	1.816	550797831	1000000004	1.727149993	98.69%	1.31%	156.3
628.pop2	9613265	3122748	6490517	1.076	929660789	1000000003	6.490516981	67.52%	32.48%	189.4
641.leela	123270	67173	56097	1.319	758253081	1000000004	0.05609699978	45.51%	54.49%	136.7
644.nab	215375	7813	207562	1.018	982537445	1000000000	0.207562	96.37%	3.63%	155
648.exchange2	154	0	154	2.047	488596845	1000000002	0.00015399999	100.00%	0.00%	167.3

MPKI vs. Benchmark



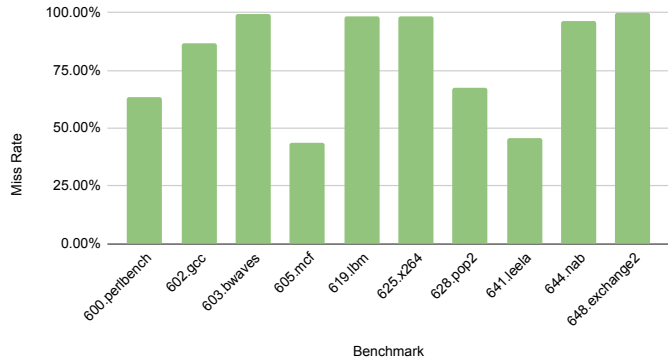
IPC vs. Benchmark



Miss Latency geometric avg MPKI average

161.9121069 0.657073388

Miss Rate vs. Benchmark



Miss Latency Cycles vs. Benchmark

