PULSONIX

Pulsonix Datasheet

Pulsonix PCB Design

Pulsonix is a modern electronics design product designed for the challenging needs of today's environment. Created from the ground up using up-to-date writing techniques and the latest development tools, Pulsonix carries no legacy data or old style methodology.

Innovative and Exciting

Pulsonix is innovative and exciting, every feature has been well thought through and is efficient in its use. All options have been designed with the user in-mind at all times and getting the design produced as quickly as possible.

Integrated Environment

Supplied as an integrated Schematic capture and PCB design layout package, Pulsonix is a ready-to-use product right out of the box. Supplied with comprehensive libraries, graphical and textural library creation tools, you can be up and running instantly with no need for formal training.

High-Tech Architecture

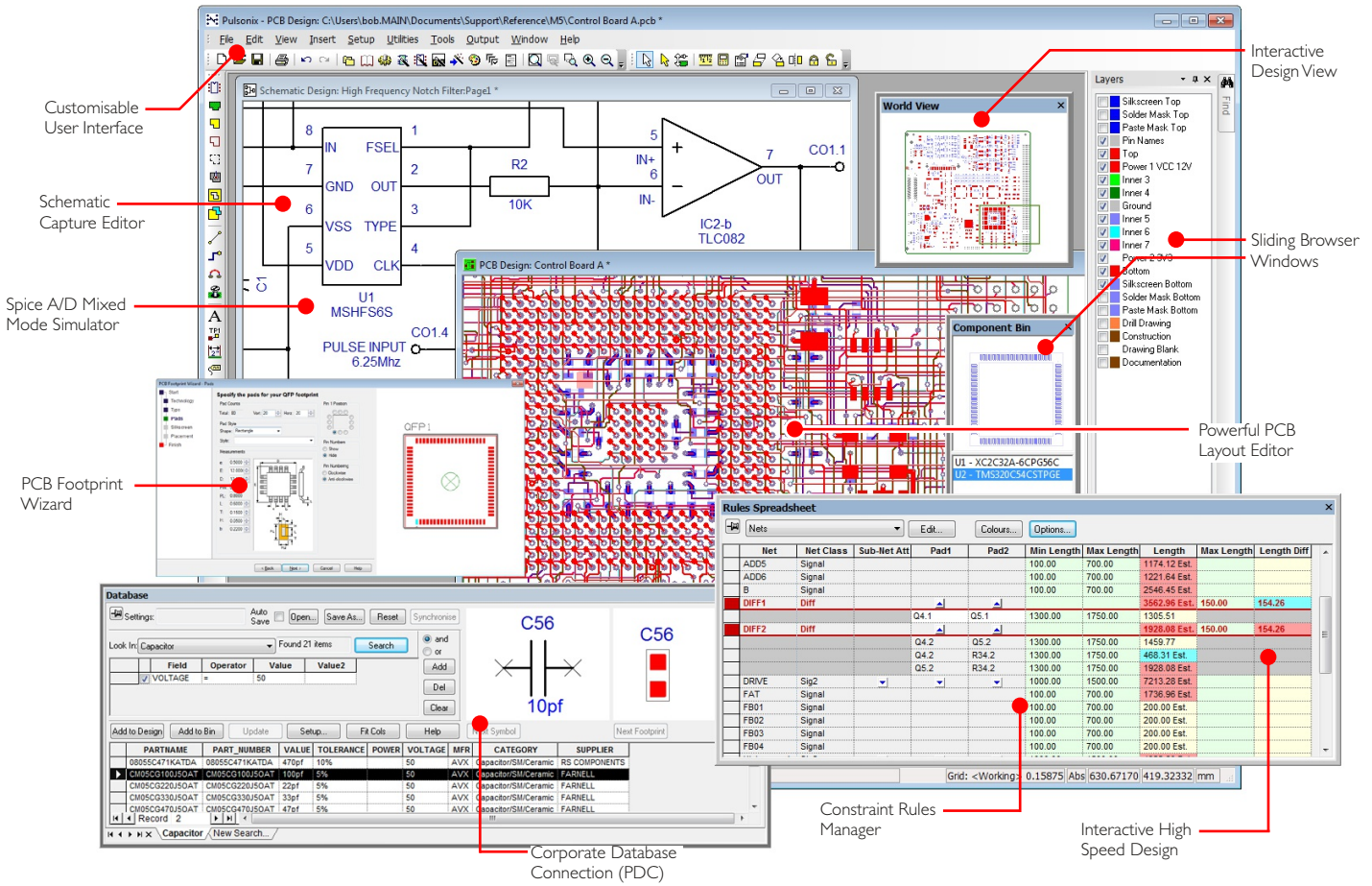
Built on a high-tech modern structured 'mosaic' architecture, Pulsonix is well positioned to add the futures' technology and design trends as they become developed and others that are still only concepts!

Confidence With Tight Design Integrity

With a large range of design validation features from electrical rules checking (ERC) in Schematics, to PCB design rules checks (DRC) and manufacturing checks (DFM/DFT), every stage of your design process can be verified and its integrity maintained.

Free Access to over 13 million Library Parts

The Component Search Engine is an integrated repository that provides you access to over 13 million Parts. Parts are selected based on part number and then allocated suppliers part numbers once the supplier has been chosen. This ensures accurate BOMs using easily accessible parts.



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FPGA Interface

As standard, Pulsonix is supplied with a built-in FPGA interface to integrate with the Altera Quartus II or Xilinx ISE development systems.

Standard Manufacturing Outputs

Extensive manufacturing outputs are exported - Gerber, Excellon, ODB++, Windows, PDF plus a fully customisable report maker interface to create company reports, BOMs, netlists and assembly placement outputs as you require.

Cost Effective Solution

As a cost effective solution, Pulsonix has additional options to enhance your design suite as your needs grow.

With features like Interactive High Speed design and constraint rules, Embedded Component Technology (ECT) for Micro-vias and flexi-circuits, Spice simulation, Product Lifecycle Management (PLM) integration and Connection to your corporate database; Pulsonix is powerful and affordable.

5-Star Service

Pulsonix is widely known for its high quality features and efficient after-sales care. Our service has been given a 5-star rating by our customers, proving that your service and support are a high priority to us.

Migrate your existing Designs and Libraries

Retain your IP with the industry's largest range of high quality and accurate import filters. Import designs and libraries with importers available for:

Altium, Accel, Protel, P-CAD, Eagle, Integra, OrCAD, Allegro PCB, PADS, Cadstar, Visula, DxDesigner, ViewLogic, Zuken CR5000 plus more

What our customers say:

"Pulsonix is our new ECAD standard tool for Schematics, simulation and PCB design. It has been chosen for its modern easy-to-use interface and its perfect price/performance ratio."

O. Hollinger, Carl Zeiss AG.

"On Pulsonix, high density boards with 8 layers and 0.3mm micro-vias are being designed as well as power electronics. With an interface to our MCAD system, this has helped us a lot in shortening our development lead-times and is saving the company money."

P. Goerlich, Durr Dental GmbH

Feature Summary:

- Integrated Schematics and PCB
- Supplied with comprehensive library
- FPGA design interface
- Drag and drop methodology
- Reverse Engineering back to SCM
- Wizards for fast start-up
- Auto-Testpoint insertion & analysis
- Extensive Assembly & Part Variants included
- DFM/DFT and constraints rules checks
- True instanced Schematic hierarchy
- PTH, Blind and Buried via support
- Stacked Micro-vias/laser-vias
- Construction lines for complex shapes
- Teardrops
- Wires and Jumpers
- Track/Via Breakout/fanout support
- Power manual & auto-placement
- Easy, dynamic and flexible copper pouring
- Dynamic Dimensions
- Sub-circuit Design reuse
- Fully customisable interface
- Supports true mixed imperial/metric units
- Hierarchical constraint & spacing rules
- Star and delta points for multiple signals
- Pull-tight/push-aside routing modes
- Built-in 3D Viewer
- STEP Mechanical CAD I/O
- Support for Flexi-rigid boards
- Minimal system requirements
- Fully supported under Win 7, 8 & 10 (32 & 64 bit)
- Floating and Loan licensing available
- Educational licensing available
- First 12 months support & maintenance included

Cost Options

- Interactive High Speed design
- Spice A/D Mixed Mode Simulation
- PLM/PDM Interface
- Corporate Database Connection (PDC)
- Embedded Components (ECT)
- Chip-on-board

Pulsonix

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