A Tale of Two Unions

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Bjarne Stroustrup, in *The C++ Programming Language*, recommends avoiding the use of unions. This is of course good advice, mainly because unions are error prone due to the mixing of types that they entail. If you really do need to use unions (for performace or space reasons) then he advices wrapping them in classes. But does that increase the size of the assembly code generated? Let's investigate.

One of my projects was a BASIC interpreter, that needed to store variables values. Each value was a union that could hold an integer, a floating point type, and a string (more precisely, a pointer to a string descriptor). This project was written in C. Let's see if implementing this union using C++ classes introduces code bloat. For this experiment, I will simplify the union by omitting the string descriptors, i.e. each variable value can be a float or an int.

First, the C code:

```
enum ValueType {INT, REAL};
  struct Value {
    enum ValueType type;
    union {
      int integer;
      float real;
    } val;
  };
  #define CONST_INT(C, VAL) \
    const struct Value C = { .type = INT, .val = { .integer = VAL }}
12
  #define CONST_REAL(C, VAL) \
13
    const struct Value C = { .type = REAL, .val = { .real = VAL }}
14
  CONST_INT(a, 42);
16
17
  CONST_REAL(b, 1.5);
18
  struct Value add_vars(const struct Value *a, const struct Value *b)
19
    struct Value ret;
20
    ret.type = b->type;
21
    switch(b->type) {
22
      case INT:
23
        ret.val.integer = a->val.integer + b->val.integer;
24
        break;
25
      default:
```

```
ret.val.real = a->val.real + b->val.real;
28
29
30
    return ret;
31
32
   void add_a(struct Value *v) {
    v->val.integer += a.val.integer;
34
35
36
  void add_b(struct Value *v) {
37
38
    v->val.real += b.val.real;
39 }
```

The union itself is a standard tagged union. But we want to see what happens when we actually use the union. Since variables can be added in *BASIC*, I've added an add_vars function that does just that. To keep this simple, it assumes that the values passed in are of the same type (i.e. both floats's or both int's.).

BASIC has some built in constants (PI, for example). To simulate this, I've declared two constant values, an int and a float. I've also added two functions that take in a variable of the appropriate type and add one of these cosntants to it.

So what does this generate? I've used Matt Godbolt's excellent online tool to test the results. The code was compiled with the -O2 optimization turned on. I've liberally commented the code.

A word about the x86_64 ABI. The compiler here is gcc, so the SystemV AMD64 ABI is used here, which is discussed in this Wikipedia article. In a nutshell, the first six integer parameters to a function are passed in RDI, RSI, RDX, RCX, R8, and R9, while XMM0, XMM1, XMM2, XMM3, XMM4, XMM5, XMM6 and XMM7 are used for the first floating point arguments. Additional arguments are passed on the stack. Integer return values upto 64 bits are returned in RAX.

```
; The add_vars function
  ; The parameter 'a' is passed in rdi
  ; The parameter 'b' is passed in rsi
  add_vars:
          ; Store parameter b's type in eax
                  eax, DWORD PTR [rsi]
          ; Store parameter a's value in edx
                 edx, DWORD PTR [rdi+4]
          ; Store parameter b's value in ecx
                  ecx, DWORD PTR [rsi+4]
          mov
          ; Check if b's type is INT
11
          test
                  eax, eax
          jne
                  .L2
13
          ; If b is an INT, then add b's value to a's value using
      integer
          ; addition, and store the result in edx
          add
16
                 edx, ecx
          ; Move edx into the upper 32 bits of rdx, clearing the
17
      lower 32
```

```
; bits of rdx
18
          sal rdx, 32
19
          ; Store b's type in the lower 32 bits of rdx
20
                  rax, rdx
21
          ; At this point, rax contains the entire contents of the
22
      union.
          ; The upper 32 bits contain the value, and the lower 32
      bits
          ; contain the type.
25
           ; Return the result in rax
26
  ; If b is a REAL, then add b's value to a's value using floating
      point
  ; addition
  .L2:
29
          ; Store a's value in xmm0
30
          movd xmm0, edx
          ; Store b's value in xmm1
32
33
          movd xmm1, ecx
          ; Add a's value to b's value, storing the result in xmm0
34
35
          addss xmm0, xmm1
          ; Move the result into edx
36
          movd edx, xmm0
37
          ; Move edx into the upper 32 bits of rdx, clearing the
38
      lower 32
          ; bits of rdx
39
          sal
                 rdx, 32
40
          ; Store b's type in the lower 32 bits of rdx
41
42
          or
                  rax, rdx
          ; At this point, rax contains the entire contents of the
43
          ; The upper 32 bits contain the value, and the lower 32
44
          ; contain the type.
45
          ; Return the result in rax
46
47
48
  ; The add_a function
50 add_a:
51
           ; The constant 'a' is 42, so just add 42 to 'a' and return
                 DWORD PTR [rdi+4], 42
          add
53
          ret
54
55 ; The add_b function
  add_b:
56
           ; Load the value of the constant 'b' into {\tt xmm0}
57
          movss xmm0, DWORD PTR .LC0[rip]
58
          ; Add the value of 'a' to 'b'
59
           ; Remember, the union 'a' is passed in rdi
60
          addss xmm0, DWORD PTR [rdi+4]
61
          ; Store the result in the back into 'a' and return
62
          movss DWORD PTR [rdi+4], xmm0
63
64
          ret
65
  ; The constant 'b'
67 b:
       .long 1
```

```
1069547520
           .long
69
  ; The constant 'a'
71
72 a:
                    0
           .long
73
74
           .long
                    42
  ; The constant 'b', again!
76
77 .LCO:
           .long
                    1069547520
```

No surprises here, except for the redundant location $LC\theta$ storing the value of b; $add_{-}b$ could just have taken the value of b from the location b.

And now, the C++ code:

```
class Value {
    enum class ValueType {Integer, Real};
    enum ValueType type;
    union {
      int integer;
      float real;
    } val;
    public:
10
    // Constructors
    constexpr Value(int i): type{ValueType::Integer}, val{.integer{i
    constexpr Value(float f): type{ValueType::Real}, val{.real{f}} {
13
    // Getting the value
    int integer() const { return val.integer; }
16
    float real() const { return val.real; }
17
18
    // + operator
19
    Value operator +(const Value& v) const {
20
      switch(v.type) {
21
        case ValueType::Integer:
          return Value(val.integer + v.integer());
23
24
        default:
          return Value(val.real + v.real());
25
26
27
28
    // += operator
29
30
    Value& operator +=(const Value& v) {
      switch(v.type) {
31
        case ValueType::Integer: val.integer += v.integer(); break;
        default: val.real += v.real(); break;
33
34
35
      return *this;
    }
36
  };
37
39 constexpr Value a{42};
40 constexpr Value b{1.5f};
```

```
41
42
43
44
44
45
46
47
48
48
49
50
void add_a(Value& v) {
    v += a;
}

void add_b(Value& v) {
    v += b;
}

void add_b(Value& v) {
    v += b;
}
```

What does this C++ code generate? Take a look:

```
add_vars(Value const&, Value const&):
             eax, DWORD PTR [rsi]
    mov
    mov
             ecx, DWORD PTR [rdi+4]
             edx, DWORD PTR [rsi+4]
    mov
    test
             eax, eax
             .L2
    jne
    add
             edx, ecx
             rdx, 32
    sal
             rax, rdx
    or
    ret
11
  .L2:
             xmm0, edx
12
    movd
             xmm1, ecx
13
    movd
             eax, 1
    mov
14
    addss
             xmm0, xmm1
             edx, xmm0 rdx, 32
    movd
16
    sal
17
             rax, rdx
18
    or
    ret
19
  add_a(Value&):
             DWORD PTR [rdi+4], 42
21
    add
22
    ret
23 add_b(Value&):
           xmmO, DWORD PTR .LCO[rip]
    movss
             xmm0, DWORD PTR [rdi+4]
25
    addss
             DWORD PTR [rdi+4], xmm0
    movss
26
27
  .LCO:
28
             1069547520
    .long
```

The assembly generated from the C++ source is virtually identical! So much for "C++ bloat", eh?

Ok, so if the C++ code generates identical assembly to C code, then what advantages has implementing the union in C++ given me? Let's see.

Take a look at how constants are defined in the two regimes. In C, we do this:

```
CONST_INT(a, 42);
CONST_REAL(b, 1.5);
```

That is just ugly; opaque #define's getting in the way of code clarity. In contrast, in C+++, we do this:

```
constexpr Value a{42};
constexpr Value b{1.5f};
```

Pretty transparent, I think. We don't even have to specify the type of the constant being declared, as we had to in C; the compiler deduces the type, thanks to the two constructors we've put in for the Value class.

Next, adding two values. In C's add function, we had to do the right thing depending on the union's type tag:

```
struct Value add(const struct Value *a, const struct Value *b) {
    struct Value ret;
    ret.type = b->type;
    switch(b->type) {
        case INT:
        ret.val.integer = a->val.integer + b->val.integer;
        break;
    default:
        ret.val.real = a->val.real + b->val.real;
        break;
}

return ret;
}
```

This is a lot of noise. Look at how we add two values in the C++ version:

```
Value add(const Value& a, const Value& b) {
   return a+b;
}
```

Add means Add, and nothing else! All the noise is pushed into the implementation of the Value class, so that the user of the class doesn't have to bother with it. You could argue that the add function in C is doing the same thing, i.e. hiding implementation behind an API. But in C++, I don't even need the add function; I only added it here to show you the assembly generated by adding two values. In C++, I can simply use the + operator to add two values.

So in conclusion, a modern compiler will give you a tight implementation of your code, and by using C++, you can achive all the code clarity that using domain specific classes (in this case, the Value class) gives you.