

Packet Header Information

Packet Counter	Time stamp	Epoch Second	PPS Count	Local IP	Destination IP	No of Antenna	No of Channel	Packet Order	RCS App	RCS Lib	Error Flags	Reserved
64 bits	64 bits	32 bits	32 bits	32 bits	32 bits	16 bits	16 bits	16 bits	32 bits	32 bits	64 bits	128-16-16
Total 512 bits												

- Packet Counter : Start from zero after reset
- Timestamp: 400MHz clock counter. Start from zero after reset.
- Epoch Second: Second to Epoch Unix time. Transfer from PC
- PPS Count: 1pps pulse. Start from zero after reset
- Local IP: IP if this FPGA 100G Ethernet. Read and write by PC.
- Destination IP:
- No. of Antenna: Total antenna of the system
- No. of Channel: Total channel of the spectrum.
- Packet Order: Order no. If a spectrum divided by 8 packets, this number will be 0 -7.
- RCS App and RCS Lib: Bitcode version control.
- Error Flags: For Overflow and Underflow flags.