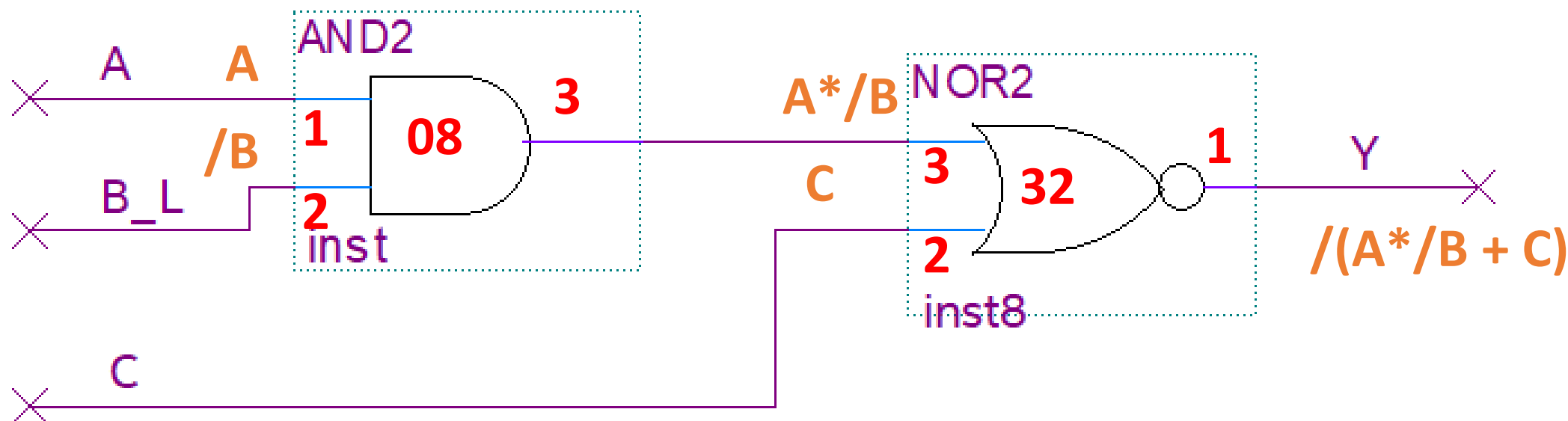






$$Y = \neg(A * \neg B + C)$$



A		INPUT VCC
B_L		INPUT VCC
C		INPUT VCC

OUTPUT  Y

