Department of Electrical & Computer Engineering

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Using a Bus in Quartus Block Diagram/Schematic (bdf) Files

In Quartus schematic entry (bdf) files you can use busses. There is a thick wire symbol, circled in red in Figure 1, which denotes a bus. Several example bus label are shown in Figure 2, e.g., X[3..0], X[2..0], Y[3..0], etc.

Figure 2 also shows how a single bus can be broken up into multiple parts. For example the signal X (X[3..0]) appears as X[2..0] and X[3] in various places in the block diagram/schematic (bdf) file. Finally, Figure 2 also shows an LPM (library of parameterized modules) of a variable-sized OR gate. (Note that other LPM are also available in Quartus.)



Figure 1: Bus symbol circled in red.

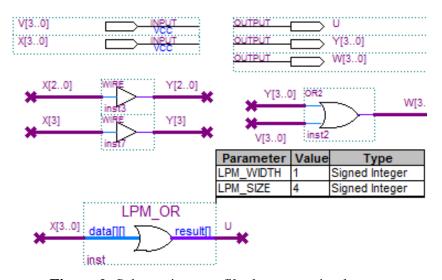


Figure 2: Schematic entry file demonstrating buses.