



Kazi A. Asif Fuad

Machine Learning Researcher
Computer Vision Researcher

- August 22, 1992
- House 51, Block E, Road G-11, South Banasree, 1219 Dhaka (BD)
- +880 1731413140
- <https://asifahmedfuad.github.io>
- asif.ahmed.fuad@gmail.com
- asif-fuad
- asifahmedfuad

Hard Skills

Python	● ● ● ● ●
C/C++	● ● ● ● ●
MATLAB	● ● ● ● ●
Machine Learning	● ● ● ● ●
Deep Learning	● ● ● ● ●
OpenCV	● ● ● ● ●
PyTorch	● ● ● ● ●
TensorFlow 2.0	● ● ● ● ●
Keras	● ● ● ● ●
HTML, CSS, Javascript	● ● ● ● ●
QT (C++)	● ● ● ● ●
VHDL, SystemVerilog	● ● ● ● ●

and relevant softwares.

Soft Skills

Team Management	● ● ● ● ●
Organising Event	● ● ● ● ●

Languages

Bangla	● ● ● ● ●
English	● ● ● ● ●

Field of Interests

Computer Vision, Machine Learning, Deep Learning, Explainable AI, Augmented, Virtual and Mixed Reality, Predictive Systems

Experience

2020 – 2020	Master Thesis Intern LaBRI
	Master Thesis on "Explainable Artificial Intelligence"
2019 – 2019	Openlab Summer Student CERN openlab
	Research on "Graph Neural Network (GNN) Inference on FPGAs"
2016 – Present	Lecturer American International University-Bangladesh(AIUB)
	Responsible for taking classes of undergraduate students on Analog and Digital Electronic Courses
2014 – 2016	Teaching Assistant American International University-Bangladesh(AIUB)
	Responsible for assisting teachers to take laboratory classes of final year undergraduate students

Education

2018 – 2020	Erasmus+Mundus Joint Master Degree on Image Processing and Computer Vision (IPCV) UB,UAM,PPCU
	Focus: Advanced Image processing and Computer Vision Algorithms and Applications
	Tutored Research Development Project UB,UAM
	Multi-Camera Object Detection and Association
	Master Thesis LaBRI
	Recognition of Sport Gestures with 3D Deep CNNs: Explanation of Networks
2014–2015	MSc. in Electrical and Electronic Engineering AIUB
	Focus: Digital Hardware Design, Microelectronics and VLSI Design
	Master Thesis AIUB
	Area-Efficient FPGA Realization of Edge Detectors by Varying Sample-Widths and Gradient Operators and Utilizing Software-Hardware Co-Design
2009–2013	BSc. in Electrical and Electronic Engineering AIUB
	Focus: Electronic and Power Engineering
	Bachelor Thesis AIUB
	Designing and Implementation of Centralized Load Controlled Automated Power System Network-CLCAPSN

Publications

2020	Features understanding in 3D CNN for actions recognition in video[Accepted]	IPTA-2020
2018	Watch IT Version-II: An Assistive Device for Hearing and Speaking Impaired	SAIISC
2017	Automated anti-collision system for automobiles	ECCE
2015	Hardware Software Co-Simulation of Canny Edge Detection Algorithm	IJCA
2015	Varying Sample-Width to Realize Area-Efficient FPGA Realization of Sobel-Fieldman Edge Detector	AJSE
2015	Hardware/Software Co-Simulation of Gradient-based Edge Detectors: A Comparative Study	STM JoIPPRP
2015	Design and simulation of centralized load controlled automated power system network(CLCAPSN)	WIECON-ECE 2015

Certifications

2019	Neural Networks and Deep Learning	deeplearning.ai, Coursera
	Credential ID: D8ECHRWRH2UN	
2019	Machine Learning	Stanford University, Coursera
	Credential ID: LK4WHUY7PW7D	
2019	Machine Learning with Python	IBM, Coursera
	Credential ID: DJMB9242WT5P	

Honours and Scholarship

2018 – 2020	Erasmus+Mundus Joint Master Scholarship	IPCV, European Union
2016	Summa Cum Laude, MSc.	AIUB
2016	Vice-Chancellor's Best Master Thesis Award	AIUB
2014	Vice-Chancellor's Best Bachelor Thesis Award	AIUB

Extra-Curricular Activities

2019 – 2020	Student Representative, IPCV Advisory Board
2016 – 2018	Motivator, IEEE AIUB Student Branch
2018	Vice-Chair(Technical), IEEE Young Professionals Bangladesh