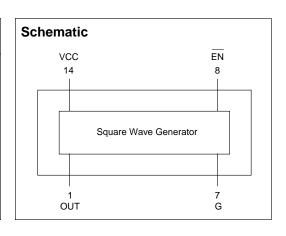
## 14 Pin DIP TTL Square-Wave Generator

## **Features**

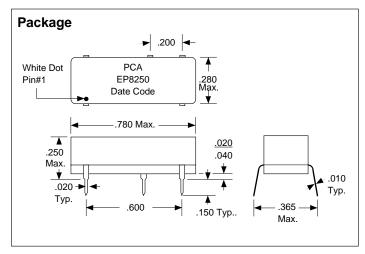
- Output frequencies from 2 to 100 MHz ±5%
- Low Profile 14 pin dual-in-line package
- Operating temperature range 0 to 70 °C
- Output synchronized using enable line
- Inherent Delay 4nS Typ.
- 50% duty cycle
- Schottky TTL

PART NUMBER	MHz ±5%	PART NUMBER	MHz ±5%
EP8250	2	EP8259	35
EP8251	3	EP8260	40
EP8252	4	EP8261	45
EP8253	5	EP8262	50
EP8254	10	EP8263	60
EP8255	15	EP8264	70
EP8256	20	EP8265	80
EP8257	25	EP8266	90
EP8258	30	EP8267	100

DC Electrical Characteristics								
	Parameter	Test Conditions	Min	Max	Unit			
VOH	High-Level Output Voltage	V <sub>CC</sub> = min. V <sub>II</sub> = max. I <sub>OH</sub> = max	2.7		V			
VOL	Low-Level Output Voltage	VCC= min. VIH= min. IOL= max		0.5	V			
VIK	Input Clamp Voltage	VCC= min. II= IIK		-1.2V	V			
ΙΗ	High-Level Input Current	$V_{CC}$ = max. $V_{IN}$ = 2.7 $V$		50	μΑ			
		V <sub>CC</sub> = max. V <sub>IN</sub> = 5.25V		1.0	mΑ			
Ι <sub>Ι</sub> L	Low-Level Input Current	$V_{CC} = \text{max. } V_{IN} = 0.5V$		-2	mΑ			
los	Short Circuit Output Current	VCC= max. VOUT = 0.	-40	-100				
ICCH	High-Level Supply Current	V <sub>CC</sub> = max. V <sub>IN</sub> = OPEN		75	mΑ			
ICCL	Low-Level Supply Current	$V_{CC} = max. V_{IN} = 0$		75	mΑ			
T <sub>RO</sub>	Output Rise Time	V <sub>CC</sub> = 5.0V		4	nS			
NH	Fanout High-Level Output	$V_{CC} = max. V_{OH} = 2.7V$		20 TTI	LOAD			
NL	Fanout Low-Level Output	$V_{CC} = max. V_{OL} = 0.5V$		10 TTI	LOAD			



	mmended ating Conditions	Min	Max	Unit
VCC VIH VIL IIK IOH IOL d	Supply Voltage High-Level Input Voltage Low-Level Input Voltage Input Clamp Current High-Level Output Current Low-Level Output Current Duty Cycle Operating Free-Air Temperature	4.75 2.0 45 0	5.25 0.8 -18 -1.0 20 55 +70	V V V mA mA mA °C



DSD82XXb 8/25/94

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