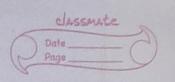
		11 Fav On object
	(build)	The Clarest Anna Control - Trace
	Build (connect)	alau mysching extens
	Phases (end-of-elaboration))
	start-of-simulation	
		pre-reset
	Managara and area or and the	(reset)
	A continue of the continue of	(post_reset)
	- i dolfners toolde	pre-cordigue
	Run	(configure)
	Phales run	post-configure
		(pre_main)
	compensation of training	main main
((52))	grown arma. (ce grains grana.	
fra	namos a por m	pre-shirtelown
	I Fare the minor than the property	anthort Shirtclawn
		(post-shittdown)
1010	(extract)	ivationary has tradocares)
	Cleany parodo and martin	note to the section of the section of
entre	Phase (report)	= transporter une
4 2 h. 1	transport ver mile (final)	

	only run phase is touk and other all is function dissuate
9/	why connect is bottom up while build is top bottom. Date page
	when you call run phase which phase start first and how invoke test.
91	
	1 1 2 2 2 2 1 1 2 2 2 2 2 2 2 2 2 2 2 2
1.	Build phasel - where the textbench is configured and
	n habour moconstructed living
2	Run-time phases - where time is consumed in running
	the testcare on the testbench
. 3.	Clean up phases - where the results of the test care are
	collected and reported.
	water mitternit over photherm starte littled soft IIA
	-: starting vum Phase Execution :-
	This rivers are and by the same of the sam
	To start a UVM textbench, the run_text() method has to be
	called from the static part of the textbench. It is isrially
	called from within an initial block in the top level module
	of the testbench.
	most proposed transamos doubt bet attended to
	calling run test() construct the UVM environment root
	component and then thit later the uum phasing
	as bronglesh at transgram done to writnestones out!
9	The run test 1 method can be paved a string argument to
	define the detaut type name of an lum component
	derived clay which is used ay the root node of the
	tertbench hierarchy, og and and blind att paired
	The second was the second of t
A	However, the run-text() method checks for a command line
	plusary called UVM-TESTHAME and wer that plusary
	string to lookup a factory registered uum component,
	everydling dary default type name
	The same same days 4 for a
	Usim +b-top + UVM_TESTNAME = my-test
	think that sandared phranelid transported decident alt
	through phromaid att to control and most

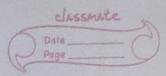
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	Date Page
and the	even over her with health shorte shorte short and this your more
-	CAN CHEL THE THE PARTY PARTY TO SELECT TO THE
- 1.	Build Phaser : - Market with a round of the land of th
	The build phases are executed at
	the start of the uum textbench similation and
	the start of the out to construct, configuration
	their overall purpose is to construct, configuration,
	and connect the testbench component hierarchy.
	beleasare bus patrallar
-	All the build phase methods are functions and
1	therefore execute in zero simulation time.
1-	
1 st at 1	Build :- a Otist and soft donathist MULL of tiple of
	Once the UVM textbench root hade component is
	constructed, the build phase starts to execute
-	donastict at to
	It constructs the textbench component hierarchy from
-1	the totopovidownwardy, set tourteness (state and million
-	component and then itsilates the same prenage
	The construction of each component is derferred so
	that each layer in the component hierarchy can be
	Configured by the level above things alter and the
	slevived place with is used as the root made col
1-	During the build phase unm-components are inlinectly
	constructed using the UUM factory.
- sell !	Homeway in val 23/29/2 haddand () first new all varioust .
Egy/	Connect :- MALL Som JMAKTESTI MALL MALLA OVACULAL
	The connect phase is wed to make TLM
	connections how components or to origin handles to
	textbench resources.
	It has to occur after the build method has put
	the textbench component hierarchy in place and works
-	from the bottom of the hierarchy upwards
	The state of the s



	end-of-elaboration:
	The end of elaboration phase is used to
	make any final adjustments to the structure, configuration
	or connectivity of the textbench before simulation starts.
	AND THE PERSON OF THE PERSON O
	4s implementation can assume that the testbench component
	hierarchy and inter-connectivity is in place.
	secondimite and not have of home and mathetimis
	This phase executed bottom up.
2	Run Time Phase :- make the manufacture of standard of
	The textbench stimulus is generated and
	executed during the run time phases which follow the
	builds phase setmon has united to low mithought
	After the start of simulation phase, the UVM executes the
	run phases and the phases pre-reset through to post
	shutdown in parallel.
	Run phase is a phase that transaction will we.
	The other phasel were added to the oum to give finer
	runtime phase granularity for tests, scoreboard and other
	similar components
	It is expected that most tatbenches will only we reset,
	configure, main and shutdown and not their pre and
	post variants.
	the known strangeros video has interest movies .
	Start of simulation & 1910/19 31944 Howaldon
	The start of simulation phase is a
	function which occurs before the time consuming part
	of the textbench begins.
	Waster Would be made and Delegance at the



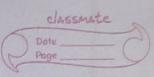
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atheres has	It is intended to be used for displaying bannon; testbench topology; or condiguration information. It is called in bottom up order.
-hongrand	The run phase occurs after the start of simulation phase and is used for the stimulus generation and checking activities of the testbench.
· long 6	The run phase is implemented as a task, all two- component run-phase cy tasks are executed in parallel.
	Transaction such as drivers and monitor will nearly
	always we this phase asimporis to trate at sotta
	Pavallel Run-Time Phayer: - lallowing orun-time
+ther	phasel execute in-order in parallel with the
- hat	there phase should only be called from the text and the enu to start sequences
•	privers, monitors and other components should not implement there phases.
	The stand of simulation plants and the simulation of simulations of the simulation of simulations of the simulation of t



(i) pre-relet: -The pre-reset phase starts at all same time as the run phase. Its purpose is to take care of any activity that should occur before reset, such as waiting for a power-good signal to go notive. The fort and 2. reset: The reset phase is reserved for DUT or Interface specific reset behaviour eg; the phase would be wed to generate a reset and to put an interface into its default istates that whom and troops of The postureset phase is intended for my activity required immediately following reset. This might include training or rate hegotiation behaviour. 4. pre contigure: The pre-configure phase is intended for anything that is required to prepare for the DUT's configuration process after reset is completed, such as waiting for components (e.g. drivers) required for configuration to complete training and/or rate negotiation. It may also be used to program the DUT and any memories in the textbench so that it is reading It may also be used to program the DUT and any as a last chance to modify the information described by

the test/environment to be uploaded to the DUT.

Configure :-The configure phase is wed to program the but and any memories in the textbench so that it is ready for the start of the tett care. It can also! be used to set signal, to a state ready for the test case stant. 6. post configure: The post configure is well to wait for the effect of configuration to propagate through the Dut, or for it to reach a state where it is ready to start the main test stimuly. 7. main: This is the where stimulus specified by the text care is generated and applied to Dut. It complete when either all stimuly is expanded or a timent occur. Most data throughout will be handled by sequences started in the phase. A. Shutdown 3- massa st brillion the Shutdown phase is weet to ensure that the effect of the stimulus generated durings the main phase have propagated through the but and that any resultant data has drained away. . It might also be wed to execute time consuming segmences that read status registers.



	Date_Page
2	·Clean Up Phase 3-
	Ment of the post of the state o
	extraction when I have a second the second t
	The extract phase is used to retrieve and
	process information from scoreboards and functional
	coverage of monitors winds
	coverage of manitors (2000) Votimental 1 2000
	This may include the calculation of statistical Information
	wed by the report phase. This phase is usually wed by
	analysis components.
	Julius Control of the
	check: - me man more in instrongtho substitutely man
	The check phase is used to check that the
	Dut behaved correctly and to identify any errors that
	may have occurred during the execution of the tertbeach.
	This phase is usually used top analysis components.
	14 1 man on for some a waithy to be a column to the column
	report :- 1+mon dans slitus transports would
	The report phase is used to display the results
	of the simulation or to write the results to file. This
	phase is usually used by analysis components
	A AMERICA CALLED AND AND AND AND AND AND AND AND AND AN
	final &
	The final phase iso used to complete any other
	outstanding actions that the textbench has not already
	completed.
	which is a suite of a suite of day
	stimu vast ne vatinger approved das