## UUM- Day 26

Constructing Register Model > Register Field:

Register field are declared in register

clau with num\_reg\_field.

The acceu policy of a field is specified ming the uvm-reg field: configure() method.

Configure method how to be called from the build () method of the register that instantiated it

map () Method:

A virtual maps) function, with um-reg-map and address offset arguments map() method shall call num reg map: add regul for all register class properties. The map() method may call the add hall-path() method for all register / register file class properties.

set\_offset Method:-

design as Awirtual set offset of function, with a un reg map and actories offset arguments, may also implemented The set offset() method shall can the set offset() method for all register and register file class properties.

Memory Type: -

A memory type is constructed using a class extended from the youn ment clays of the

The name of the memory type clay must be unique within the scope of its declaration.

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Packaging and Integration Register Model: Packaging a Register Model > The following practices are recommended but not required · Block type should be located in separate packages Block types a header file, with all the required import statements to we the register model, should be generated. · A lengthy build() method may be split into several, shorter sub-methods. The sub-methods shall be declared local and ealled by the build () methods and has Integrating a Register Model > : wd cpor 199 MUU A register model must be integrated with the pow agent of whitoparoxt . The integration with the but agent must only be done on root blocks. · Root blocks model the entire DUT and they are only ones who have accell to and knowledge of the externally - visible address maps i.e in the environment of the textbench. UUM Register Model Predictor: UVM RAL Predictor predicts the register acceu done through the register model and updates the RAL Model registers. (i) Implicit prediction => Implicit prediction only required the integration of the register model with one or more but sequencer. (ii) Explicit prediction:> Explicit prediction requires the register model to be integrated with both the bus sequences and corresponding by monitors,

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	converts transactions of RAL methods to interface!
	but transactions of hoteral ad hillands 12917 Holls.
	The adapter converts blu register model read, write
	methods and the interface - specific transactions
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