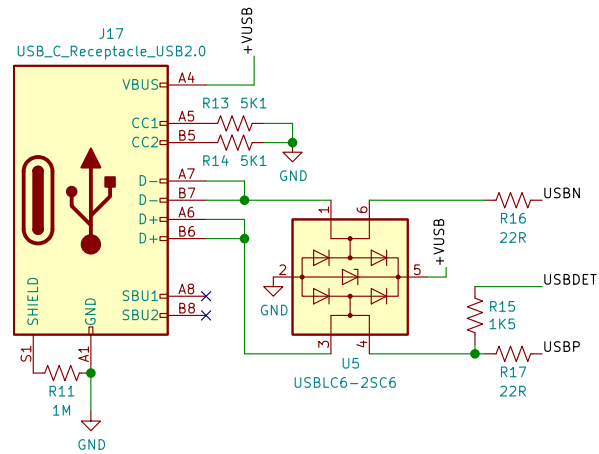
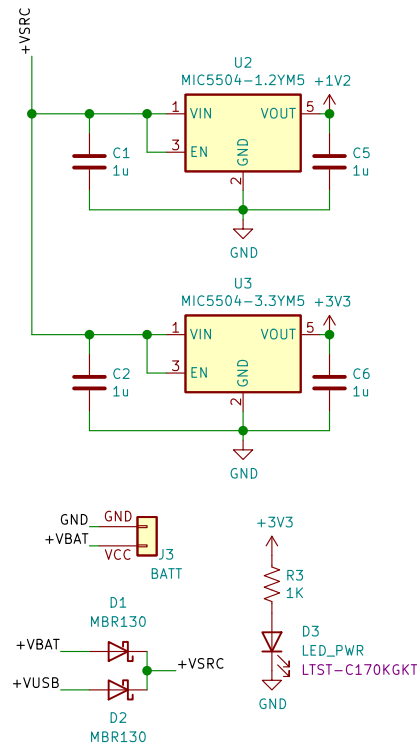


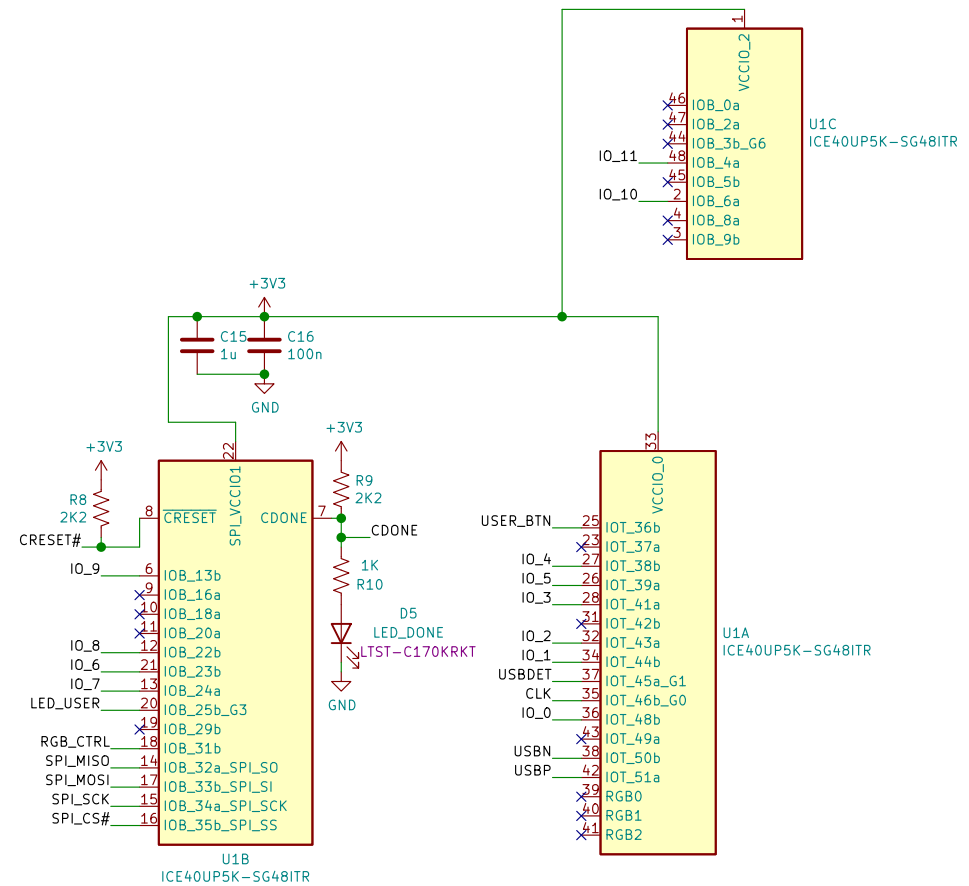
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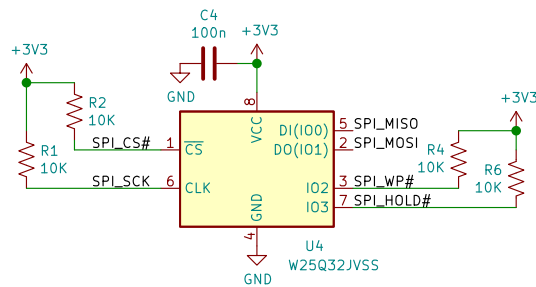
Power Regulators



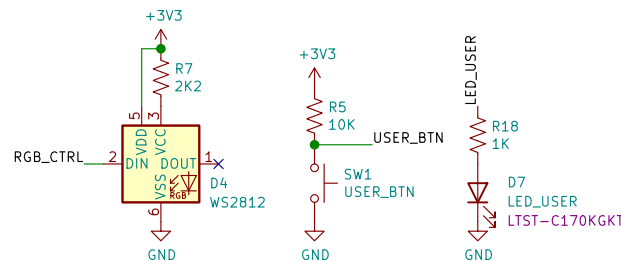
FPGA I/O



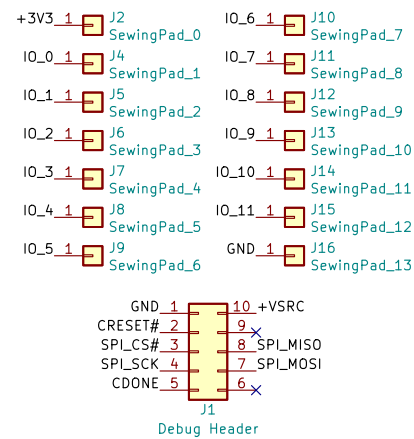
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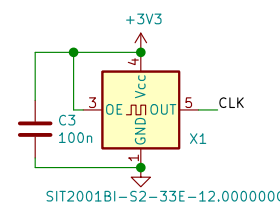
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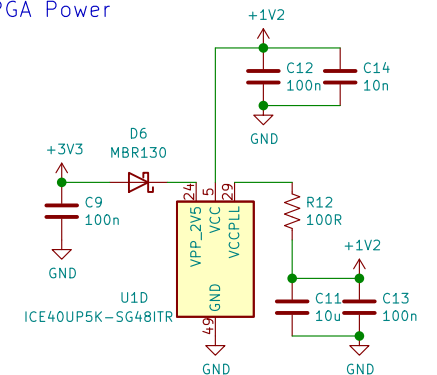
I/O Pads



Oscillator



FPGA Power



Anish Singhani

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