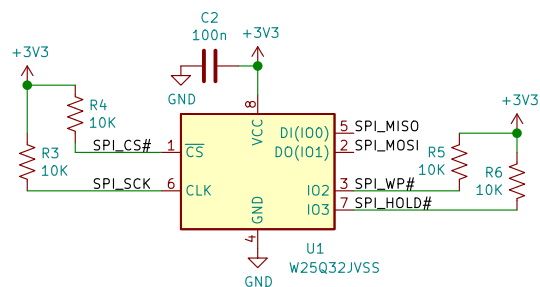
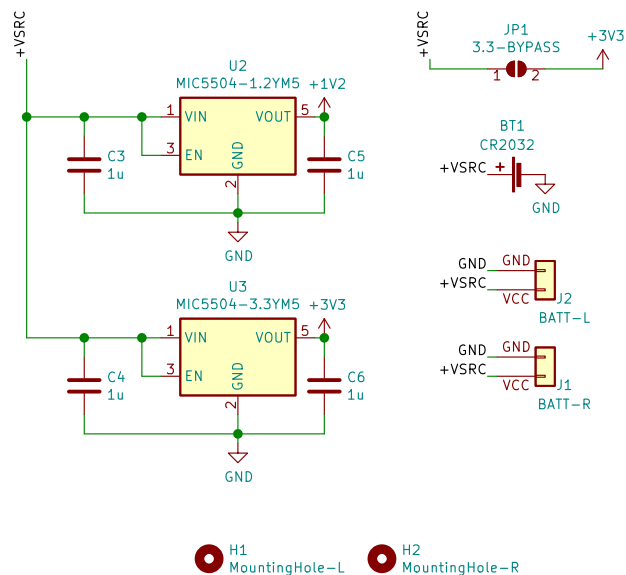


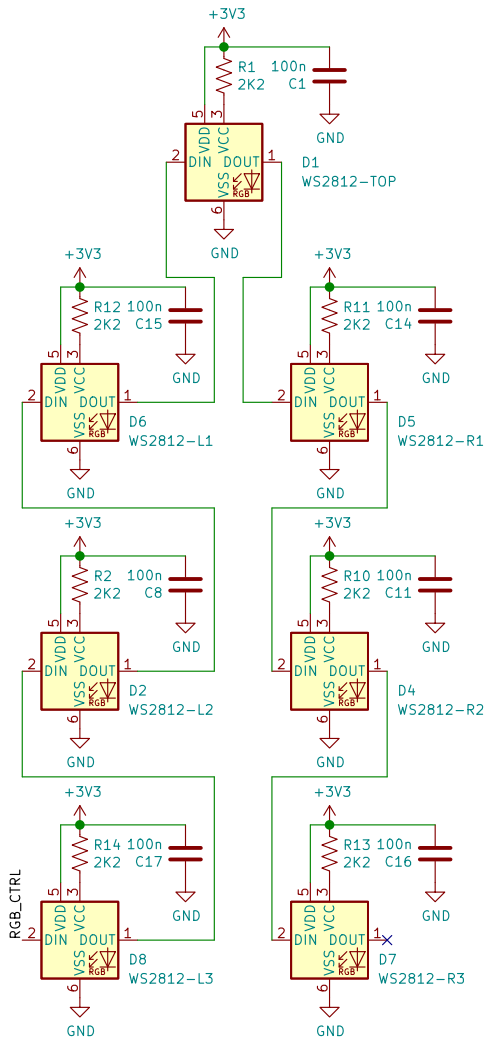
SPI Flash



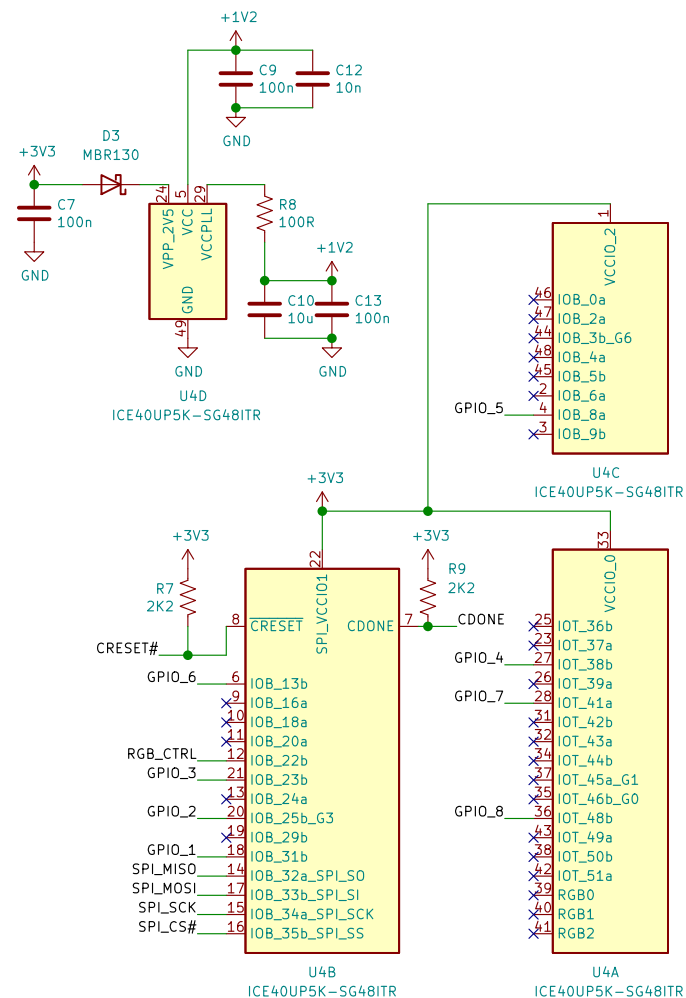
Power Regulators & Battery



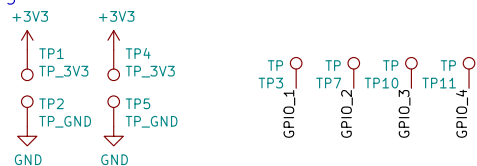
RGB LEDs



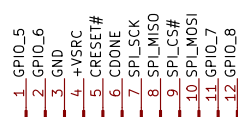
FPGA I/O



I/O and Debug Ports



J3 Debug Header



Sheet: /
File: ice40_catears_v1.sch

Title:

Size: A4	Date:
KiCad E.D.A. kicad (5.1.9-0-10_14)	

Rev:
Id: 1/1