

Combinational circuits' output depends on present input only unescas sequential circuits' output depends on the present input as well as the past output.

&: dequential -> traffic lights negisters, counters

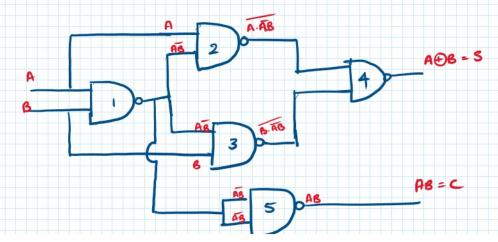
Combinational ->

HALF ADDER



Inputs		Outputs	
Α	В	S	C
0	0	0	0
0	- 1	1	0
t	٥	\ \ \	0
1	1	0	

Half adder using only NAND gates

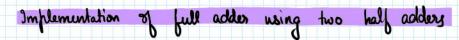


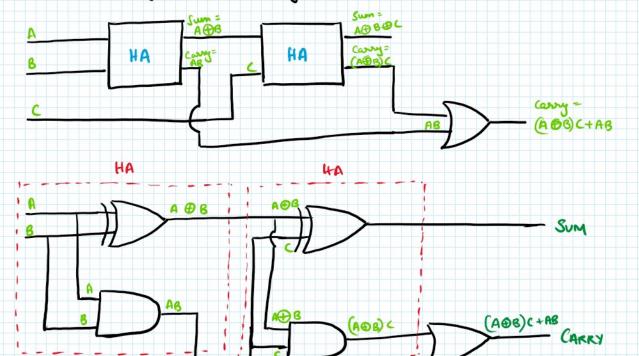


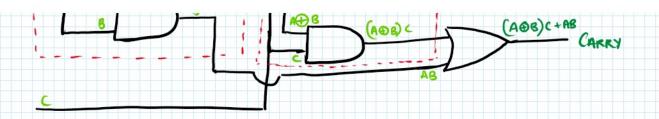
FULL ADDER



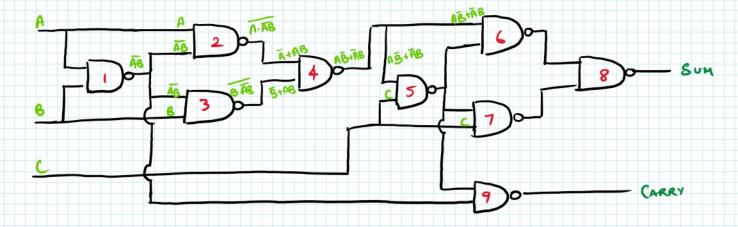
Inputs			Outputs	
X	У	Z	С	S
0	0	0	0	0
0	0	ı	0	1
0	u	0	0	
0	1	1	- 1	0
	0	0	0	1
1	0		ı	0
1	t	0	t	0
	ı		1	1

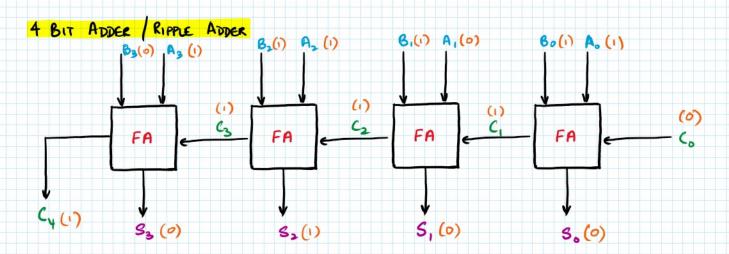






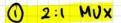
Full adder circuit using only NAND gates

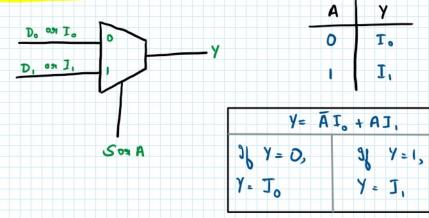




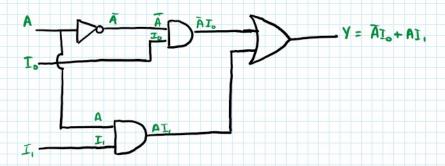
Acts like a token system; condrols which input goes through MUX at any time

MUX Input: Output

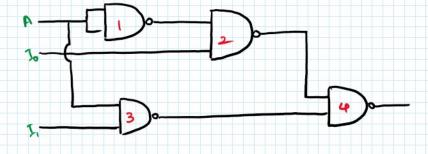




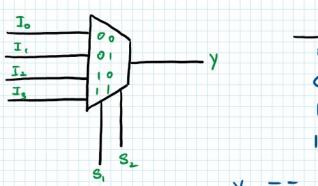
Using basic gates



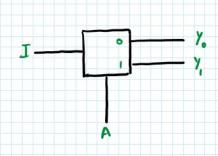
Using NAND gates



2 4:1 MUX



3 1:2 DEMUX

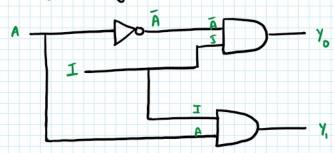


A	4.	γ,
0	I	0
1	0	I

$$Y_o = \overline{A}I$$

 $Y_i = AI$

Using basic gates



Using NAND gates

