### 0.0.1 Qudrature Decoder introduction

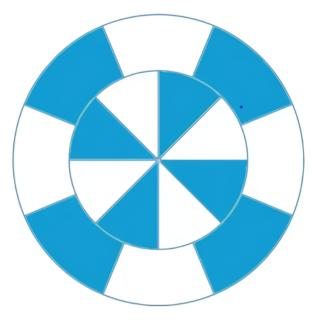
The Quadrature Decoder is used to interprete the position and velocity of the system's motors using the signals from the two enocders, each mounted on motor. This subsection will introduce a little theory of a both Quadrature Decoder and encoder and how the Quadrature decoder has been implementated on the FPGA.

## 0.0.2 Quadrature Encoder

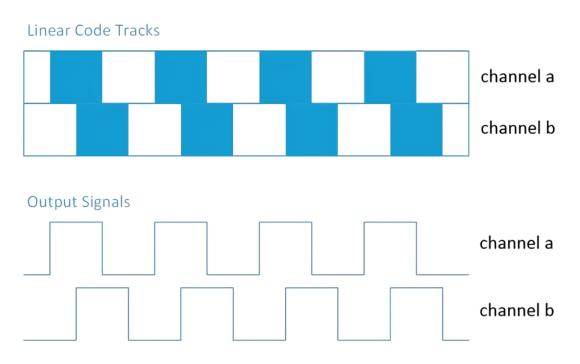
First, in order to understand the implementation of the Quadrature Decoder on the FPGA, a small explanation of how a Quadrature Encoder works is needed.

A Quadrature Encoder uses two channels to sense the position of, typically, a rotating disk/shaft or a linear strip. The disk or strip has two paths on it, positioned 90 degresse out of phase of each other, see figure 1 for a rotary encoder and figure 2 for a strip encoder. Statinoary sensores are typically placed on top of the encoder, so when a track moves in relation to a sensor, it outputs a logic high or low output signal depending on what part of the track is visible to the sensor. An encoder has two output signals, one for each channel, typically called A and B, see figure 2 for a representation of those for both encoder types. This two signals, A and B, is what the decoder uses to determine the position of the encoder.

# Rotary Code Tracks



**Figure 1:** The figure shows the tracks of a rotary encoder.



**Figure 2:** The upper figure a Linear encoder and the lower figure shows the output of both a rotary and linary encoder

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### 0.0.3 Decoder principle

The basic principle of a Quadrature Decoder is for it to decode the two output signals the encoder produces and return a position. The idea is to observe the both the encoders outputs. By counting the transitions from high to low and low to high on just one of the outputs, it can be determined how far the encoder has rotated. However, by adding the second output, the direction of the encoder can be computed as well as adding a higher resolution to the position. The encoder used in the project is a rotary encoder with a resolution of  $360^2$ , which means that a Quadrature Decoder will count 360 "ticks" per shaft turn. This resolution and rotation can not be mapped directly to the system itself as its motors are geared. The motors do three full rotations of their shafts before the system itself has completed a full rotation. Hence the resolution for the system is 1080. This is very useful, since it gives a  $\frac{1}{3}$  of a degrees position precision.

#### 0.0.4 Statemachine

To use the transitions between encoder output A and output B, a truth table of every possible combination of the two singals must be computed. As mentioned in system design it is necessary to include a third signal called "return to home" in order to reset the position counter if a new reference point is desired. The truth table including all three signals is shown in figure 3.

 $<sup>^{1} \</sup>rm https://www.digikey.com/eewiki/pages/viewpage.action?pageId = 62259228 - kilden = 6225928 - kild$ 

 $<sup>^2 \</sup>mathrm{See}$  data sheet x

A_prev	A_new	B_prev	B_new	Reset	Direction	Position
0	1	0	0	1	Forward	+ 1
1	1	0	1	1	Forward	+ 1
1	0	1	1	1	Forward	+ 1
0	0	1	0	1	Forward	+ 1
0	0	0	1	1	Backward	- 1
1	0	0	0	1	Backward	- 1
0	1	1	1	1	Backward	- 1
1	1	1	0	1	Backward	- 1
x	X	x	x	0	No change	0

**Figure 3:** This figure shows the truth table for the Quadrature decoder

The truth table shows nine possible scenarios that can occur from tracking the transistions of the outputs. Four of these results in a forward direction and the counter will increment, four scenarios results in a backwards direction and the counter will decrement. The last scenario occurs if the reset flag is set  $low^3$ , and the counter is set to zero regardless of what it was on before. From these observations a state machine containing 6 states has been computed. The states are: AB\_low with the gray code  $001^4$ , AB\_high with gray code 111, A\_high with gray code 101, B\_high with gray code 011, undefined and reset xx0. The undefined state is purely for debugging purposes used if an error with the signal A and B ocurs. A potential error could be if the state is currently AB\_low the gray code can not be 111 in the next instance. This would mean that both signals changed at the same time. The state machine diagram is shown below in figure ??.

The decoder works by first initialalizing and go to a state depending on the level of signal A and B. The decoder will when use the debounced signals to determine the next state. If the starting state is AB\_low, there is three correct outcomes: the reset signal goes low and the counter will go to zero, the signal A goes high and the state changes to A\_high or the last possible outcome, that does not give an error, is B\_high. By looking at the truth table from the above figure 3, it can be observed that if the state goes from AB\_low to A\_high, the direction will be forward and the counter will increment where the counter would decrement and direction set to backwards if the next state is B\_high.

The counter keeps track of the position of the encoder. The counter counts position zero as the angle the system was in, as the encoder was initialized. It is thereforth considered useful to add a reset function, so a new reference point can be added as desired. The Statemachine approach has been implemented and the process described in section ??

<sup>&</sup>lt;sup>3</sup>Reset is implemented as active low, as can be observed in the truth table 3.

<sup>&</sup>lt;sup>4</sup>The first digit is signal A, second is signal B and thrid is the reset signal

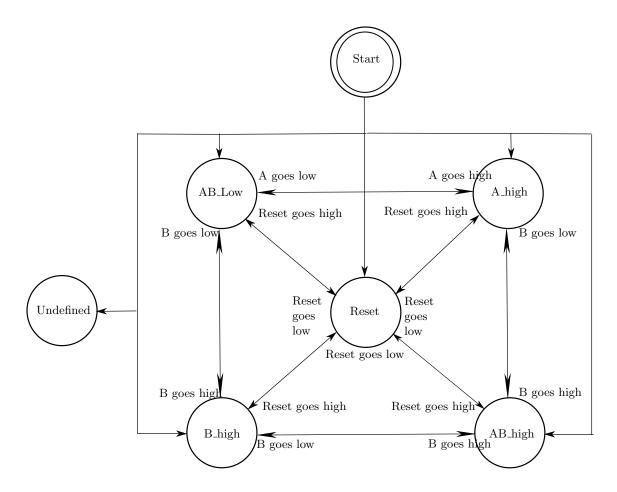


Figure 4

As mentioned above, a reset is needed for the application. This allows for a new reference point to be set as desired. The state machine does not make use of a clock singal so the reset is desired to be a asyncron reset to avoid adding more complications to the application. The reset should be an asyncron reset, because this does not depend on the clock signal.

### 0.0.5 Velocity measure

The decoder is not only responsible for computing the position of the system but also the velocity. The simplest way to estimate the velocity is by diving the change in position with the change in time. The most classic options are either to keep the  $\Delta t$  fixed, measure the position between the interval and then take the difference between x(n) and x(n-1) as shown in equation 0.1. The other is to keep the position fixed and compute how long it takes to reach the fixed  $\Delta x$  like in equation 0.2.

$$v(n) = \frac{x(n) - x(n-1)}{T}$$
 (0.1)

$$v(n) = \frac{X}{t(n) - t(n-1)}$$
(0.2)

The second approach 0.2 is considered the most reliable at slow speeds, which arguably the system does fall under. However, this approach depends on the output to be a "fixed interval pulse train". The system does not fit this criteria, because it generates different

frequency waves depending on the supplied PWM value<sup>5</sup>. The other approach is not hindred by this, so that one has been chosen As the first approach using equation 0.1 is not hindred by the difference in frequency, as long as it does not affect the timer, it has been chosen as the desired appraoach.

As this approach requires a fixed interval one such must be decided. The issue to take into account here is to make sure the interval is neither too short or too long. A too short interval might yield scenario where the velocity seems to be zero even if the system does move. However, on the opposite side, a too long interval could yield a scenario that gives a wrong velocity, if the system changes velocity a lot. If the interval is say 1 second, the velocity at the first 10ms be 50 ticks, but it slows down considerably at the 20ms mark and hardly moves at 70ms mark. This will give an average velocity that might not be as useful and downright misleading as it shows the system to have travelled with a low speed instead of a showing that it was fast at first but slow towards the end. In this case it is important to the system speed up and slow down, so a low interval time of 10ms has been chosen.

<sup>&</sup>lt;sup>5</sup>https://www.embeddedrelated.com/showarticle/158.php