
0.0.1 Softwaredesign of a PI controller

In order to implement the aforementioned PI-controller in a fashion that complies with API the provided by FreeRTOS, it is chosen that a single interface is to be designed. This is to avoid writting multiple sub-programs with the same functionality, and thus it requieres to be generic if multiple PI-controllers are to be implemented.

Omskriv sætning, meget mærkelig

To meet the given requirements the following design criteria must be fulfilled.

- Must comply with FreeRTOS's API
- Must be re-entrant
- Must be able to be initialized from main
- Must be generic

In order to write a routine that will act as the PI-controller, it must be wrapped into a never ending loop. In this the desired calculations will be done. This is done to meet the first criteria and is achieved in listing 1.

Listing 1: *Function body*

```
1 void tsk_PI(void* pvArgs);
2
3 void tsk_PI(void* pvArgs)
4 {
5     PI controller = *((PI*)pvArgs);           //use pvArgs to set up the PI
6     controller.conditional_anti = 1;
7     int16_t error = 0;
8
9     for (;;)
10    {
11        error = calculate_error_PI(controller.ref, controller.measured_y_n, &controller );
12        calculate_PI( error, &controller );
13
14        vTaskDelay(pdMS_TO_TICKS(100));
```

tsk_PI() will be the argument to the function by FreeRTOS, that creates a task. It is noticed that if the functions; calculate_error_PI() and calculate_P() are assumed to be re-entrant. Then all of the above criteria are achieved. The third and fourth criteria are achieved by passing the struct PI from the function task_PI().

The struct will contain all the necessary gains and data-samples, which a PI-controller will need in order to operate. The struct members can be seen in listing 2.

Listing 2: *struct definition*

```
1 typedef struct PI_dif_eq
2 {
3     int16_t x_n[3];
4     float y_n[3];
5     float kp;
6     float ki;
7     float sample_rate;
8     int16_t saturation;
9     int16_t* ref;
10    int16_t* PWMout;
11    int16_t* measured_y_n;
```

```

12     int16_t* clamp_ref;
13     uint8_t conditional_anti;
14 }PI;

```

Since the design requirements are met, the next step is to implement the functionality of a PI-controller, which has the following requirements:

- Must be implemented as a difference equation
- Must include anti-windup

The reasoning for it to be implemented as a difference equation, is due to it being an exact relationship between current and past samples.

0.0.2 Deriving the PI difference equation

In order to obtain the difference equation for the PI-controller, the standard PI equation in the s-domain is observed:

$$u(s) = \left(k_p + \frac{k_i}{s} \right) \cdot e(s) \quad (0.1)$$

To get the PI equation into the time-discrete z-domain, it is chosen to use Tustin's Method. This is done by substituting s with the following

$$s = \frac{2}{T} \cdot \left(\frac{z-1}{z+1} \right)$$

Equation (0.1) now becomes

$$u(z) = \left(k_p + k_i \cdot \frac{T}{2} \frac{z+1}{z-1} \right) \cdot e(z)$$

Now the expression on the right side is refactored

$$u(z) = \left(\frac{k_p \cdot 2(z-1) + k_i \cdot T \cdot (z+1)}{2 \cdot (z-1)} \right) \cdot e(z)$$

In order to have the discrete equation in terms of previous samples, both sides of the fraction are divided by z^{-1}

$$u(z) = \left(\frac{k_p \cdot (1 - z^{-1}) + k_i \cdot \frac{T}{2} \cdot (1 + z^{-1})}{(1 - z^{-1})} \right) \cdot e(z)$$

It is wished to get an expressions in terms of $u(z)$ and $e(z)$ with no non-constant-fractions, therefore both sides are multiplied by $(1 - z^{-1})$

$$u(z) \cdot (1 - z^{-1}) = \left(k_p \cdot (z-1) + k_i \cdot \frac{T}{2} \cdot (z+1) \right) \cdot e(z)$$

The inverse z-transformation is now performed on both sides

$$u[n] - u[n-1] = k_p \cdot (e[n] - e[n-1]) + k_i \cdot \frac{T}{2} (e[n] + e[n-1])$$

The final difference equation becomes

$$u[n] = u[n-1] + k_p \cdot (e[n] - e[n-1]) + k_i \cdot \frac{T}{2} (e[n] + e[n-1])$$

0.0.3 Implementation of the difference equation

With the software design chosen and the difference equation derived, the next step is to implement it. This is done in the following code snippet, which includes anti-windup whilst having the PI-controller expressed as a difference equation.

```
1 void calculate_PI(int16_t error, PI* K)
2 {
3     int16_t tmp_output = 0;
4     K->x_n[2] = K->x_n[1];
5     K->x_n[1] = K->x_n[0];
6     K->x_n[0] = error;
7
8     K->y_n[2] = K->y_n[1];
9     K->y_n[1] = K->y_n[0];
10    K->y_n[0] = K->y_n[1] + ( K->kp * (K->x_n[0] - K->x_n[1]) +
11      ( K->conditional_anti * K->ki * K->sample_rate / 2) * (K->x_n[0] + K->x_n[1]) );
12    K->conditional_anti = 1;
13
14    tmp_output = K->y_n[0];
15
16    if(tmp_output >= K->saturation)
17    {
18        tmp_output = K->saturation;
19        K->conditional_anti = 0;
20    }
21    else
22    {
23        if (tmp_output <= -K->saturation)
24        {
25            tmp_output = -K->saturation;
26            K->conditional_anti = 0;
27        }
28    }
29
30    *K->PWM_out = tmp_output;
31 }
```

Lyder lidt som en tale fra en diktator - With the Software-design completed the Implementation with anti-windup, is shown is ref??