0.1 Definition

Full duplex, 2MHz. Hej med dig

Tiva	**Datatype**
Velocity/PWM	9 bit signed
FPGA	
Position	12 bit signed
Velocity	12 bit signed
Amps	12 bit unsigned
Index	1 bit
Reserved for future use	3 bit

Tabel 1: *Datatypes*

Tabel 2: *Package format - Tiva*

0 8	9			12
Velocity M0 - 9bits	R P 0	R V 0	R A 0	R I 0
Velocity M1 - 9bits	R P 1	R V 1	R A 1	R I 1

Tabel 3: *Shorthand*

RPX	Request position from motor X
RVX	Request velocity from motor X
RAX	Request amps from motor X
RIX	Request index from moter X

Tabel 4: *Package format - FPGA*

Request0 - 1 to 27 bits				Request1 - 1 to 27 bits			
Position0	Velocity0	Amp0	Index0	Position1	Velocity1	Amp1	Index1