

NOTES:

1. LV4: COMPONENTS NOT LOADED FOR BASIC ASSEMBLY.

ECO		REV	DESCRIPTION	DATE	UPDATED BY
C48745		01	DEVELOPMENT RELEASE	12-12-22	R. NICOLAISEN

4-channel Mux I2C 7-bit Address	Fixed				Hardware Selectable		Fixed	Read/Write Bit
					A2	A1		
0x70	1	1	1	0	0	0	0	x
0x72					0	1		x
0x74					1	0		x
0x76					1	1		x

Don't Care				Channel Selection Bits				Function					
B7	B6	B5	B4	B3	B2	B1	B0						
x	x	x	x				0	Channel 0 disabled					
							1	Channel 0 enabled					
						0		Channel 1 disabled					
						1		Channel 1 enabled					
					0			Channel 2 disabled					
					1			Channel 2 enabled					
				0				Channel 3 disabled					
				1				Channel 3 enabled					
*B7-B4 are Don't Care; they are set to 0 at power-up/reset													
**Multiple channels can be enabled, but care should be taken not to exceed bus sink capacity													

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APPROVALS		DATE	
DRAWN BY: R. NICOLAISEN		12-12-22	
APPROVED BY: C. LAMB		12-12-22	
DWG NUMBER		REV	
225253		01	
PWB REF: 225254		SHEET 1 OF 6	

D

C

B

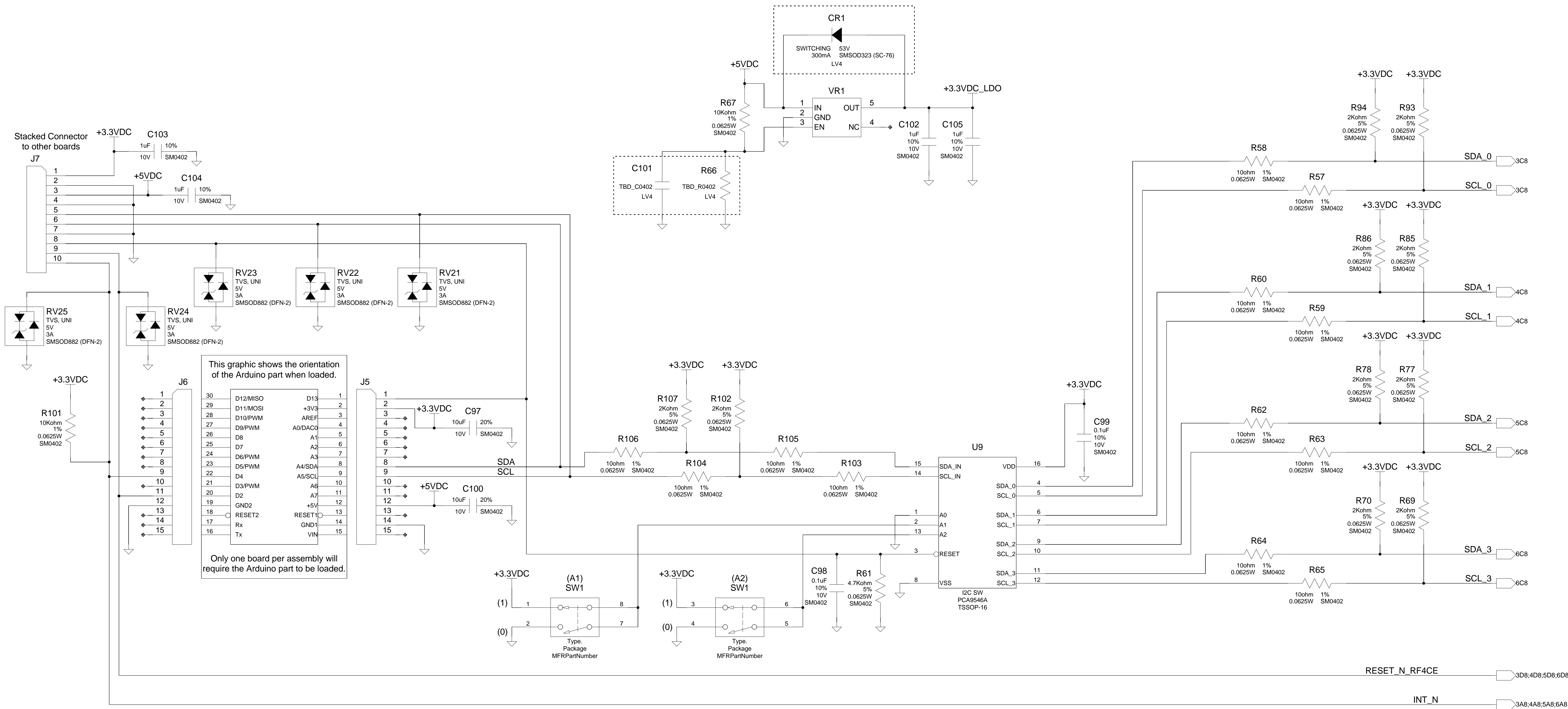
A

D

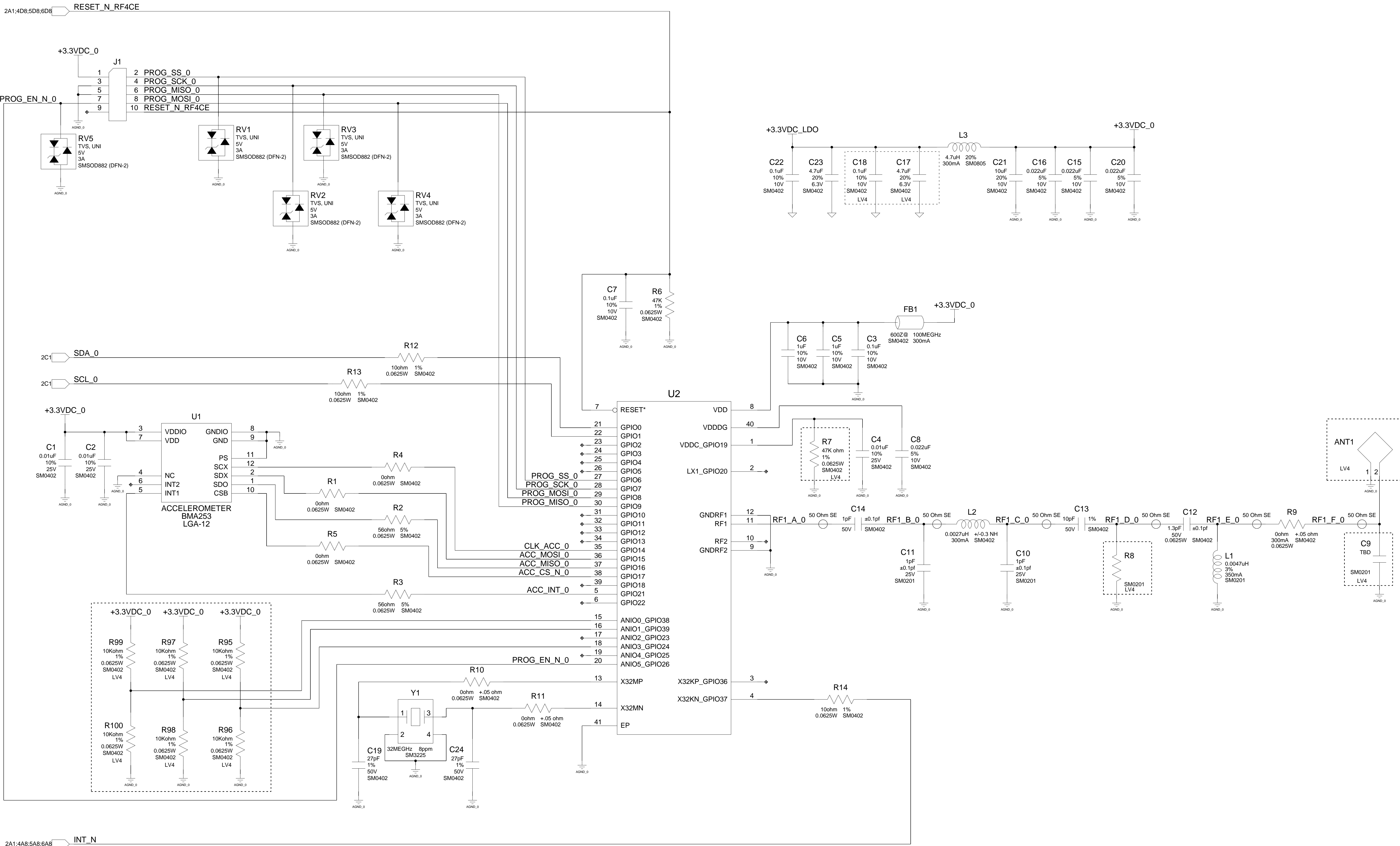
C

B

A

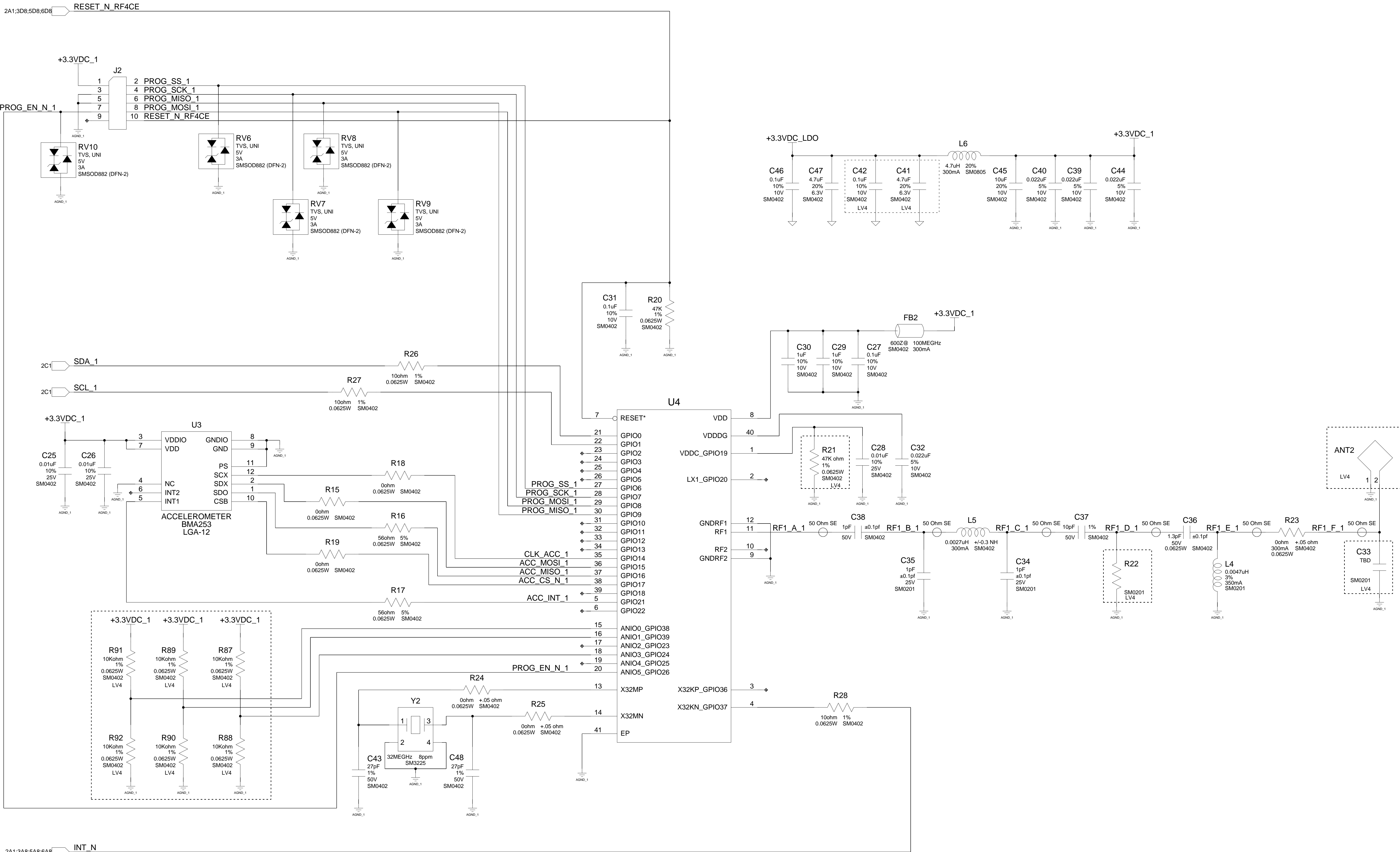


SECTION:				2
PWB REF: 225254		DWG NUMBER 225253		REV 01
				SHEET 2 OF 6



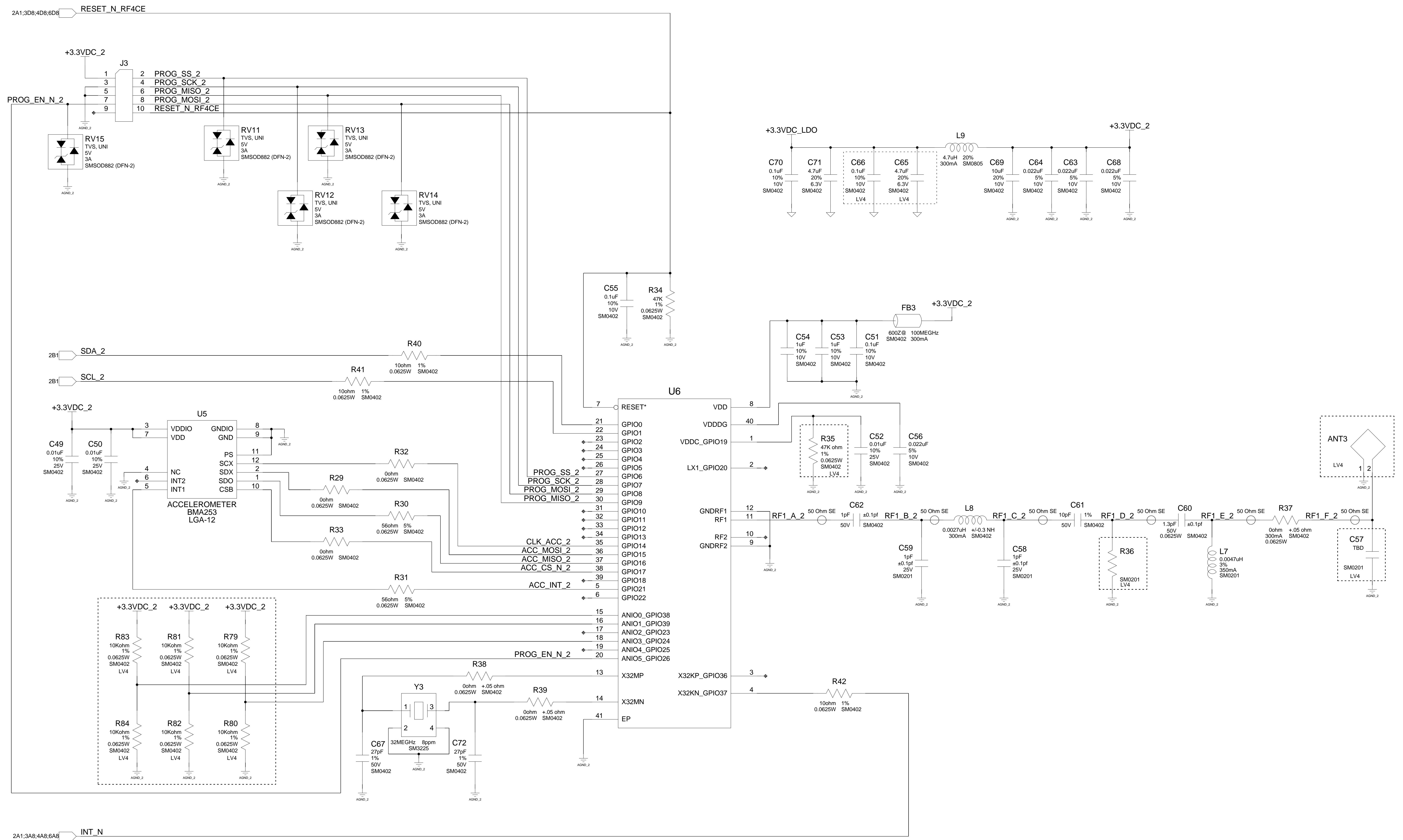
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PWB REF:	DWG NUMBER	REV	SHEET
225254	225253	01	3 OF 6

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SECTION:			RF4CE-1	
PWB REF:		DWG NUMBER	REV	SHEET
225254		225253	01	4 OF 6

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SECTION:

RF4CE-2

PWB REF:
225254

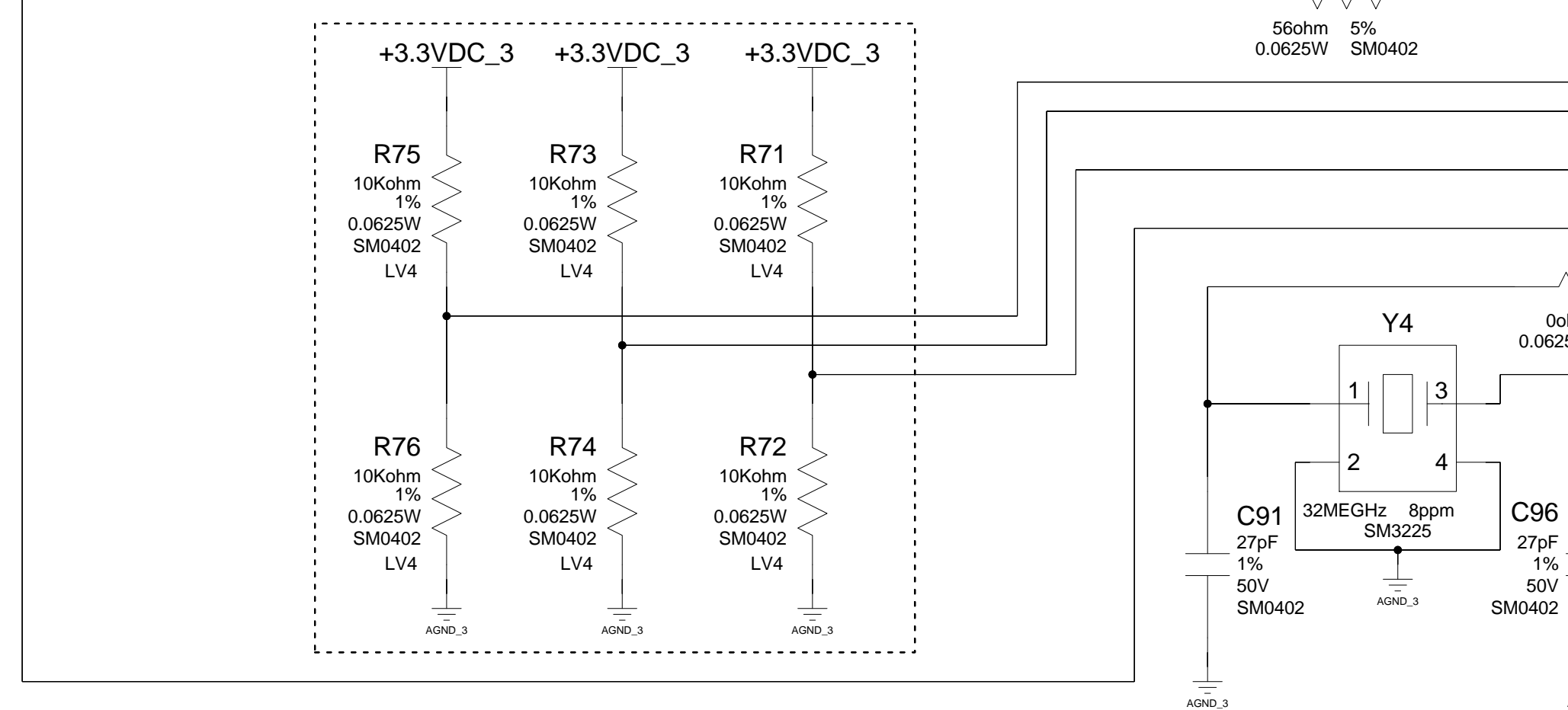
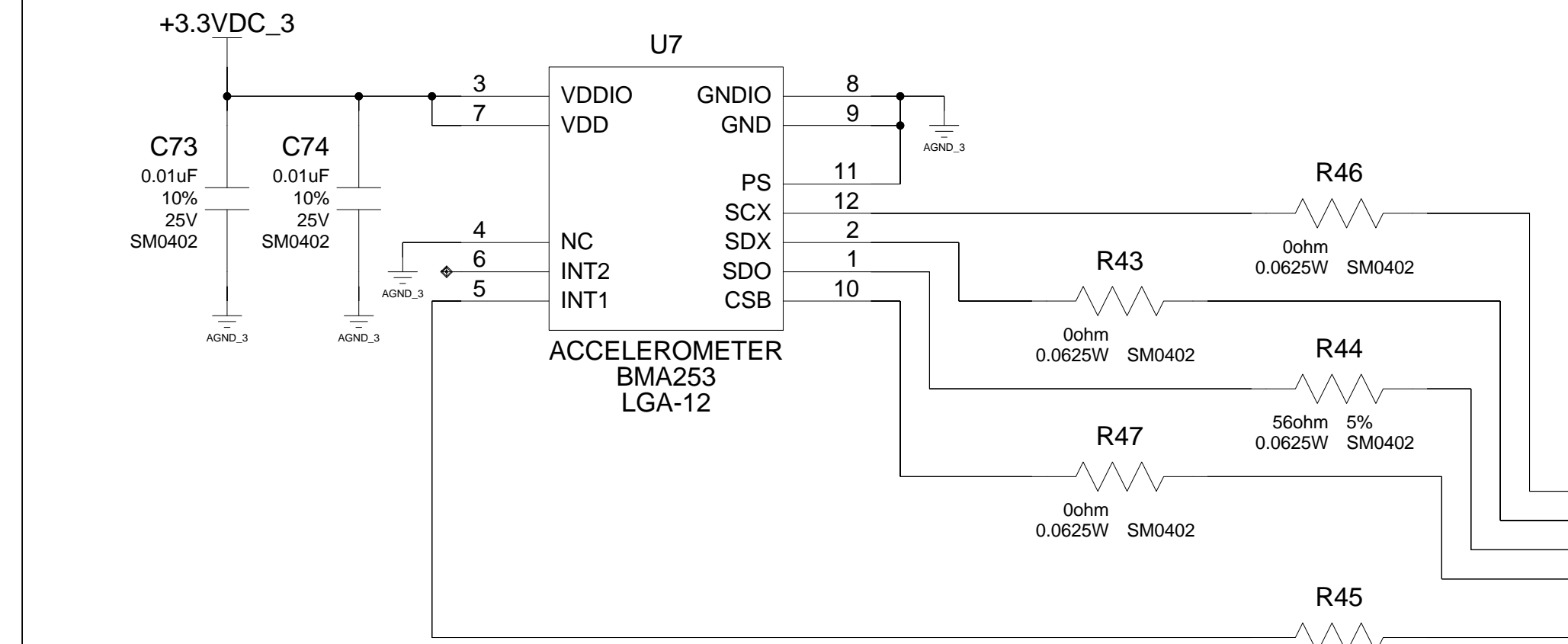
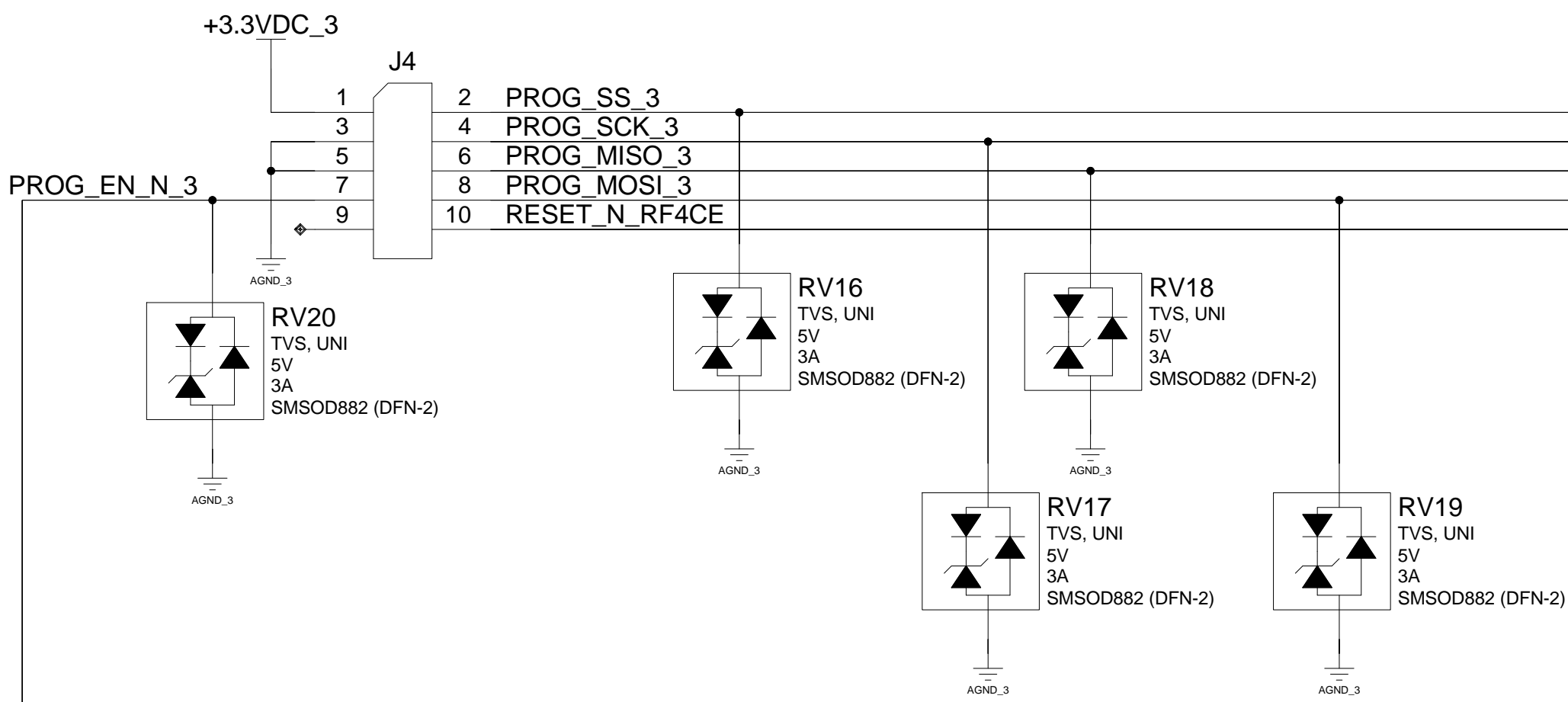
DWG NUMBER
225253

REV	SHEET
01	5 OF 6

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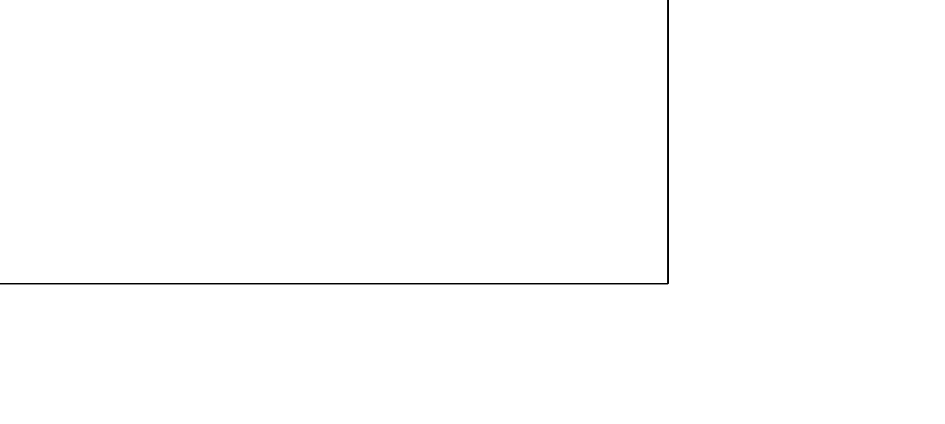
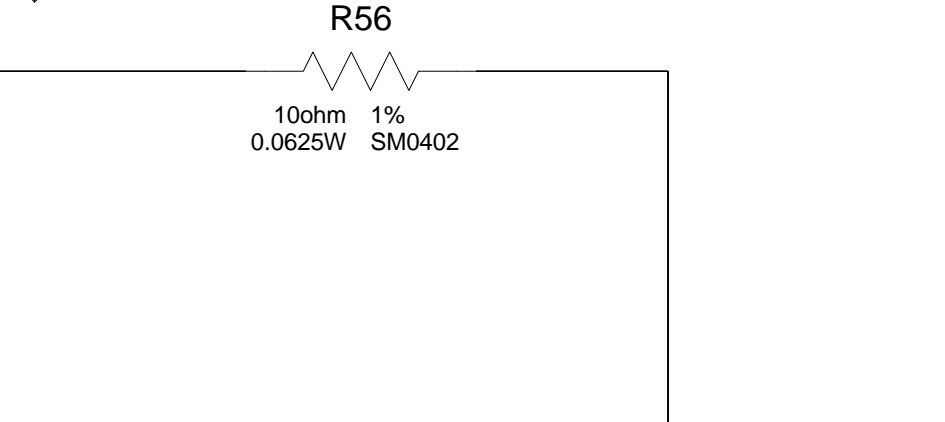
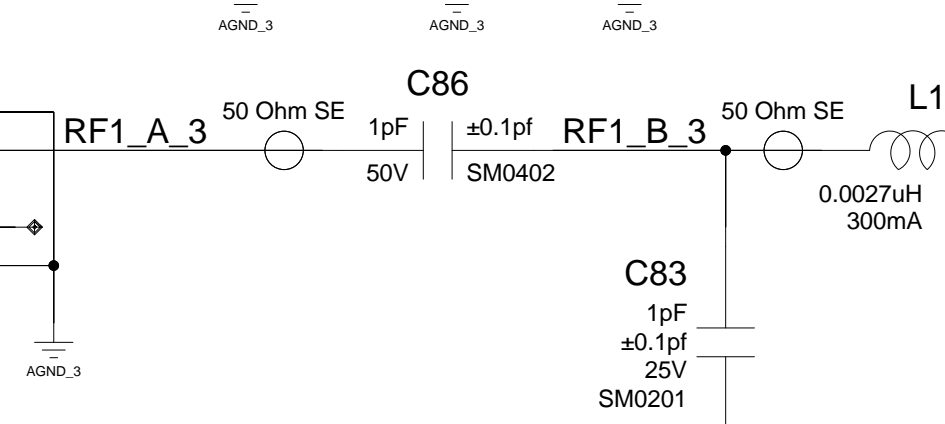
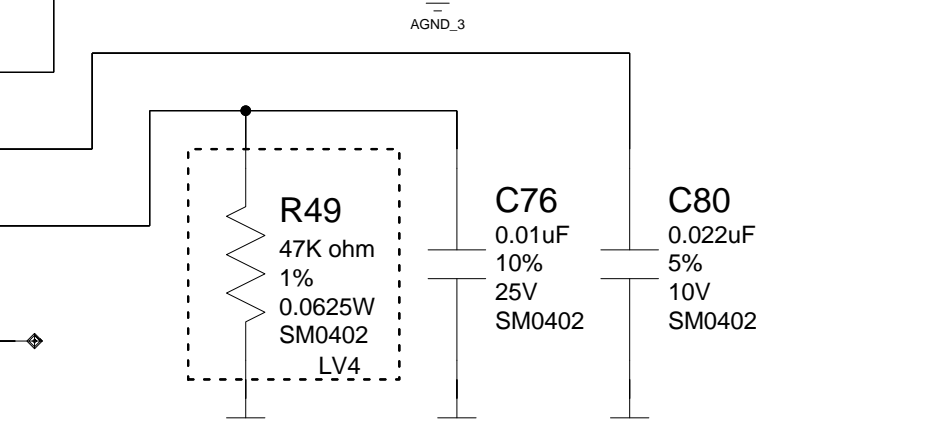
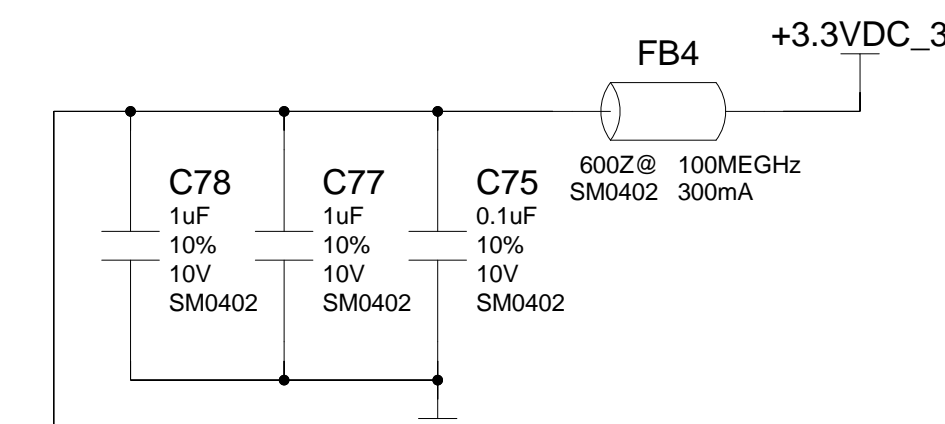
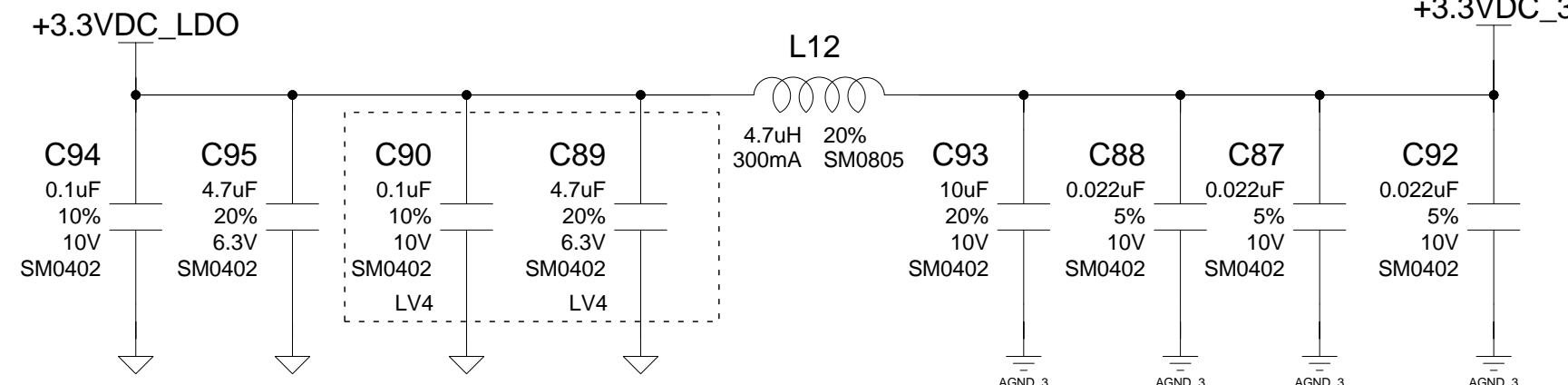
2A1;3D8;4D8;5D8

RESET_N_RF4CE



2A1;3A8;4A8;5A8

INT_N



SECTION:				RF4CE-3	
PWB REF:		DWG NUMBER		REV	SHEET
225254		225253		01	6 OF 6

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