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SELF INTRODUCTION

- Open-minded
- Self-disciplined and drive for self-improvement
- Willing to help and share
- Works well as a member of a team
- Strives to do the right thing
- Overthinker, Perfectionist

EXPERIENCE

2014/07 - 2015/02 Delta Electronics 台達電子 HVP R&D Dept. - Firmware Engineer

- Semiconductor Fab Equipment Power Supply
- High Voltage DC Power Supply

2015/03 - 2017/08

Montage Technology 瀾起科技 Software Dept. - Software Engineer

- Employee of the Year 2016(the only winner in Taiwan)
- Implement IoT Applications(SDK) using FreeRTOS
 - UART software Flow Control
 - AT Command Parser
- IC Test Firmware
 - FT ATE & WiFi Module MP Test
- FPGA/SoC Verification and HAL Development
- Implement CI and White-box Test Flow



EXPERIENCE

2017/09 - 2019/04 Phison Electronics 群聯電子 Chip R&D Dept. - Firmware Engineer

- NAND Flash Controller IC HAL Development
- FPGA/SoC Verification
 - NAND Flash IP
 - Error Handling(Raid)
 - Coprocessor Communication(ARM + Andes)

2019/05 - Present Igis Technology 神亞科技 System Design Dept. - Senior Engineer

- System Firmware Architecture Design and Development
 - BootROM, BSP, Security Update, ISR, IRQ APIs
 - Performance Optimization, Makefile, Linker Script
- Implement Command Line Interface(UART) and Porting FreeRTOS
- Sensor Driver & HAL Development
- FPGA/SoC Verification
 - UART, GPIO, Interrupt, Timer, Timing Control, SPI slave
 - DSP, DMA System Bus, PMU, ADC, Sleep mode



SKILLS & TOOLS

Skills

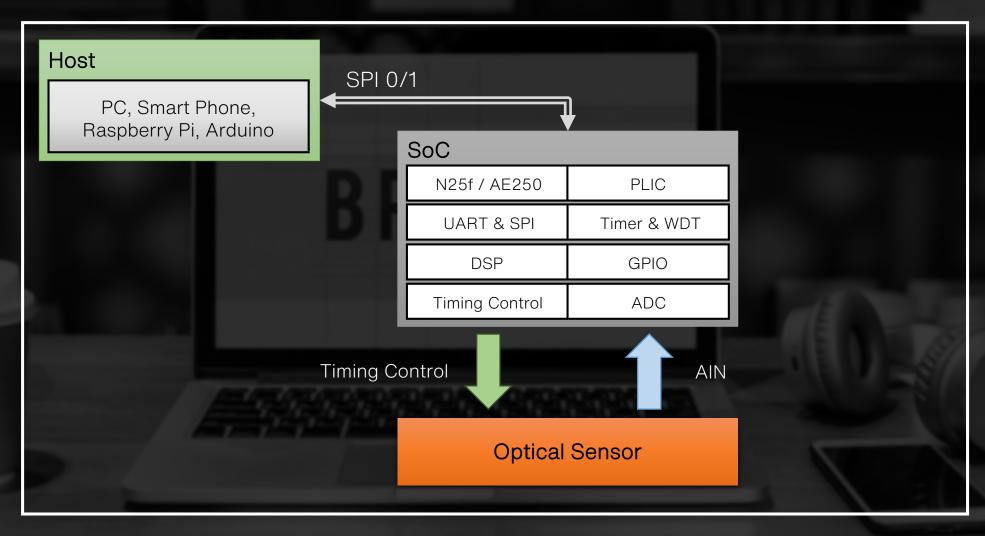
- Languages
 - ✓ C
 - ✓ Assembly
 - ✓ Bash
 - ✓ Python
 - ✓ Markdown
- Applications & Drivers
 - ✓ UART (flow control)
 - ✓ GPIO, SPI, ISR, Timer, ADC
 - ✓ Command Line Parser
 - ✓ FreeRTOS
 - ✓ Boot ROM, Bootloader
 - ✓ Security Update, OTA

Tools

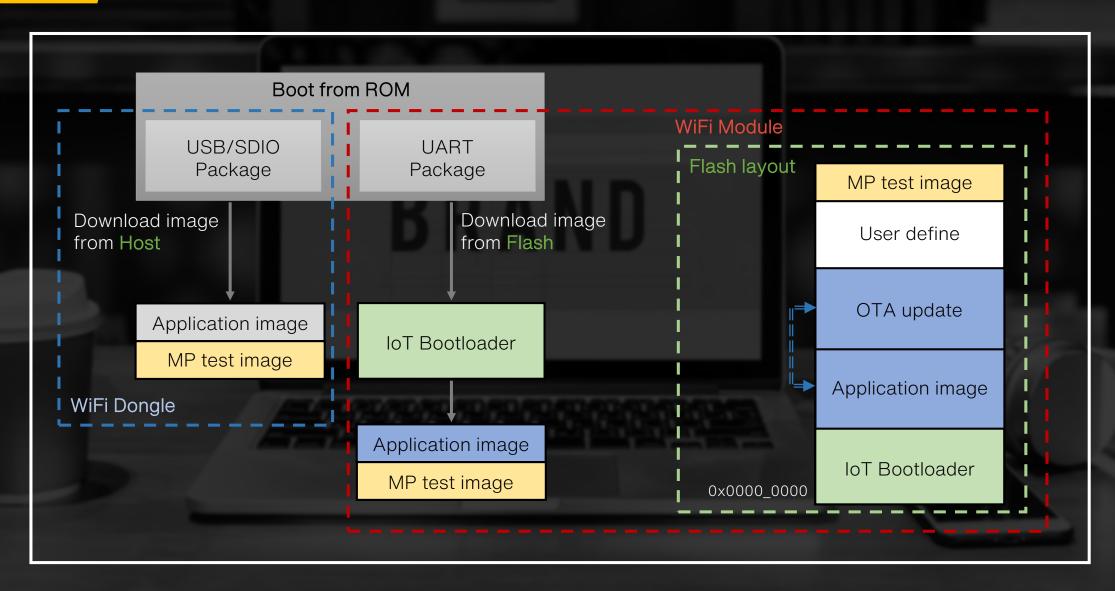
- Debug Tools
 - ✓ ICE
 - ✓ Scope
 - ✓ Logic Analyzer
- Code Editors
 - ✓ Vim
 - ✓ Source Insight
 - ✓ Meld
- Version Control
 - ✓ Git
 - ✓ SVN
 - ✓ Sourcetree
- Ubuntu, Linux Bash Command
- Synopsys HAPS & Verdi
- Cadence Palladium

WORK EXPERIENCE FLOW CHART

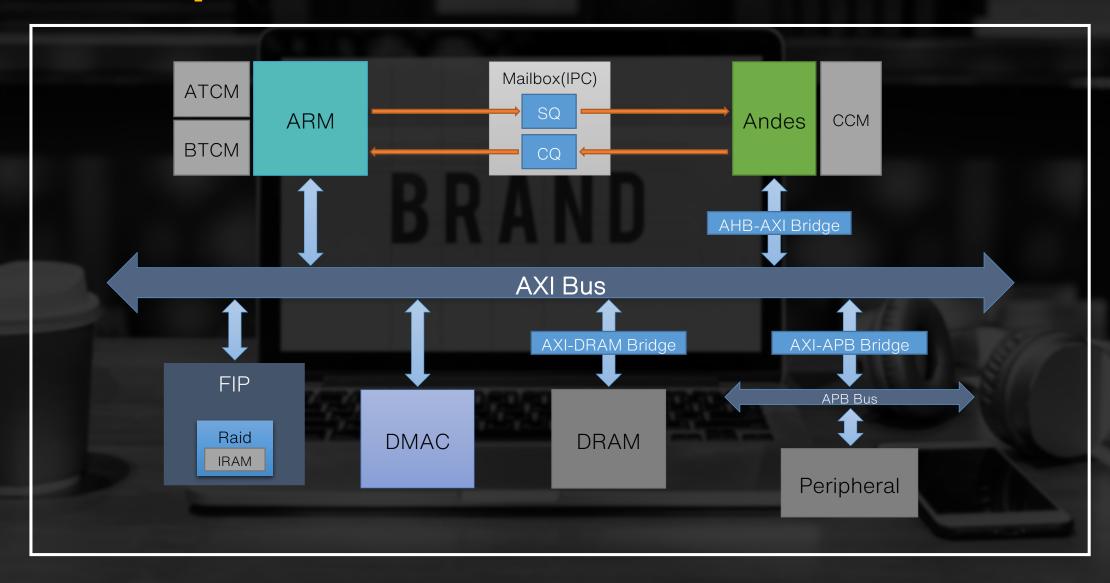
Fingerprint Capture



WiFi Module Boot Flow



Coprocessor Architecture



Mass Production Test

