

Maysam Zoor

*PhD in Computer Science
Program Manager*

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Objective

As a Ph.D. graduate in Computer Science from the prestigious Institut Polytechnique de Paris with over seven years of experience in both corporate and research roles, I am eager to leverage my expertise in program management and my ability to bridge the gap between technical and non-technical stakeholders to drive innovation and success within your organization. Notably, I secured €1 million in funding through successful EU proposals, including Eureka projects. My doctoral thesis, part of the AQUAS European project, involved collaboration with industry leaders such as Siemens and ClearSy. I am eager to contribute to innovative projects and thrive in a challenging and rewarding team environment

Experience

2023–Present **Program Manager**, DEMAND SIDE INSTRUMENTS, Caen, France.

- Writing successful European Union proposals, securing 1 million euros in funding for research projects (ex Eureka)
- Leading successful proposal writing teams, resulting in the acceptance of multiple abstracts and presentations at international conferences.
- Specializing in Monitoring and Control Systems Platforms for Saving Energy and Water
- Communicating effectively complex technical information to non-technical audiences
- Managing a multi-national European Union research project, involving collaboration between partners from different countries.
- Managing successfully the implementation and release of the advanced irrigation system "Aliaterra", a system aiming to meet the needs of the smart city and precision agriculture.
- Cross-functional collaboration with teams to design and implement efficient and cost-effective hardware and software solutions spanning multiple domains.
- Analyzing product requirements and evaluating potential technical solutions.
- Developing and managing project plans, budgets, and timelines for the project.

2022–2023 **Embedded System Research Engineer**, DEMAND SIDE INSTRUMENTS, Caen, France.

- Leading collaborative research projects with academic and industrial partners
- Bringing new ideas and research to life
- Designing, developing, testing and optimizing software components.
- Documenting and validating software designs.
- Working with project management tools such as Jira, Google Sheets/Excel

2017–2021 **PhD Student**, INSTITUT MINES-TELECOM, Sophia Antipolis, France.

Project: AQUAS European project (European Union's Horizon 2020)

- Attending plenary meetings and telcos on a regular basis and collaborating with partners
- Modeling two use cases to verify timing requirements in safe and secure embedded systems and automation processes
 - Industrial Drive use case with Siemens Austria
 - Rail Carriage Mechanisms use case with ClearSy

2016–2017 **Masters of Computer Science and Risk Management**, MINES PARISTECH, Sophia Antipolis, France.

- **Training:** Utilizing Anylogic Java-based platform to develop a flexible maritime network simulations
 - Implementing agent-based systems, graph structures, and algorithms, including the shortest path algorithm, to assess network vulnerability
 - Implementing disruptions corresponding to various maritime risk scenarios while continuously applying geometrics and computing expertise

2015 – 2016 **Software Engineer**, LIBATEL.

- Programming and enhancing a call accounting system that transforms the raw data into meaningful reports: Sharpdial- AVAYA certified
- Installing and maintaining call back server solution for windows
- Developing AVAYA IP phone application and telephony notification system
- Tuning SQL queries related to applications and reports

2013 – 2015 **Software Engineer**, PATH SOLUTIONS.

- Programming and migrating, IMAL Banking solutions project, from PowerBuilder to JAVA Multitier Platform Web project (Ajax, Java, JSP, Struts, Spring, IBatis, JavaScript, Tomcat Server)
- Developing queries and procedures in Sybase and Oracle PL/SQL Databases > 2TB

2012 – 2013 **Embedded System Engineer**, EKT ELECTRONICS.

- Designing circuits PCBs layouts and schematics
- Designing and Developing NTP (Network Time Protocol) Clock Project and releasing it as a new product to market

Professional Certificates

- IBM Applied DevOps Engineering Professional Certificate (2024 ongoing Online course)
 - Introduction to DevOps(2024 Online course - IBM)
 - Introduction to Agile Development and Scrum (2024 Online course - IBM)
- Google Project Management: Professional Certificate (2023 ongoing Online course)
 - Project Initiation: Starting a Successful Project (2023 Online course - Google)
 - Foundations of Project Management (2023 Online course - Google)
 - Project Planning: Putting It All Together (ongoing Online course - Google)
- Fundamentals of TinyML (2022 Online course - HarvardX)
- Introduction to Embedded Machine Learning (2021 Online course - Edge Impulse)
- Managing Global Projects (2018 - Eurecom)

Education

- 2017–2021 **PhD student**, INSTITUT POLYTECHNIQUE DE PARIS, Paris, France.
Proposing, formally defining and implementing a new approach to verify timing requirements in embedded systems based on representing the system model as a graph and analyzing simulation/execution traces along this graph. The approach can be extended to design safe and secure embedded systems while respecting timing constraints.
- **Subject:** Latency Verification in Execution Traces of HW/SW Partitioning Model
 - **Institution:** Telecom Paris
 - **Project:** AQUAS European project (European Union's Horizon 2020)
- 2016–2017 **Masters of Computer Science and Risk Management**, LU.
- **Rank:** 3/14 - **Average:**80.29 - **Honor:** Very Good
 - **Research Work:** Security Challenges in CityPro: a new intelligent city that is constructed by Internet of Things, cloud computing and other information technologies
- 2007–2012 **BE in Computer and Communication Engineering**, NOTRE DAME UNIVERSITY.
- Graduated with **distinction**. **Cumulative GPA:** 3.26/4 (**Dean's** honor List)
 - **Senior Project:** Intelligent Digital Pen (ID-Pen).Transforms handwritten text into typed letters.It is based on IMU sensor (accelerometer and gyroscope). *Achieved with **A** grade*

Advanced Software Tools

- **Project Management Tools:**
 - Monday.com, Jira, Microsoft Project, ClickUp, Google Workspace tools
- **Software Languages/Tools:**
 - Go, Python, JAVA, C++, C#, ASP.NET, SYBASE, PL/SQL, ORACLE, JAVASCRIPT, JQUERY, Anylogic, MATLAB
- **Designing Languages/Tools:**
 - UML, SysML, TTool, AUTOCAD, MIKROBASIC, Verilog, SIMULINK, MULTISIM, ALEGRO

Volunteering Experience

- CROIX-ROUGE
- LES PETITS FRÈRES DES PAUVRES

Languages

- English, French, Arabic

Publications

- Zoor, M., Apvrille, L., Pacalet, R., and Coudert, S. (2023). Execution trace analysis for a precise understanding of latency violations. *Software and Systems Modeling*, 1-23.
- Zoor, M. (2021). Latency verification in execution traces of HW/SW partitioning model (Doctoral dissertation, Institut Polytechnique de Paris).
- Zoor, M., Apvrille, L., and Pacalet, R. (2021, October). Execution Trace Analysis for a Precise Understanding of Latency Violations. In *2021 ACM/IEEE 24th International Conference on Model Driven Engineering Languages and Systems (MODELS)* (pp. 123-133). IEEE ^a.
- Zoor, M., Apvrille, L. and Pacalet, R., 2020, October. SysML models: studying safety and security measures impact on performance using graph tainting. In *Proceedings of the 23rd ACM/IEEE International Conference on Model Driven Engineering Languages and Systems: Companion Proceedings* (pp. 1-10).
- Zoor, M., Apvrille, L. and Pacalet, R., 2020, February. Impact of Security Measures on Performance Aspects in SysML Models. In *MODELSWARD* (pp. 373-380).
- Fujdiak, R., Blazek, P., Apvrille, L., Martinasek, Z., Mlynek, P., Pacalet, R., Smekal, D., Mrnustik, P., Barabas, M. and Zoor, M., 2019, June. Modeling the trade-off between security and performance to support the product life cycle. In *2019 8th Mediterranean Conference on Embedded Computing (MECO)* (pp. 1-6). IEEE.

^aMODELS Conference is ranked A2- one of the top conferences in the field