

SHIREEN WARNOCK

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EDUCATION

Massachusetts Institute of Technology

Cambridge, MA

- Candidate for a Bachelor of Science in Electrical Engineering and Computer Science in 2011
- GPA: 4.9/5.0
- Relevant Coursework: Physics (Mechanics & Kinematics, E & M, Waves & Vibrations), Intro to EECS I, Probabilistic Systems Analysis (Spring 09): Intro to EECS II, Circuits & Elements, Applied Electromagnetism (Fall 09): Signals & Systems, Microelectronic Devices & Circuits, Introductory Analog Electronics Laboratory

EXPERIENCE

CERN

Geneva, Switzerland

Jun. 2009-July 2009

- Develop, run, and analyze tests on a component of the LHC CMS detector to ensure compatibility with heavy ion experiments

MIT Lincoln Laboratory

Lexington, MA

Jan. 2009

- Developed expertise of Kalman filter, and did parameter study of the multi-target tracking problem using both a simple Kalman filter and an adaptive filter

IBM

Poughkeepsie, NY

Jun. 2008-Aug. 2008

- Circuit design for a microprocessor chip for one of IBM's system z servers
- Miscellaneous scripting for data analysis

SKILLS

Computer

- Proficient in Windows computers and software, including Microsoft Word, Excel, Powerpoint, Photoshop; Experience with HTML, C++
- Also proficient in Python, MATLAB, Perl

Language

- Fluent in Farsi. Familiarity with French

ACTIVITIES

Alpha Chi Omega

Aug. 2009-Present

- Junior Representative for Chapter Relations and Standards Board, handled concerns and served as liaison between junior members and the chapter

Athletics: MIT Crew

Sept. 2007-May 2008

- Coxswain for novice first boat, led and encouraged the team to work together and work more efficiently