

Wendi Li

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Objective	To obtain a full time entry level position to begin Summer 2009.
Education	<p>Masters of Engineering in Electrical Engineering and Computer Science, June 2009, GPA available 2009 Massachusetts Institute of Technology, Cambridge, MA Relevant Coursework: Feedback Systems, Solid State Circuits, Analog & Mixed Signal CMOS Design, Device Physics, Communication, Control and Signal Processing, Digital IC Design, Power Electronics, Probabilistic Systems Analysis</p> <p>Bachelors of Science in Electrical Engineering, June 2008, GPA 4.6/5.0 Massachusetts Institute of Technology, Cambridge, MA</p> <p>High School Diploma in International Baccalaureate Program and Math Science & Engineering Magnet Suncoast High School, Riviera Beach, FL National Merit Finalist 2004; National AP Scholar 2004; National Honor Society President</p>
Experience	<p>MIT EECS, Masters of Engineering Thesis, Graduation June 2009, supported by Linear Technology Corporation Design, layout, and fabrication of a fully differential operational amplifier capable of driving loads of 5pF and 50Ω with third harmonic distortion performance of 90dB and 1.5nV/√Hz input referred noise. Amplifier features ±25mV digitally programmable input offset provided by complimentary current DACs, and full scale common mode input compatibility. Op amp is designed to drive a programmable G_m-C filter and is fabricated in .35μm SiGe complementary BiCMOS process. Also design of evaluation and testing boards, and laboratory evaluation of performance of chip.</p> <p>MIT 6.002 Circuits & Electronics Course Staff, February – December 2008, September –December 2006</p> <p>Linear Technology Corporation, Analog Design Intern, May – August 2007 Designed subnanosecond, PECL output comparator processed in 0.35μm SiGe BiCMOS. Chip had ~1000 gain with toggle rate of 1.8Gb/s, gain bandwidth product of 1.8terahertz. Design, simulation and layout performed using Cadence family of design tools.</p> <p>MIT Course Projects 2006-2007 Microcontroller Lab: Taught robotic arm to play a piano using the Intel 8051, controlled by PC GUI interface Analog Lab: Built full FM Radio Digital Lab: Interfaced VGA, AC97, and NTSC on FPGAs to create an interactive DDR/piano hybrid game.</p> <p>MIT 6.111 Digital Design Laboratory Course Staff, January 2007 – May 2007</p> <p>GE Healthcare, Engineering Intern, May – August 2006 Worked on next generation electromagnetic surgery trackers for image guided surgery. Investigated low frequency transmission and its effects on tolerance of metals in a working volume. Wrote testing and analysis software in Matlab using novel planar testing protocol. Built GUIs to facilitate the tracker hardware design process.</p> <p>MIT Sloan Law Program, Researcher, June – December 2005 Focus areas in copyright law, accountant's liability, medical kickbacks, corporate governance.</p> <p>MIT Laboratory for Electromagnetic and Electronic Systems, January – June, 2005 Designed, constructed, and processed output with a team for components of a variable speed, constant frequency, high efficiency torus wind energy generator. Tested system performance using LabVIEW interface. Performed all Solidworks modeling of the machine.</p> <p>FPL Power Marketing, Inc., Credit Management Intern, June-August 2004 Performed credit analysis for over 300 FPLE trading counterparties.</p>
Skills	<p>Cadence (Virtuoso, Spectre, Layout), Verilog, Matlab, Solidworks, PSPICE, HSpice, NanoSim, LTSpice Some experience in: C++, Assembly, Lisp OS: Linux, Windows, Mac Fluent languages: English, Chinese Six Sigma Green Belt trained</p>
Awards & Leadership	<p>MIT Institute Awards: 2008 Laya Wiesner Award winner, Presented by MIT President Susan Hockfield to the “undergraduate woman student who has most enhanced MIT community life”</p> <p>MIT Women's League Scholar 2007 & 2008</p> <p>Society of Women Engineers President 2007, Vice President 2008 & 2006, Membership Dev Chair 2005, Historian 2004</p> <p>MIT Sloan – China Tsinghua University EMBA Conference Program Coordinator, 2006</p> <ul style="list-style-type: none">• Concert Choir • MIT Campus Tour Guide 2004-2008 • Women's Orientation Committee• Eta Kappa Nu and Tau Beta Pi Honor Societies • IEEE Faculty/Alumni Chair• Career Fair 2007&2008 Negotiator and Organizing Committee• Technology & Entrepreneurship Forum Logistics Committee