

## 1. Description

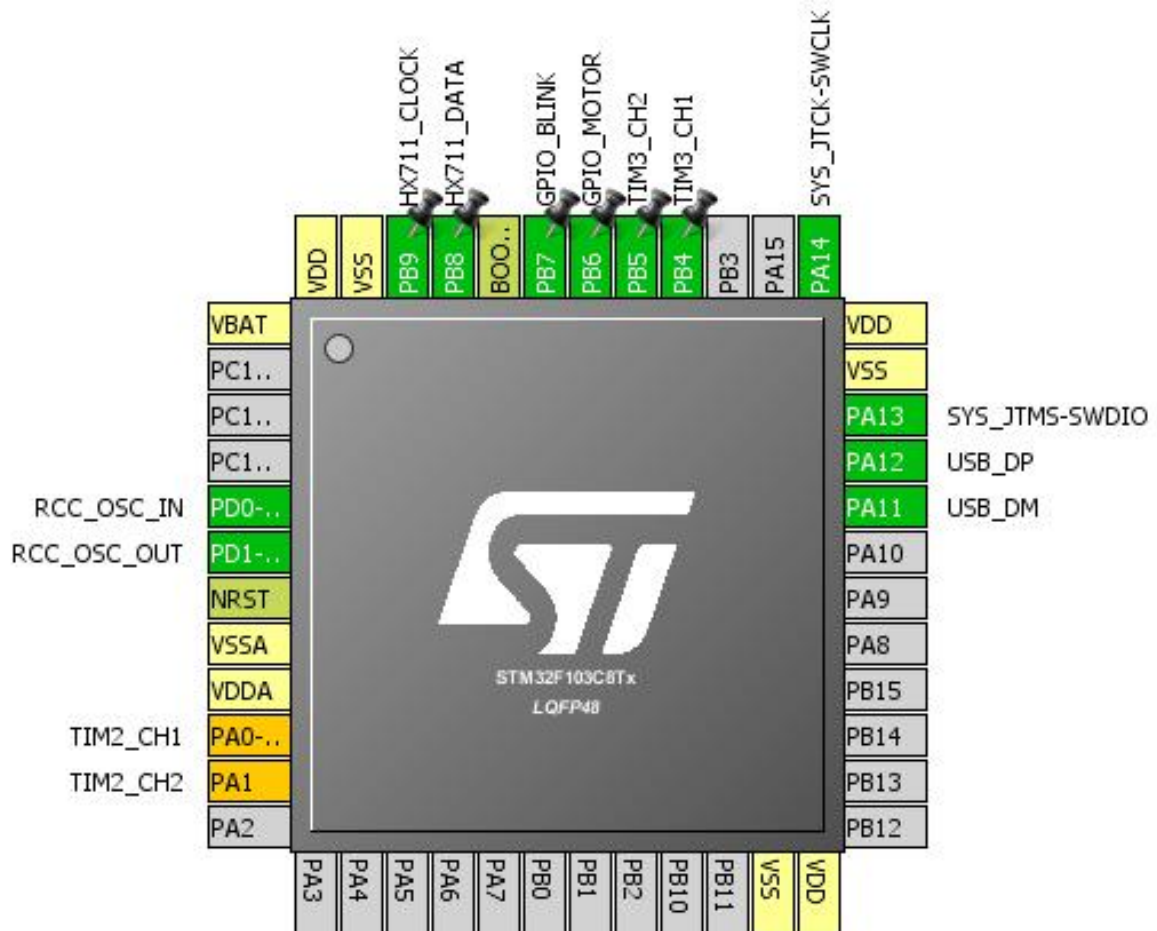
### 1.1. Project

Project Name	HX711_F1
Board Name	HX711_F1
Generated with:	STM32CubeMX 4.19.0
Date	06/03/2017

### 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

## 2. Pinout Configuration



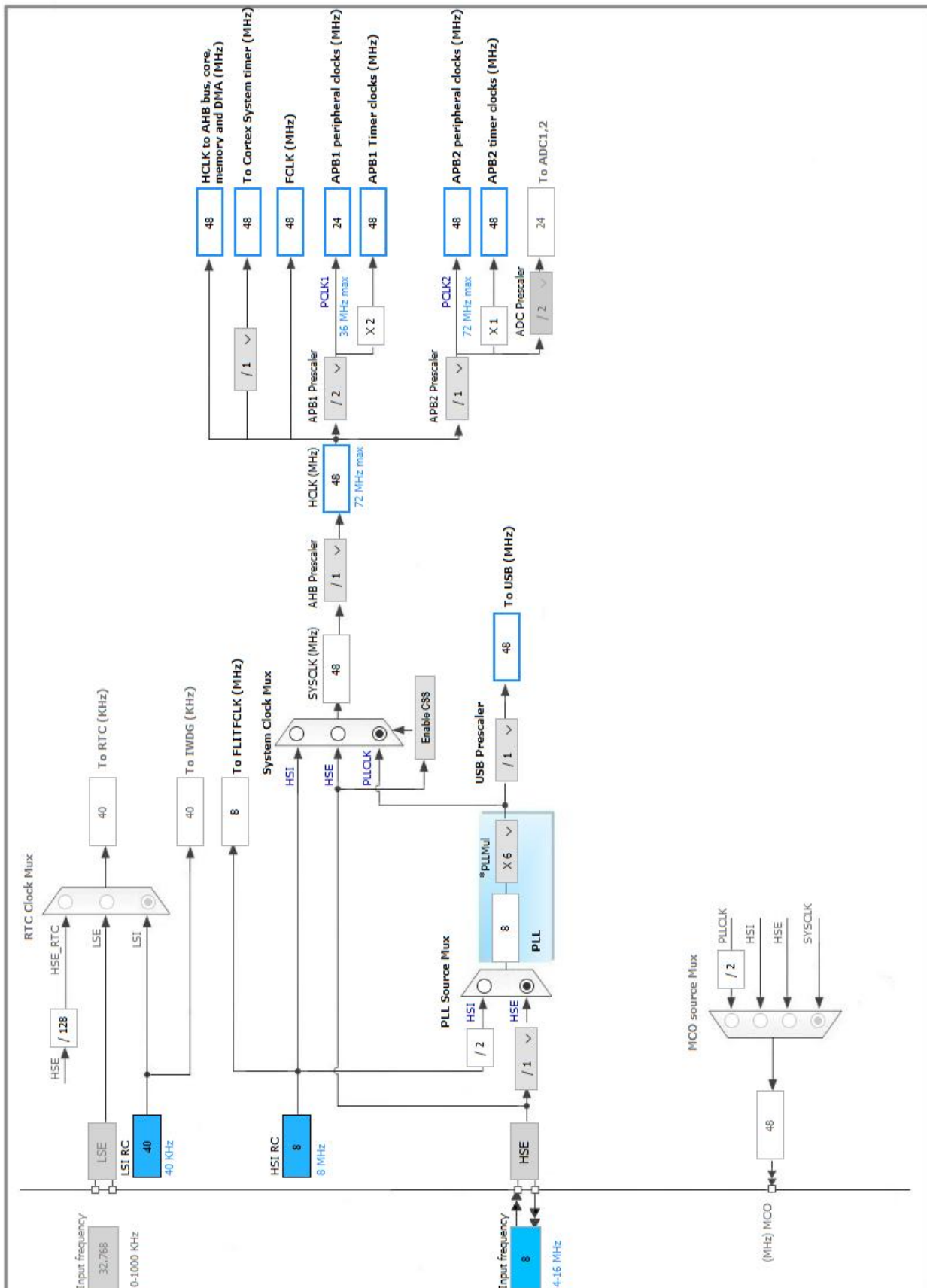
### 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP *	I/O	TIM2_CH1	
11	PA1 *	I/O	TIM2_CH2	
23	VSS	Power		
24	VDD	Power		
32	PA11	I/O	USB_DM	
33	PA12	I/O	USB_DP	
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
40	PB4	I/O	TIM3_CH1	
41	PB5	I/O	TIM3_CH2	
42	PB6 **	I/O	GPIO_Output	GPIO_MOTOR
43	PB7 **	I/O	GPIO_Output	GPIO_BLINK
44	BOOT0	Boot		
45	PB8 **	I/O	GPIO_Input	HX711_DATA
46	PB9 **	I/O	GPIO_Output	HX711_CLOCK
47	VSS	Power		
48	VDD	Power		

\*\* The pin is affected with an I/O function

\* The pin is affected with a peripheral function but no peripheral mode is activated

## 4. Clock Tree Configuration



## 5. IPs and Middleware Configuration

### 5.1. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

##### 5.1.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

###### RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

### 5.2. SYS

#### Debug: Serial Wire

#### Timebase Source: SysTick

### 5.3. TIM3

#### Combined Channels: Encoder Mode

##### 5.3.1. Parameter Settings:

###### Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value )	<b>65535 *</b>
Internal Clock Division (CKD)	No Division

###### Trigger Output (TRGO) Parameters:

Master/Slave Mode	Disable (no sync between this TIM (Master) and its Slaves
Trigger Event Selection	<b>Update Event *</b>

###### Encoder:

Encoder Mode	<b>Encoder Mode TI1 and TI2 *</b>
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\_\_\_\_ Parameters for Channel 1 \_\_\_\_

Polarity	Rising Edge
IC Selection	Direct
Prescaler Division Ratio	No division
Input Filter	0

\_\_\_\_ Parameters for Channel 2 \_\_\_\_

Polarity	Rising Edge
IC Selection	Direct
Prescaler Division Ratio	No division
Input Filter	0

## 5.4. USB

### mode: Device (FS)

#### 5.4.1. Parameter Settings:

##### Basic Parameters:

Speed	Full Speed 12MBit/s
Endpoint 0 Max Packet size	8 Bytes

##### Power Parameters:

Low Power	Disabled
Link Power Management	Disabled
Battery Charging	Disabled

## 5.5. USB\_DEVICE

### Class For FS IP: Communication Device Class (Virtual Port Com)

#### 5.5.1. Parameter Settings:

##### Basic Parameters:

VirtualMode	Cdc
USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)	1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)	1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)	512
USBD_SUPPORT_USER_STRING (Enable user string descriptor)	Disabled
USBD_SELF_POWERED (Enabled self power)	Enabled
USBD_DEBUG_LEVEL (USBD Debug Level)	0: No debug message

**Class Parameters:**

USBD\_CDC\_INTERVAL (Number of micro-frames interval) 1000

**5.5.2. Device Descriptor:**

**Device Descriptor:**

VID (Vendor Identifier) 1155  
LANGID\_STRING (Language Identifier) English(United States)  
MANUFACTURER\_STRING (Manufacturer Identifier) STMicroelectronics

**Device Descriptor FS:**

PID (Product Identifier) 22336  
PRODUCT\_STRING (Product Identifier) STM32 Virtual ComPort  
SERIALNUMBER\_STRING (Serial number) 00000000001A  
CONFIGURATION\_STRING (Configuration Identifier) CDC Config  
INTERFACE\_STRING (Interface Identifier) CDC Interface

\* User modified value

## 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PD0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
TIM3	PB4	TIM3_CH1	Input mode	<b>Pull-up *</b>	n/a	
	PB5	TIM3_CH2	Input mode	<b>Pull-up *</b>	n/a	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
Single Mapped Signals	PA0-WKUP	TIM2_CH1	Alternate Function Push Pull	n/a	Low	
	PA1	TIM2_CH2	Alternate Function Push Pull	n/a	Low	
GPIO	PB6	GPIO_Output	Output Push Pull	n/a	Low	GPIO_MOTOR
	PB7	GPIO_Output	Output Push Pull	n/a	Low	GPIO_BLINK
	PB8	GPIO_Input	Input mode	<b>Pull-up *</b>	n/a	HX711_DATA
	PB9	GPIO_Output	Output Push Pull	<b>n/a</b>	Low	HX711_CLOCK

### 6.2. DMA configuration

nothing configured in DMA service



### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
USB low priority or CAN RX0 interrupts	true	0	0
TIM3 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USB high priority or CAN TX interrupts	unused		

\* User modified value

## ***7. Power Consumption Calculator report***

### 7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103C8Tx
Datasheet	13587_Rev17

### 7.2. Parameter Selection

Temperature	25
Vdd	3.3

## 8. Software Project

### 8.1. Project Settings

Name	Value
Project Name	HX711_F1
Project Folder	C:\Users\lss\Documents\GitHub\HX711_F1
Toolchain / IDE	EWARM
Firmware Package Name and Version	STM32Cube FW_F1 V1.4.0

### 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No