



# **N-Channel JFETs**

PRODUCT SUMMARY							
Part Number	V <sub>GS(off)</sub> (V)	V <sub>(BR)GSS</sub> Min (V)	g <sub>fs</sub> Min (mS)	I <sub>DSS</sub> Min (mA)			
J304	−2 to −6	-30	4.5	5			
J305	−0.5 to −3	-30	3	1			

#### **FEATURES**

- Excellent High Frequency Gain: J304, Gps 11 dB (typ) @ 400 MHz
- Very Low Noise: 3.8 dB (typ) @ 400 MHz
- Very Low Distortion
- High ac/dc Switch Off-Isolation
- High Gain: A<sub>V</sub> = 60 @ 100 μA

#### **BENEFITS**

- Wideband High Gain
- Very High System Sensitivity
- High Quality of Amplification
- High-Speed Switching Capability
- High Low-Level Signal Amplification

#### **APPLICATIONS**

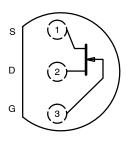
- High-Frequency Amplifier/Mixer
- Oscillator
- Sample-and-Hold
- Very Low Capacitance Switches

#### **DESCRIPTION**

The J304/305 n-channel JFETs provide high-performance amplification, especially at high-frequency. These products are available in tape and reel for automated assembly (see Package Information).

For similar products in TO-236 (SOT-23) packages, see the 2N/SST5484 series data sheet, or in TO-206AF (TO-72) packages, see the 2N/SST4416 series data sheet.

#### TO-226AA (TO-92)



Top View

#### **ABSOLUTE MAXIMUM RATINGS**

Gate-Source/Gate-Drain Voltage	30 V
Forward Gate Current	mΑ
Storage Temperature	0°C
Operating Junction Temperature	0°C

Lead Temperature ( $^{1}/_{16}$ " from case for 10 sec.)	300°C
Power Dissipation <sup>a</sup>	350 mW

Notes

a. Derate 2.8 mW/°C above 25°C



SPECIFICATIONS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)									
				Limits					
				J304		J305			
Parameter	Symbol Test Conditions		Typa	Min	Max	Min	Max	Unit	
Static									
Gate-Source Breakdown Voltage	V <sub>(BR)GSS</sub>	$I_G = -1 \ \mu A$ , $V_{DS} = 0 \ V$	-35	-30		-30		٧	
Gate-Source Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 1 nA		-2	-6	-0.5	-3	V	
Saturation Drain Current <sup>b</sup>	I <sub>DSS</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V		5	15	1	8	mA	
Gate Reverse Current	I <sub>GSS</sub>	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$	-2		-100		-100	pА	
Gale Reverse Current		T <sub>A</sub> = 100°C	-0.2					nA	
Gate Operating Current <sup>b</sup>	I <sub>G</sub>	V <sub>DG</sub> = 10 V, I <sub>D</sub> = 1 mA	-20					рA	
Drain Cutoff Current	I <sub>D(off)</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = -6 \text{ V}$	2				PA		
Drain-Source On-Resistance	r <sub>DS(on)</sub>	$V_{GS} = 0 \text{ V}, I_D = 300 \mu\text{A}$	200					Ω	
Gate-Source Forward Voltage	V <sub>GS(F)</sub>	$I_G = 1 \text{ mA}$ , $V_{DS} = 0 \text{ V}$	0.7					V	
Dynamic									
Common-Source Forward Transconductance	9fs	V 45 V V 0 V £ 4 kH=		4.5	7.5	3		mS	
Common-Source Output Conductance	9 <sub>os</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ kHz}$			50		50	μS	
Common-Source Input Capacitance	C <sub>iss</sub>		2.2						
Common-Source Reverse Transfer Capacitance	C <sub>rss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}$ f = 1 MHz	0.7					pF	
Common-Source Output Capacitance	C <sub>oss</sub>	· · · · · · · · · · · · · · · · · · ·	1						
Equivalent Input Noise Voltage	e <sub>n</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}$ f = 100 Hz	10	_				nV∕ √Hz	

TYPICAL HIGH-FREQUENCY SPECIFICATIONS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)										
	Symbol			Limits (Typ)				_		
				J304		J305				
Parameter		Test Condition	ons	100 MHz	400 MHz	100 MHz	400 MHz	Unit		
High-Frequency				•		•	•			
Common-Source Input Conductance	g <sub>iss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub>	= 0 V	80	800	80		μS		
Common-Source Input Susceptance	b <sub>iss</sub>	$V_{DS} = 15 \text{ V, } V_{GS} = 0 \text{ V}$		2	7.5	2		mS		
Common-Source Output Conductance	9 <sub>oss</sub>			60	80	60		μS		
Common-Source Output Susceptance	b <sub>oss</sub>			0.8	3.6	0.8		0		
Common-Source Forward Transconductance	9fs			4.4	4.2	3		mS		
Common-Source Power Gain	G <sub>ps</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5 mA		20	11			dB		
Noise Figure	NF		$R_G = 1 \text{ k}\Omega$	1.7	3.8			uБ		

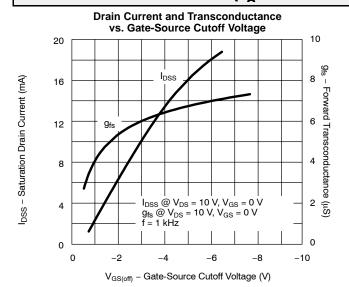
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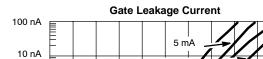
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

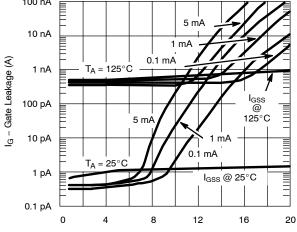
Notes a. Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing. b. Pulse test: PW  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.



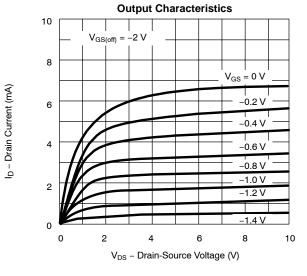
## TYPICAL CHARACTERISTICS (TA = 25°C UNLESS OTHERWISE NOTED)

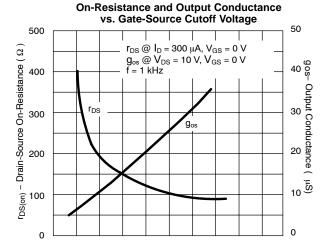






V<sub>DG</sub> - Drain-Gate Voltage (V)





V<sub>GS(off)</sub> - Gate-Source Cutoff Voltage (V)

-6

-8

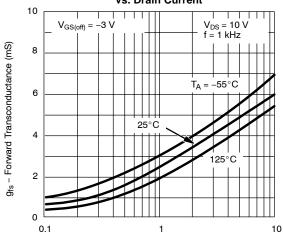
-10

-4

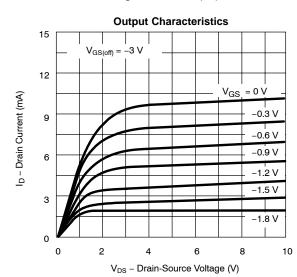
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-2

#### **Common-Source Forward Transconductance** vs. Drain Current

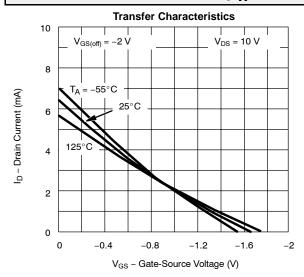


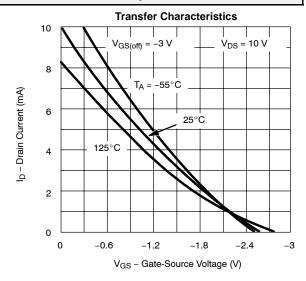
I<sub>D</sub> - Drain Current (mA)

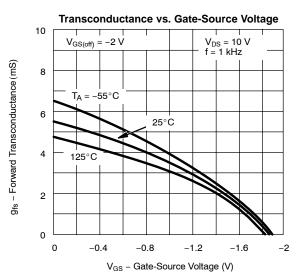


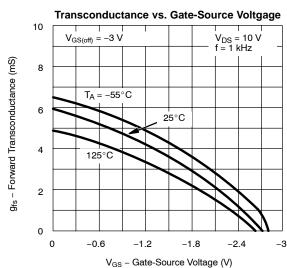


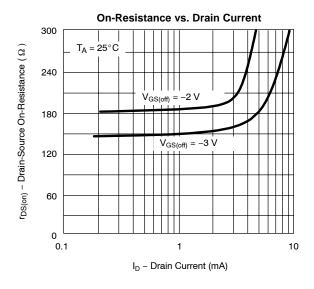
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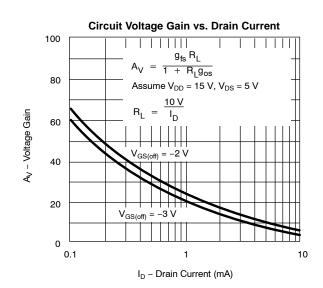






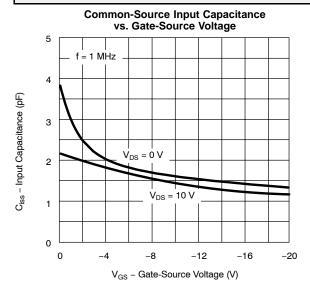


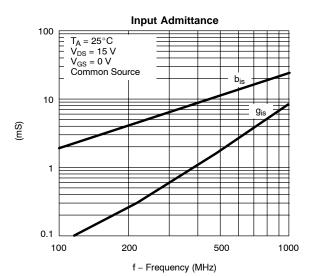


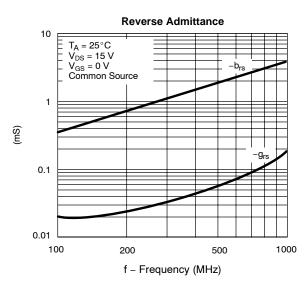


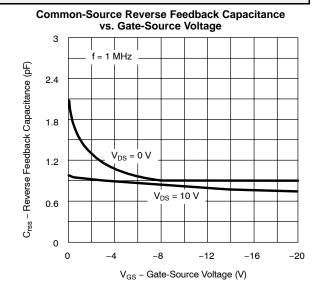


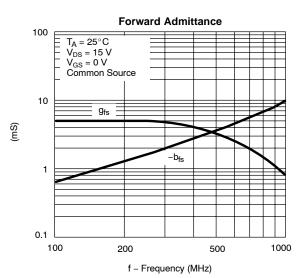
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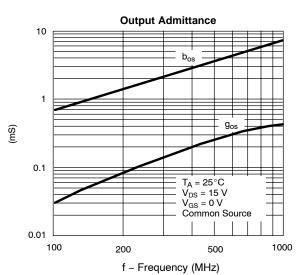






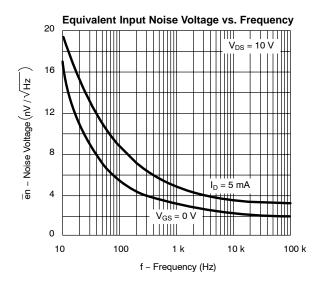


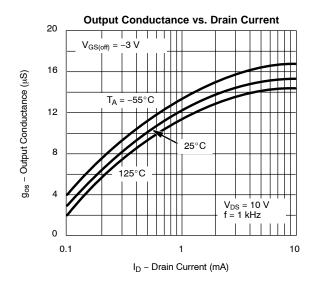






# TYPICAL CHARACTERISTICS (TA = 25°C UNLESS OTHERWISE NOTED)





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