Low Dropout Voltage

250mA CMOS LDO Regulator

CE6209 Series

INTRODUCTION

The CE6209 Series are a group of positive voltage regulators manufactured by CMOS technologies with high ripple rejection, extremely low power consumption (3.0µA Typ.) and low dropout voltage, which provide large output currents even when the difference of the input-output voltage is small. Thus the CE6209 series are very suitable for the battery-powered equipments, such as portable/palm computers, portable consumer equipments, industry equipments and so on, which want to prolong the using life of the battery.

FEATURES

- Standby Current: <0.1µA
- Output Current: 250mA
- Output Voltage Range: 1.4V \sim 5.0V,(selectable in 0.1V steps)
- High Accuracy: ±2%(Typ.)
- Low Dropout Voltage: 160mV@100mA (3.0V Typ.)
- Excellent Line Regulation: 0.1%/V
- High Ripple Rejection: 60dB @1KHz
- **Built-in Current Limiter**
- **Built-in Short Circuit Protection**
- Static safety: 2KV@HBM
- TC: 100ppm/°C
- Ceramic Capacitor Compatible

APPLICATIONS

- Battery powered systems
- Portable instrumentations
- Radio control systems

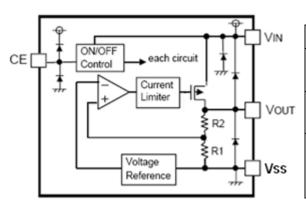
Portable consumer equipments

ORDER INFORMATION

- Portable/Palm computers
- Reference Voltage Sources

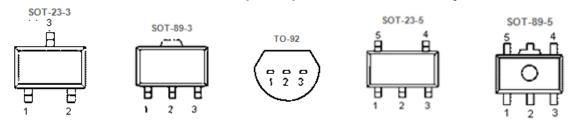
BLOCK DIAGRAM

CE6209(1)(2)(3)(4)



DESIGNATOR	SYMBOL	DESCRIPITION	
(1)	Α	Standard	
	В	With Shutdown Function	
23	Intogor	Output Voltage (0.9~5V)	
	Integer	e.g:3.0V=②:3, ③:0	
	М	Package:SOT-23-3/5	
4	Р	Package:SOT-89-3/5	
	Т	Package:TO-92	

■ PIN CONFIGURATION(Pin output sequence can be ordered by customer)



PIN NUMBER								
	SOT-23-3		SOT-89-3 TO-92		PIN NAME	FUNCTION		
М	MA	МС	MY	Р	PT	Т		
1	2	3	3	1	2	1	V _{SS}	Ground
2	1	2	1	3	1	3	V _{OUT}	Output
3	3	1	2	2	3	2	V _{IN}	Power input

SOT-23-5

PIN NUMBER	SYMBOL FUNCTION	
1	V _{IN}	Power Input Pin
2	V _{SS}	Ground
3	CE	Chip Enable Pin
4	NC	No Connection
5	V _{OUT}	Output Pin

SOT-89-5

PIN NUMBER	SYMBOL	FUNCTION	
1	V _{OUT}	Output Pin	
2	V _{SS}	Ground	
3	NC	No Connection	
4	CE	Chip Enable Pin	
5	V _{IN}	Power Input Pin	

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNITS
Input Vol	tage	V_{IN}	V _{SS} -0.3~V _{SS} +8	V
Output Cu	ırrent	I _{OUT}	500	mA
Output Vo	ltage	V_{OUT}	V _{SS} -0.3~V _{IN} +0.3	V
	SOT-23	Pd	250	mW
Power Dissipation	SOT-89	Pd	500	mW
	TO-92	Pd	500	mW
Operating Temperature		T _{opr}	-40~+85	$^{\circ}$ C
Storage Temperature		T_{stg}	-40~+125	$^{\circ}$ C
Soldering Temper	ature & Time	T _{solder}	260℃, 10s	

V2.0 2 (11)



■ ELECTRICAL CHARACTERISTICS

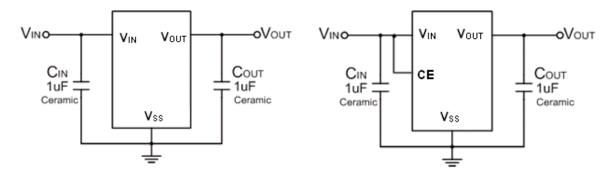
CE6209 Series $(V_{IN}=V_{OUT}+1V, C_{IN}=C_{OUT}=1\mu F, Ta=25^{\circ}C, unless otherwise specified)$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Output Voltage	V _{OUT} (E) (Note 2)	I _{OUT} =40mA V _{IN} = V _{OUT} +1V	V _{OUT} *0.98	V _{OUT} (Note 1)	V _{OUT} *1.02	V
Supply Current	I _{SS}	V _{CE} =V _{IN} =Vout+1V		3		μA
Shutdown Current	I _{SHDN}	$V_{CE} = V_{SS}$		0.1	1.0	μA
Output Current	I _{OUT}	V _{IN} ≥2.4V, V _{IN} =V _{OUT} +1V	250			mA
Dropout Voltage	V_{dif1}	$I_{OUT} = 40 \text{mA}$		80		mV
(Note 3)	V _{dif2}	I _{OUT} = 100mA		160		mV
Load Regulation	ΔV_{OUT}	V _{IN} = V _{OUT} +1V, 1mA≤I _{OUT} ≤100mA		15	40	mV
Line Regulation	$\frac{\Delta V_{OUT}}{\Delta V_{IN} * V_{OUT}}$	$I_{OUT} = 40 \text{mA}$ $V_{OUT} + 1V \le V_{IN} \le 6V$		0.1	0.2	%/V
Output Voltage Temperature Characteristics	$\Delta V_{OUT} \over \Delta T * V_{OUT}$	I _{OUT} = 40mA -40≤T≤+85		100		ppm/℃
Short Current	I _{Short}	V _{OUT} = V _{SS}		50		mA
Input Voltage	V _{IN}	_	2.4		6.0	V
CE "High" Voltage	V _{CE} "H"	_	1.5		V _{IN}	V
CE "Low" Voltage	V _{CE} "L"				0.3	V

NOTE:

- 1. V_{OUT}: Specified Output Voltage.
- 2. V_{OUT} (E): Effective Output Voltage (I.e. The Output Voltage When V_{IN} = (V_{OUT} +1.0V) And Maintain A Certain I_{OUT} Value).
- 3. V_{diff} : The Difference Of Output Voltage And Input Voltage When Input Voltage Is Decreased Gradually Till Output Voltage Equals 98% Of V_{OUT} (E); When V_{OUT} <2.4V, V_{IN} >2.4V Should be Guaranteed.

■ TYPICAL APPLICATION CIRCUIT

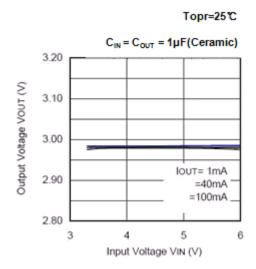


CHIPOWER

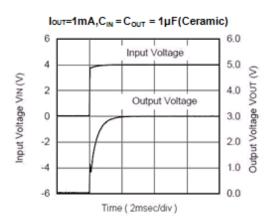
■ TYPICAL PERFORMANCE CHARACTERISTICS (CE6209A30P, for instance)

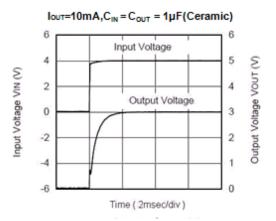
(1) Output Voltage vs. Input Voltage

C_{IN} = C_{OUT} = 1μF(Ceramic)
3.1
2.9
2.8
2.7
2.6
2
3.0
4
Input Voltage VIN (V)

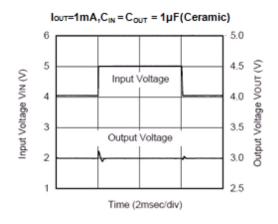


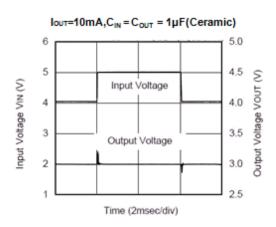
(2) Input Transient Response 1



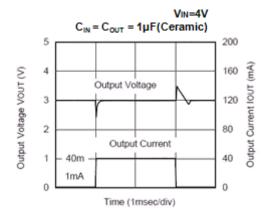


(3) Input Transient Response 2

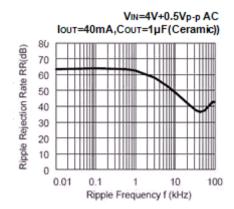




(4) Load Transient Response

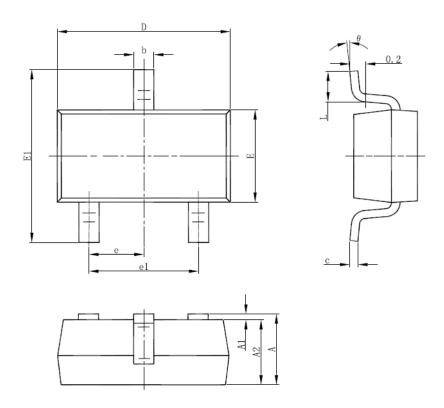


(5) Ripple Rejection Rate



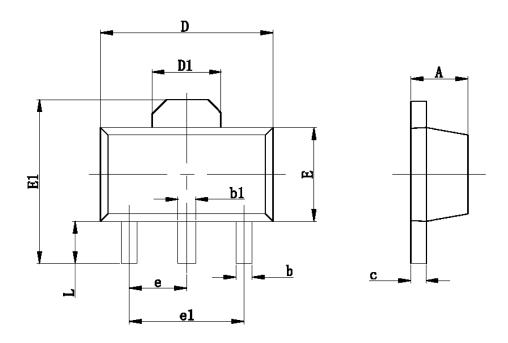
■ PACKAGING INFORMATION

• SOT-23-3 PACKAGE OUTLINE DIMENSIONS



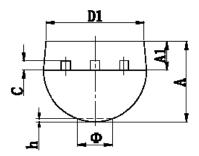
Ch a l	Dimensions In	n Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

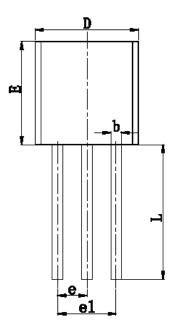
• SOT-89-3 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Syllibol	Min	Max	Min	Max	
Α	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.197	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550 REF		0.061	REF	
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
е	1.500 TYP		0.060TYP		
e1	3.000) TYP	0.118TYP		
L	0.900	1.200	0.035	0.047	

• TO-92 PACKAGE OUTLINE DIMENSIONS

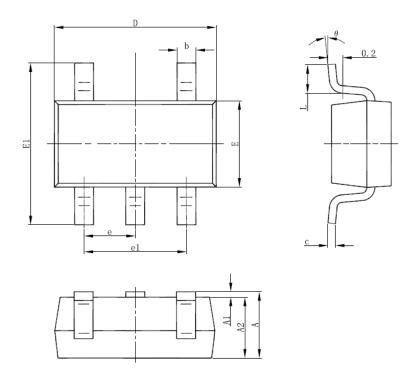




Symbol	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	3.300	3.700	0.130	0.146
A1	1.100	1.400	0.043	0.055
b	0.380	0.550	0.015	0.022
С	0.360	0.510	0.014	0.020
D	4.400	4.700	0.173	0.185
D1	3.430		0.135	
E	4.300	4.700	0.169	0.185
е	1.270 TYP		0.050) TYP
e1	2.440	2.640	0.096	0.104
L	14.100	14.500	0.555	0.571
Ф		1.600		0.063
h	0.000	0.380	0.000	0.015

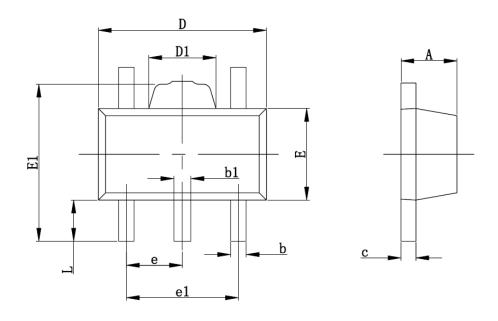
CHIPOWER TECHNOLOGY

• SOT-23-5 PACKAGE OUTLINE DIMENSIONS



Cumb a l	Dimensions In	Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

• SOT-89-5 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
Syllibol	Min	Max	Min	Max
Α	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.360	0.560	0.014	0.022
С	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.400	1.800	0.055	0.071
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
е	1.500	TYP	0.060TYP	
e1	2.900	3.100	0.114	0.122
L	0.900	1.100	0.035	0.043

© Nanjing Chipower Electronics Inc.

Chipower cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Chipower product. No circuit patent license, copyrights or other intellectual property rights are implied. Chipower reserves the right to make changes to their products or specifications without notice. Customers are advised to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete.

