

D



B



File: <Doc>

Create Date: Monday, October 22, 2018

Page Num:0

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I2S&LCDC

ADC

ADC

J2606

SARADC_IN1

ADC0_HW_ID

VCC_18

1

2

3

4

WAFER_4PIN_2.0

VCC_18

C263 4.7uF X5R C0603

C264 100nF C0402

10V

10V

1

2

1

2

SARADC_IN1

ED47 ESD5451N E0402

C257 1nF C0402

X5R 16V

1

2

ADC0_HW_ID

ED48 ESD5451N E0402

C259 1nF C0402

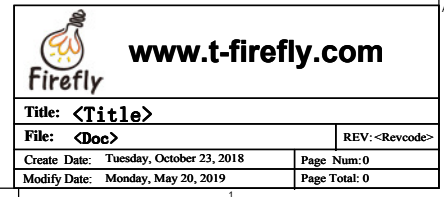
X5R 16V

1

2


[illegible]

I2C

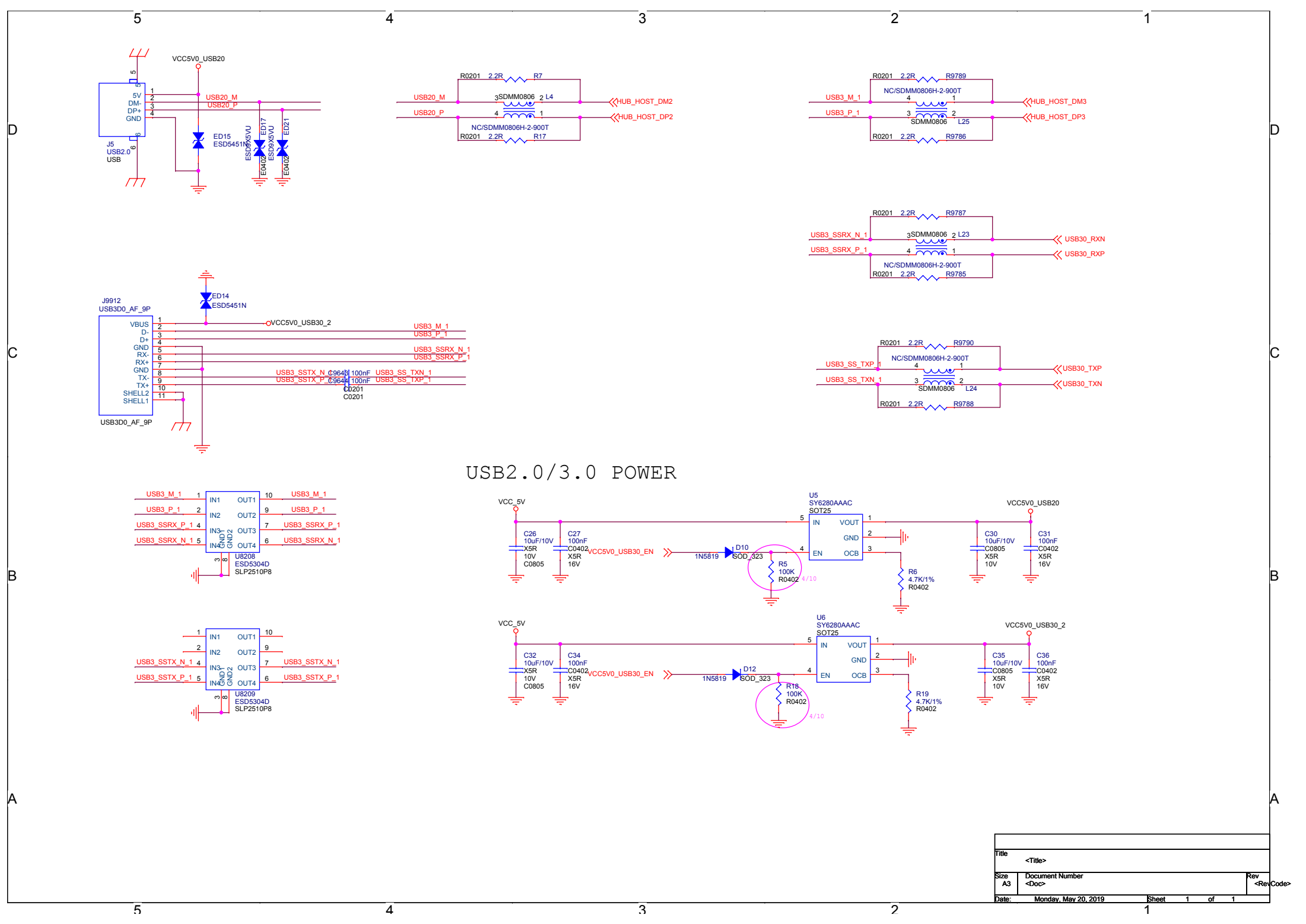


Modify note

Version	Date	Author	Change Note	Approved
V00	20180428	WXZ	First edictor	

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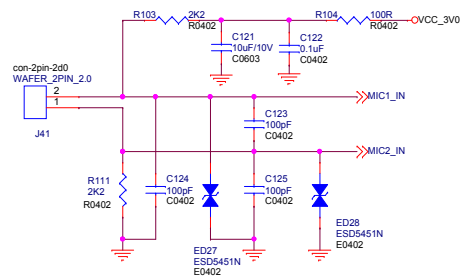
Title: Modify note	
File: A10-3399C	REV: V1.0
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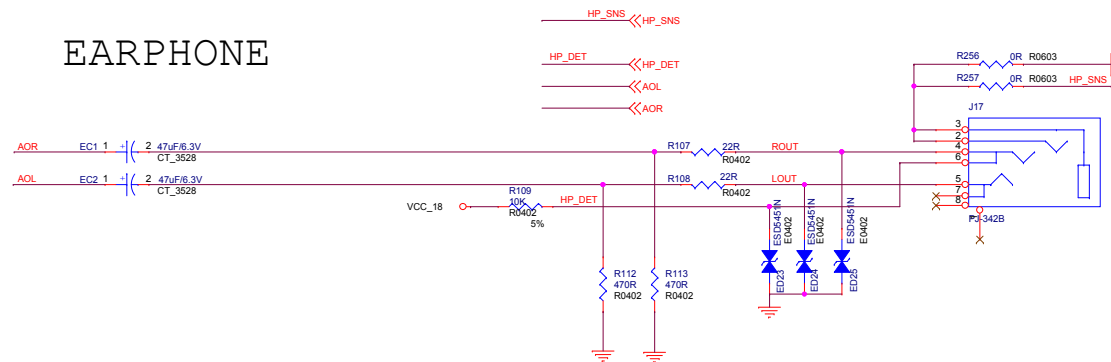
USB2.0/3.0 POWER

Title		
<Title>		
Size	Document Number	Rev
A3	<Doc>	<RevCode>
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MIC0



EARPHONE



www.t-firefly.com

Title: Audio Codec-ALC5640

File: A10_3399J

REV: V10

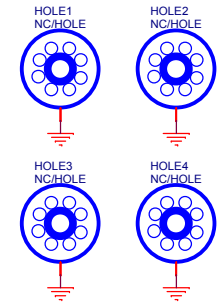
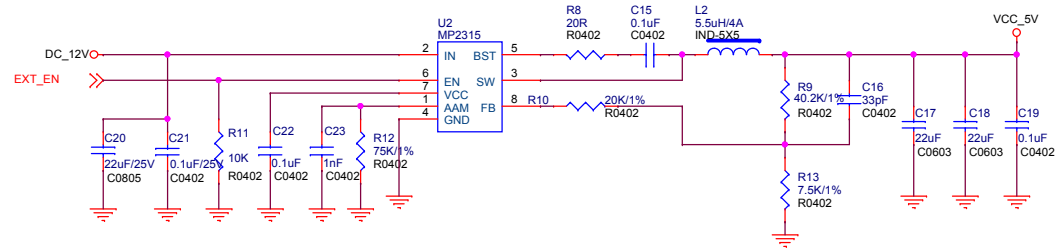
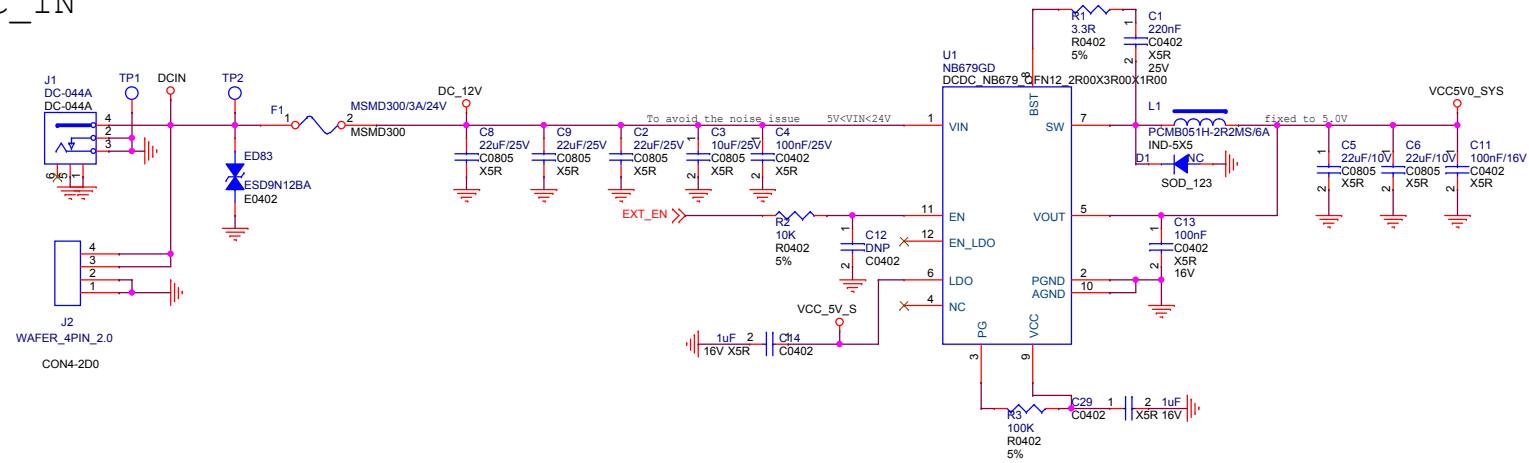
Create Date: Monday, October 22, 2018

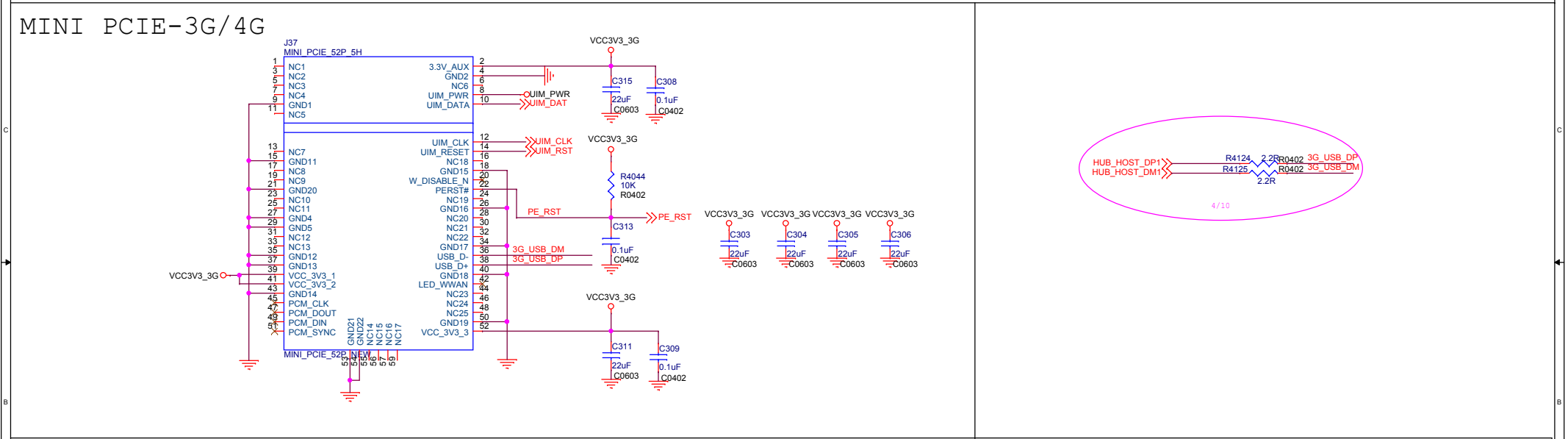
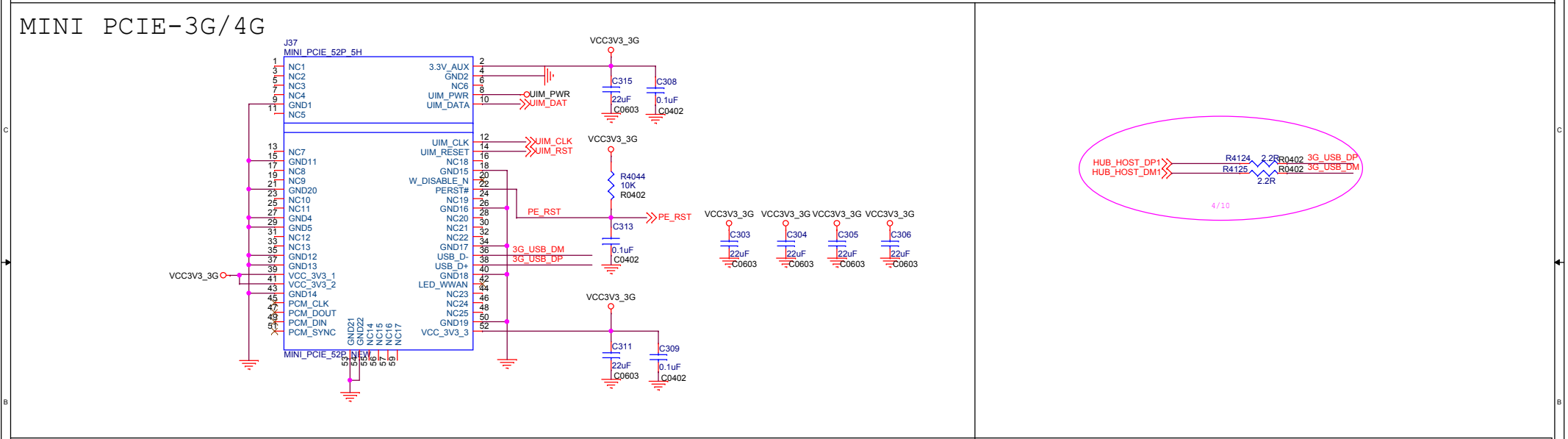
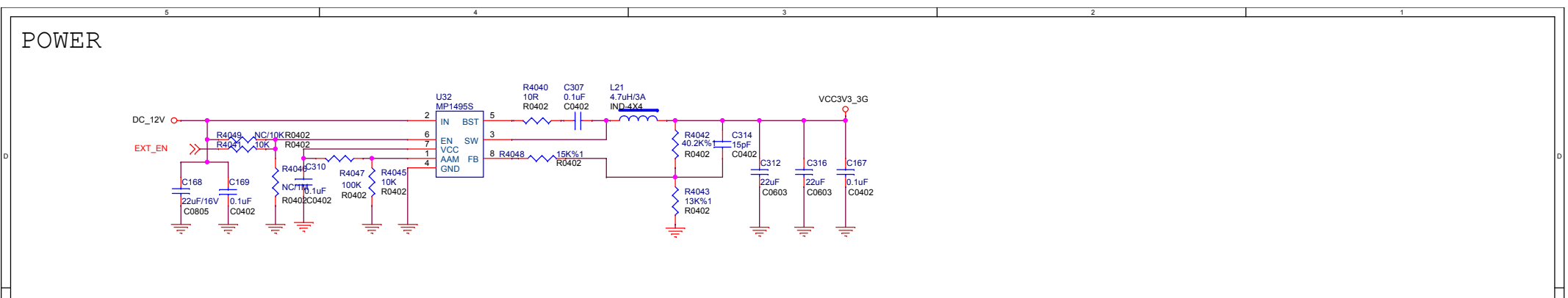
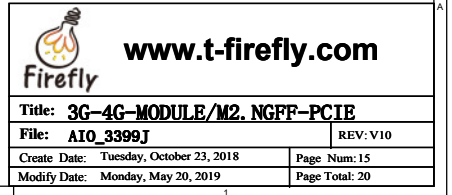
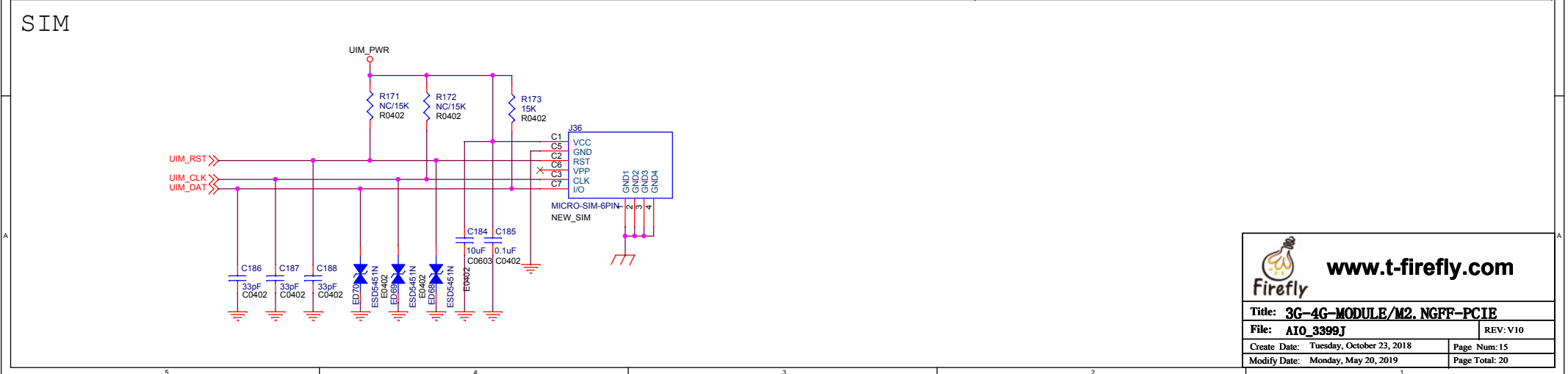
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DC_IN





[illegible][illegible]


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Title: 3G-4G-MODULE/M2. NGFF-PCIE	
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 www.t-firefly.com	
Title: 3G-4G-MODULE/M2. NGFF-PCIE	
File: ATO_3399J	REV: V10
Create Date: Tuesday, October 23, 2018	Page Num: 15
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 www.t-firefly.com	
Title: 3G-4G-MODULE/M2. NGFF-PCIE	
File: ATO_3399J	REV: V10
Create Date: Tuesday, October 23, 2018	Page Num: 15
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 www.t-firefly.com	
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File: ATO_3399J	REV: V10
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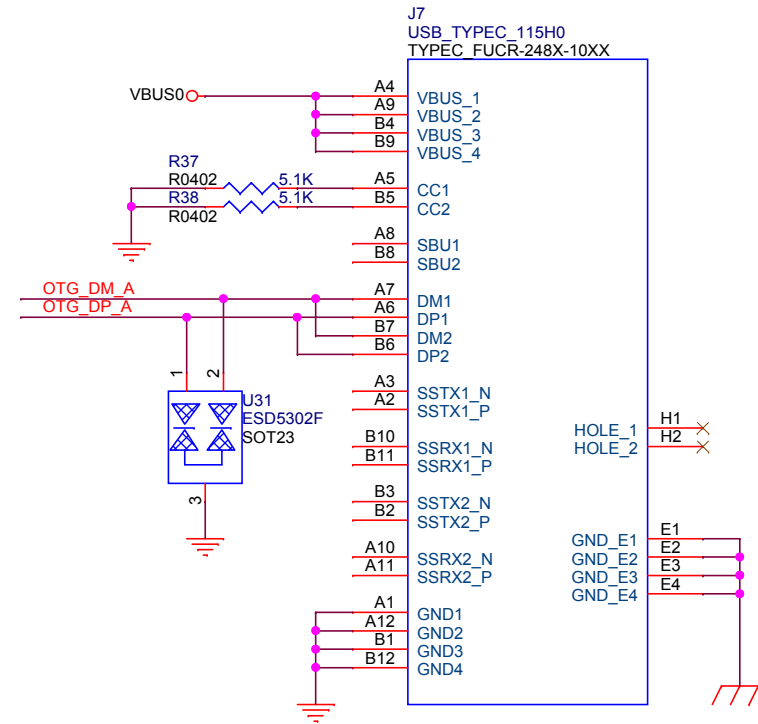
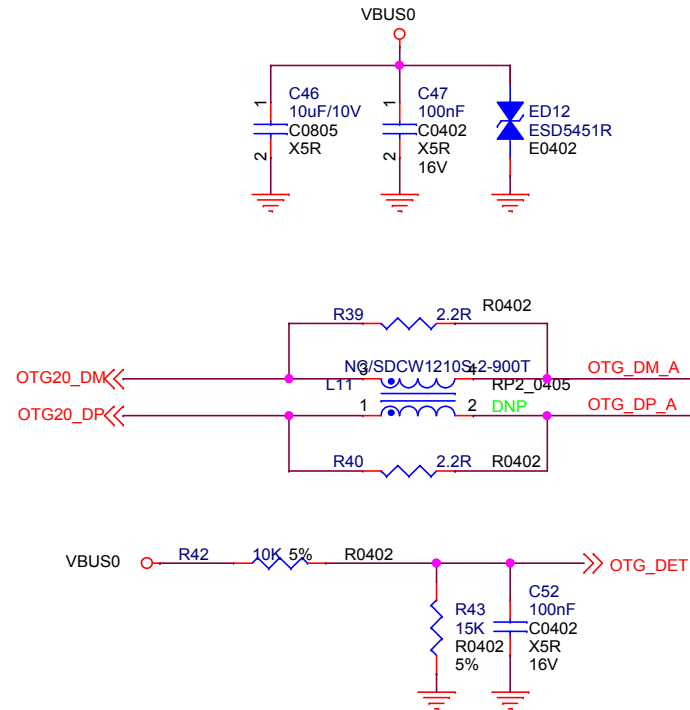
 www.t-firefly.com	
Title: 3G-4G-MODULE/M2. NGFF-PCIE	
File: ATO_3399J	REV: V10
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D



A

TYPE-C



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Title: TYPE-C0/USB3.0 Port

File:

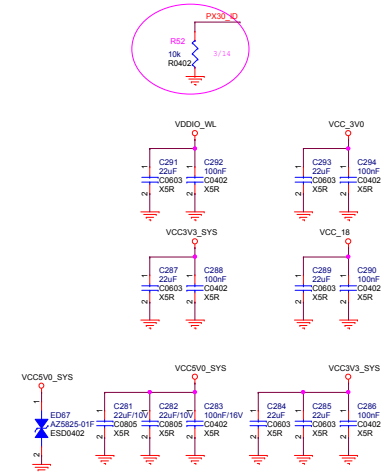
REV: V1.0

Create Date: Thursday, October 18, 2018

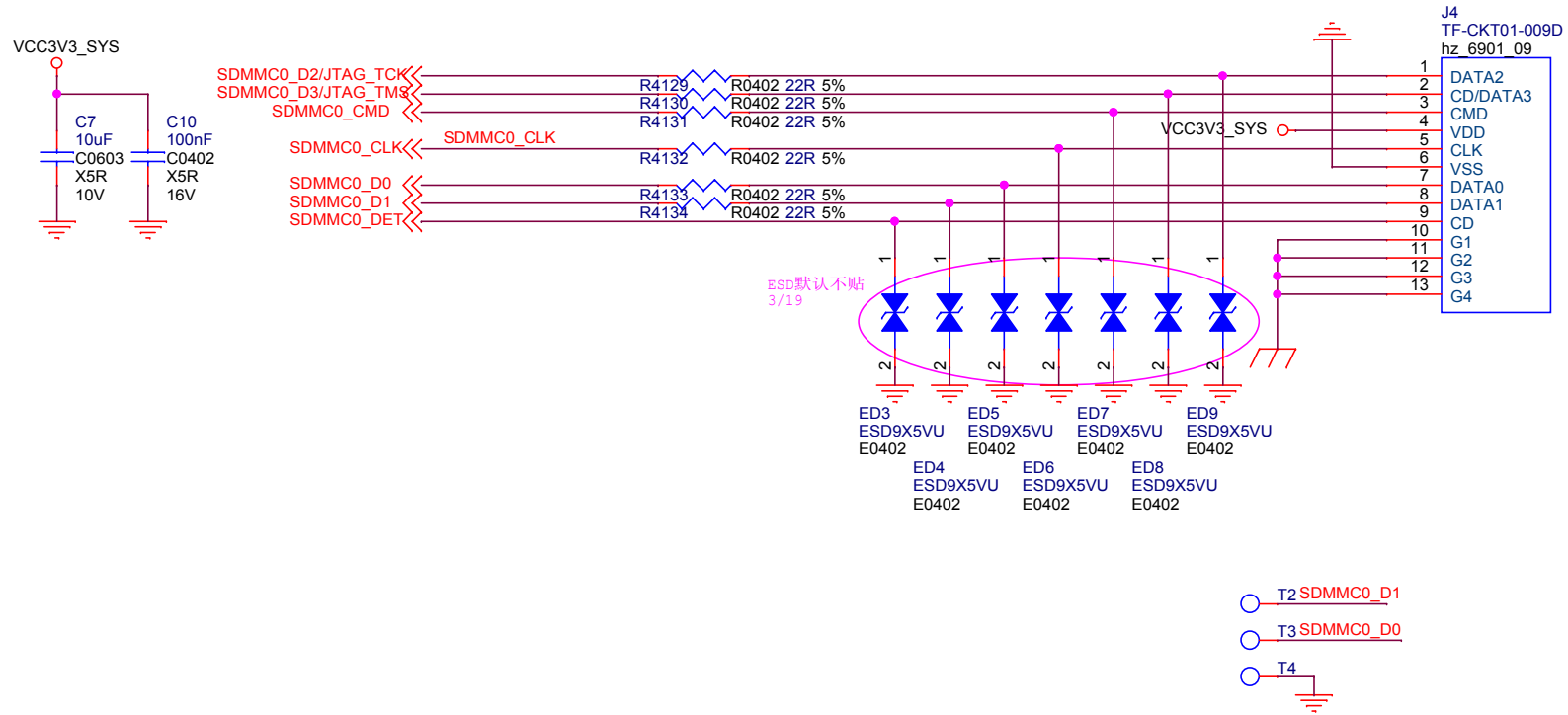
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TF CARD



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Title: TF Card/EFUSE

File: AIO-3399C

REV: V1.0

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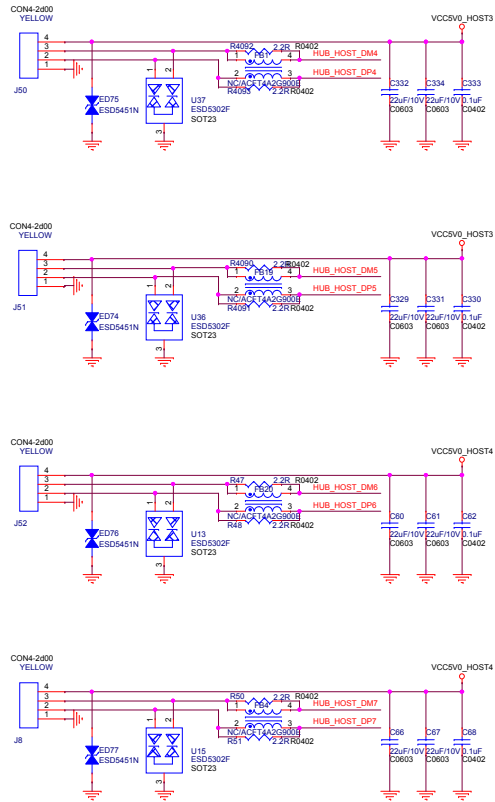
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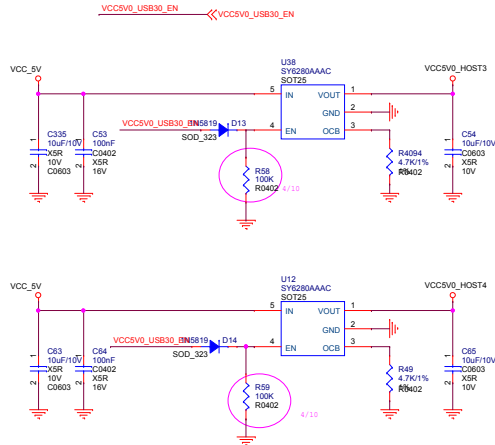
A



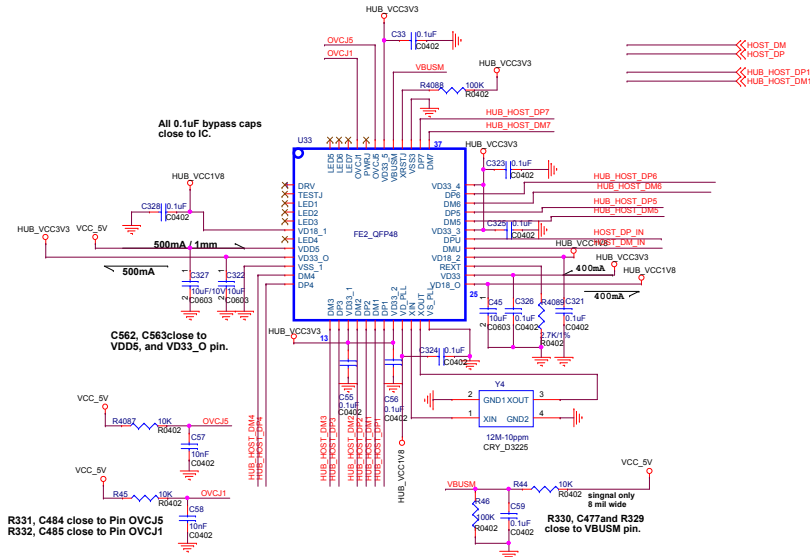
HOST 2.0



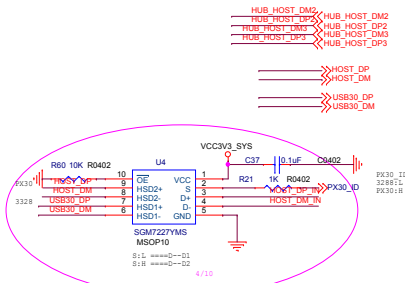
HOST POWER



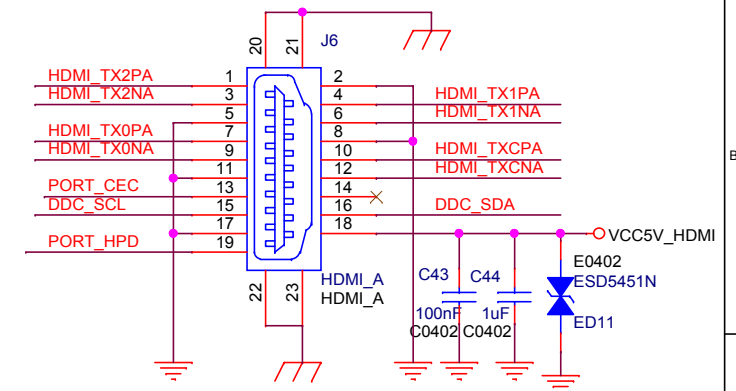
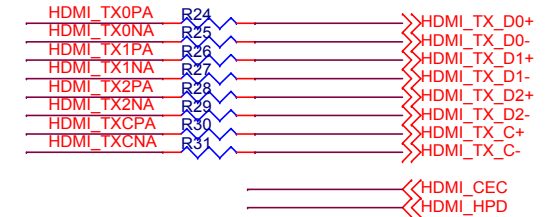
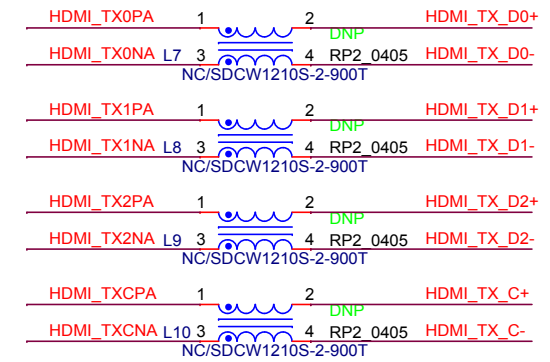
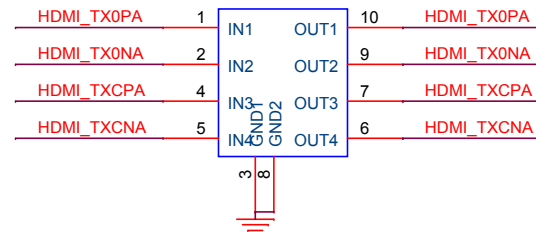
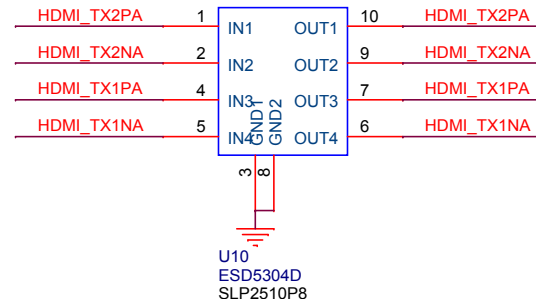
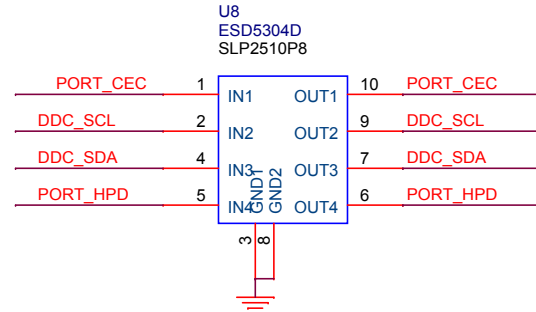
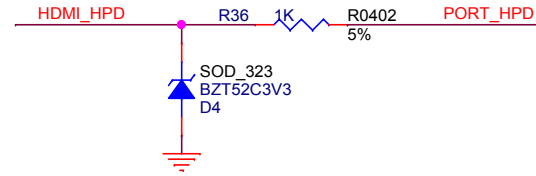
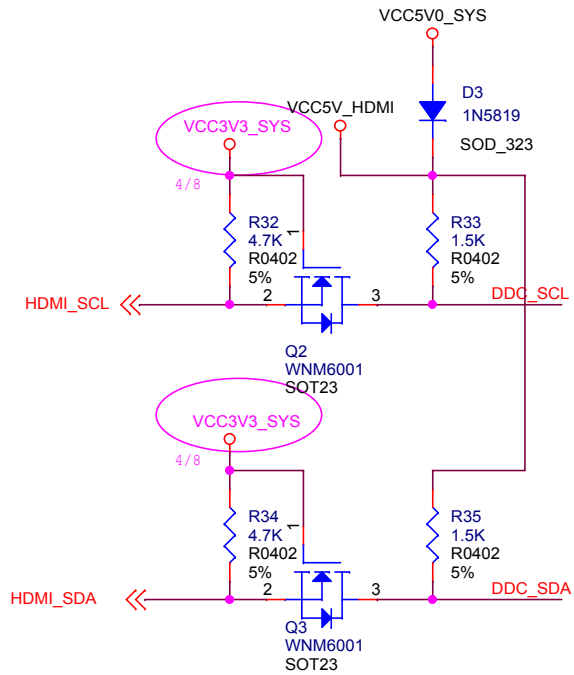
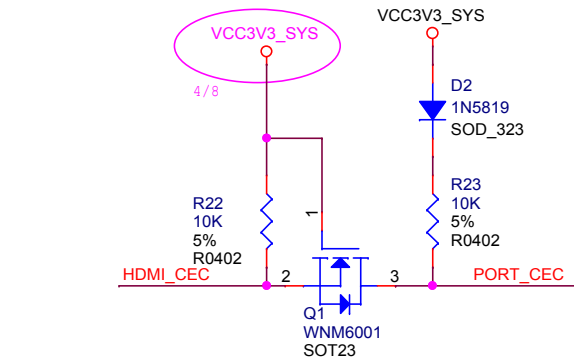
HUB



DEBUG SWITCH



HDMI Port



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Title: HDMI Port

File: AIO-3399C

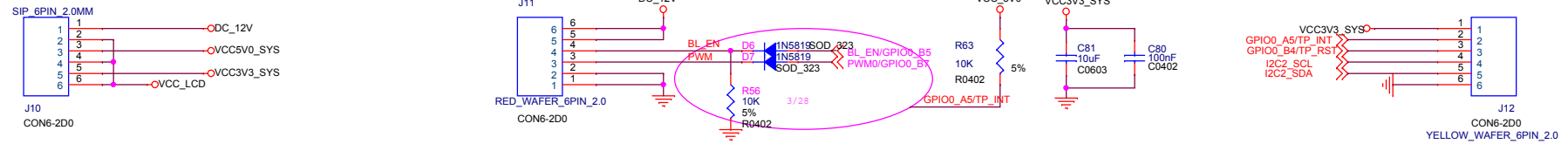
REV: V1.0

Create Date: Thursday, October 18, 2018

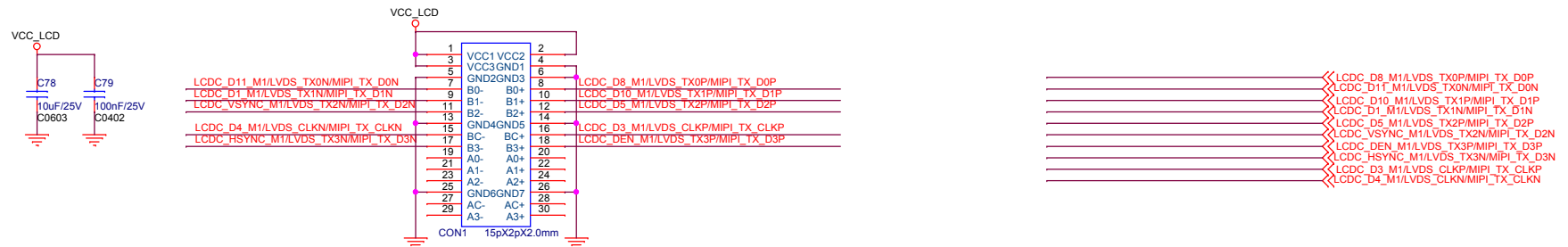
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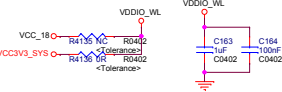
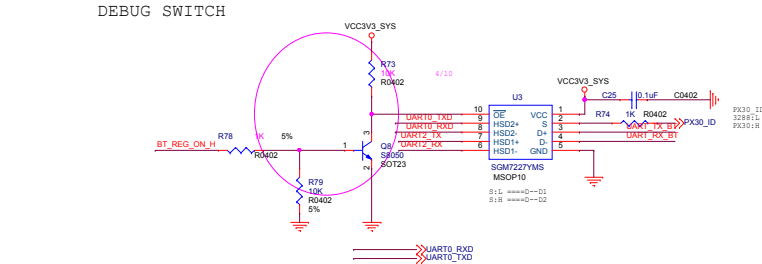
MIPI/LVDS Display Interface



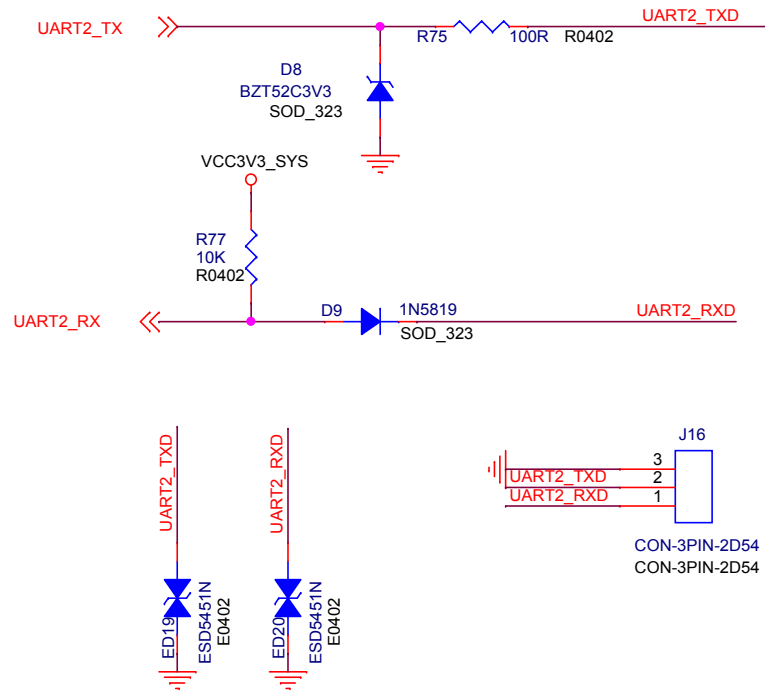
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AP6255	YES	37.4MHz	Note:
AP6212A	NO	26MHz	YES:框内要贴 NO:框内不贴

	Option2	Y6101	Default:AP6255
AP6255	YES	37.4MHz	Note:
AP6212A	NO	26MHz	YES:框内要贴 NO:框内不贴

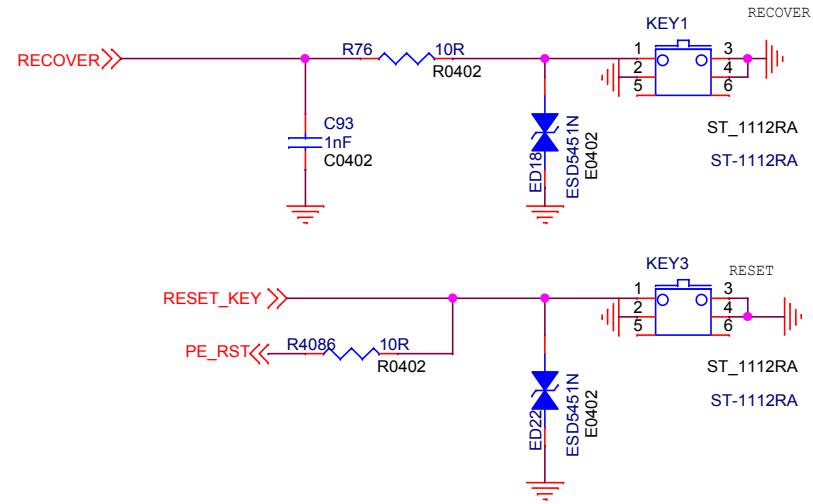
具体产品，天线按实际需求更换；
50 Ohm RF trace



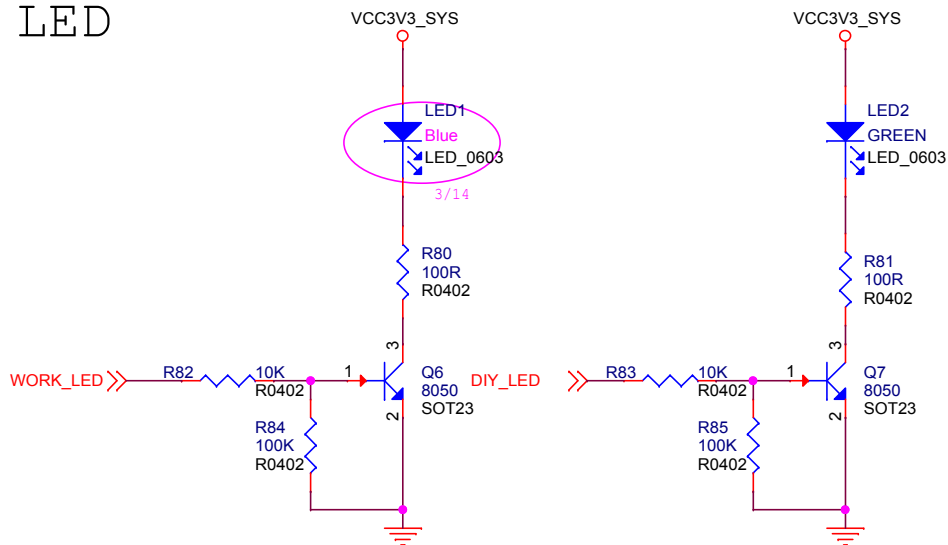
DEBUG



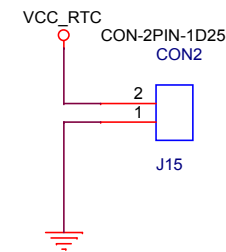
KEY



LED



RTC_BAT



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Title: KEY/LED/DEBUG

File: AIO-3399C

REV: V1.0

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