CS260-001: Computer Security

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## Problem: Implementing a pintool for shadow stack

Objective for this lab assignment is to implement a shadow stack through dynamic binary instrumentation using Pin.

## Solution

Shadow stack is a well-known and effective defense mechanism to defeat control-flow hijacking attacks that aim to overwrite a return address on the stack. The general algorithm works like: For each call instruction, identify the return address, and push it onto the shadow stack. For each ret instruction, identify the return target and see if it matches with the value on the top of the shadow stack. If so, pop up the value from the shadow stack otherwise, report an attack.

In Figure 1 you can see the Trace implementation of detecting the instruction to be of calling or returning type. We are going from block to block and for each tail instruction we are checking that whether it is calling or returning instruction. Iterating over blocks and checking only tail instructions makes this process fast by not going over each instructions. After checking we are executing based on the type of instruction, If it is a calling instruction we are passing it to the **do\_ret** analysis section.

In the Figure 2 you can see the implementation of both do\_call and do\_ret analysis section. In do\_call section I am pushing the return address (IARG\_RETURN\_IP) parameter to the stack called *shadow*. In do\_ret analysis section I am checking whether the top of *shadow* stack is same as the returning address(IARG\_BRANCH\_TARGET\_ADDR). If they are the same value then it is okay to proceed and buffer overflow exploit has not been used. I am also maintaining a global parameter isOkay which can extended to stop the further execution. If the addresses are not same I am printing the error and setting isOkay to false because return address has been changed by buffer overflow exploit.

Figure 3, 4 and 5 are the executions of compiled code. In Figure 2 & 3, I tested the implementation by calling /bin/ls and /bin/ps whose output did not raised any error and produced the correct expected output. In Figure 5, I did 3 tests, First I checked whether code is giving Access denied message when the input is wrong password, then I checked if Access Granted is showed when inputed with correct password and at last I tested by overflowing the input buffer. Which as expected printing Error message that stack values does not match and segmentation fault is occurring which can be prevented using isOkay variable but I have not implemented it as it was not required.

Another thing which can be extended as well is maintaining shadow stacks for multi-threaded system. **IARG\_THREAD\_ID** can be used to maintain a global dictionary which can then be mapped to a particular shadow stack which will maintain the return address for that particular thread and rest of the implementation is same as for a single threaded system.

Figure 1: TRACE section implementation.

Figure 2: do\_call and do\_ret analysis section implementation.

```
$ ../../../pin -t obj-intel64/calltrace.so -- /bin/ls
bsr_bsf_app.cpp calltrace.out example01
                                                              ilenmix.cpp
                                                                                    malloctrace.cpp
                                                                                                                                        shadowstack.cpp
                                                                                                           oper-imm.cpp
bsr_bsf_asm.asm
                    catmix.cpp
                                    extmix.cpp
                                                              inscount2_mt.cpp
                                                                                    objdump-routine.csh oper-imm.ia32.reference
                                                                                                                                        shadowstack.out
bsr_bsf_asm.s
                                                              inscount2_vregs.cpp obj-intel64
                                                                                                           oper-imm.intel64.reference topopcode.cpp
                    coco.cpp
                                    fence.cpp
bsr_bsf.cpp
                    dcache.cpp
                                    flowgraph.py
                                                               jumpmix.cpp
                                                                                    opcodemix.cpp
                                                                                                           pinatrace.cpp
                                                                                                                                         toprtn.cpp
                                    get_source_app.cpp
                                                              ldstmix.cpp
                                                                                    oper_imm_app.cpp
                                                                                                                                        trace.cpp
bsr_bsf.reference
                                                                                                           regmix.cpp
                   dcache.H
                                    get_source_location.cpp
                                                                                    oper_imm_asm.asm
                                                                                                           regval_app.cpp
callgraph.py
                    edgcnt.cpp
                                                              makefile
                                                                                                                                        xed-print.cpp
                                                              makefile.rules
calltrace.cpp
                    emuload.cpp
                                    icount.cpp
                                                                                    oper_imm_asm.s
                                                                                                           regval.cpp
                                                                                                                                        xed-use.cpp
```

Figure 3: Output of /bin/ls test with compiled code.

```
$
../../pin -t obj-intel64/calltrace.so -- /bin/ps
PID TTY TIME CMD
5109 pts/0 00:00:00 bash
8053 pts/0 00:00:01 ps
$
$
```

Figure 4: Output of /bin/ps test with compiled code.

```
$ ../../pin -t obj-intel64/calltrace.so -- ./example01
Enter password:
abhishek
Access denied
$ ../../../pin -t obj-intel64/calltrace.so -- ./example01
Enter password:
goodpass
Access granted
$ $
$ ../.../pin -t obj-intel64/calltrace.so -- ./example01
Enter password:
asdfgaskdasdahfjkfhjdjfjgdasgdas
Error!! Stack Values Does not match!!
Segmentation fault (core dumped)
$ $
```

Figure 5: Outputs of normal and buffer overflow exploit testing.