# FCMkII DESIGN SCRATCH

### COMMUNICATIONS

STANDARDIZED PORT NUMBERS

S. Listener (Broadcast): 65000

S. MISO: 65001 S. MOSI: 65002

Breakdown		
Master		Slave
Parts		

## • Broadcast Thread

- Standard broadcast
  - Update broadcast
  - Shutdown broadcast
- Listener thread

NOTE: Here, responding Slaves can categorized are known and unknown

- List new, unknown Slaves for adding
- Mark known, disconnected Slaves for reconnection
- Ignore messages from connected Slaves
- Send startup messages to Bootloaders when appropriate
- Slave threads

Depending on target Slave status..

**DISCONNECTED:** Wait for Slave to be marked by Listener thread

**AVAILABLE:** Attempt handshake to connect. Mark as disconnected upon failure

**CONNECTED:** Listen for messages, count timeouts when applicable, fetch commands from user

• Listener thread:

Listen for broadcasts. Depending on broadcast type...

**Standard:** If disconnected, send reply; if connected, reset Master timeout counter.

\*Master timeout: Ping Master before assuming disconnection

\*Network timeout: Ping self before rebooting

Update broadcast: Shutdown Processor and reboot

Shutdown broadcast: Shutdown Processor and reboot

Launch application: (For Bootloader) Ignore when in MkII; launch MkII when in Bootloader

• MISO thread:

Send updates to Master when connected:

- Fetch updates from Processor, if any, or send empty message to maintain connection
- Send ping requests when flagged by Listener
- Remain idle when disconnected. NOTE: Empty processor Queue

## • MOSI thread:

- Listen for messages from Master (when connected) and add them to Processor buffer
- Reset timeout counter whenever a Message is received

#### Connection and Disconnection

- Use broadcast thread to keep Slave connected
- Use MISO-side of Slave thread to listen for periodic Slaveside updates to know when to assume disconnection
- NOTE: Send Disconnect message to Slave when assuming disconnection
- Send multiple MOSI messages (use index)

- Use listener thread to know if Master is still connected (based on broadcast)
- Ping Master when considering disconnection
- Send Disconnect message to Master when assuming disconnection
- Ping self to check network status before assuming network error and rebooting
- Also reset Master timeout counter upon reception in MOSI thread
- $\bullet$  Shutdown Processor when assuming disconnection from Master

MOSI MESSAGE FORMATS
MISO

#### Broadcast-side

- Standard broadcast:
   N|PASSCODE|M\_L\_PORT
- Update broadcast: U|PASSCODE|M\_L\_PORT|FILE\_NAME|FILE\_SIZE\_BYTES
- Shutdown broadcast:  $R \mid PASSCODE$
- Launch MkII: L|PASSCODE

- Standard broadcast reply (MkII):

  <u>A|PASSCODE|S\_MAC|N|S\_MISO\_P|S\_MOSI\_P|VERSION</u>
- Error (MkII Listener): A | PASSCODE | S\_MAC | E | ERROR\_MESSAGE
- Error (Bootloader): B|PASSCODE|S\_MAC|E|ERROR\_MESSAGE

#### Communications and Control

• Set DC:

Here each character in the string of zeroes corresponds to a fan in the target Slave's array. A '1' means the fan is to be set to the specified DC, and a '0' means it is to be left unchanged.

• Chase RPM:

See "Set DC" for the meaning of the string of zeroes.

• Handshake:

O|H|COMMS\_CONFIG|ARRAY\_CONFIG

 $\overline{\text{COMMS\_CONFIG}}$  is a comma-separated list with the following:

- 1. MISO port
- 2. MOSI port
- 3. Period (ms)
- 4. Max. Master timeouts

ARRAY\_CONFIG is a space-separated list with the following:

- 1. Fan mode
- 2. Num. active fans
- 3. PWM frequency (Hz)
- 4. Counter counts
- 5. Pulses per rotation
- 6. Max. RPM
- 7. Min. RPM
- 8. Min. DC
- 9. Chaser tolerance (%)
- 10. Max. fan timeout
- 11. PWM pinout
- 12. Tach. pinout
- Disconnect: MOSI\_INDEX|X
- Reboot:
- MOSI\_INDEX<u>|Z</u>
- Reset index: MOSI\_INDEX|I

Slave will reset its MOSI index to 0.

• Ping:

MOSI\_INDEX|P

• PSU:

MOSI\_INDEX|S|W:0

Here the last character will be a 1 or 0 depending on the desired PSU state (1 for ON and 0 for OFF). The PSU will be turned on upon startup and off upon shutdown and rebootby default.

• Maintain connection:

MISO\_INDEX | M

Sent to Master when there are no updates from Processor, but a MISO message is due to maintain connection.

• Standard update:

MISO\_INDEX | T | DATA\_INDEX | RPMS | DUTY\_CYCLES

Here DUTY\_CYCLES and RPMS are comma-separated lists of the DC and RPM values of each fan in the array, in order. Negative values will be used for RPMS of fans being "Chased."

• Error (MkII MISO): MISO\_INDEX|E|ERROR\_MESSAGE

• Handshakehspace.5emconfirmation:

MISO\_INDEX|H

For Slave-side exception handling and documenting.

Ping request: MISO\_INDEX|P

• MISO index reset:

MISO\_INDEX|I

Master will reset its MISO index to 0.

### Legend

- ${f N}$  "NORMAL" i.e. Standard broadcast
- U "UPDATE" i.e. Update broadcast
- ${f R}$  "REBOOT" i.e. Reboot MCU
- L "LAUNCH" i.e. Launch MkII
- S "STANDARD" i.e. Standard command for Processor
- **D** "DUTY CYCLE" i.e. Set Duty Cycle
- ${f C}$  "CHASE" i.e. Chase RPM
- H "HANDSHAKE" i.e. Handshake to start connection
- $\mathbf{X}$  "DISCONNECT" i.e. Assume disconnection (Shutdown Processor)
- **Z** "REBOOT" i.e. Reboot MCU
- I "INDEX" i.e. Reset MISO Index
- W "POWER" i.e. Power PSU

- A "APPLICATION" i.e. Message from MkII
- **B** "BOOTLOADER" i.e. Message from Bootloader
- M "MAINTAIN" i.e. Maintain connection
- T "STANDARD" i.e. Standard update message
- E "ERROR" i.e. Error message
- P "PING" i.e. Ping request
- I "INDEX" i.e. MISO index reset

### To Do

Mon. 6/25/18 - Tue. 6/25/18

- 1. Fix Bootloader 404 and empty file bugs
- 2. Add missing pinout, PSU pins and external LED pins
- 3. Add placeholder for runtime pinout configuration
- 4. Implement new message standard

Among other things...

- Receive S.Error and B.Error messages in both Slave threads and listener thread
- Use extra warnings in the event of a Bootloader error
- 5. Implement Slave self-pinging

Wed. 6/25/18 - Fri 6/29/18

- 1. Implement runtime pinout configuration
- 2. Implement 'efficient" tachometer
- 3. Implement Master-side firmware uploads
- 4. "Fully" modularize Master
- 5. Implement "verifications" and shutdown button
- 6. Implement user configuration and "null" settings
- 7. Implement multiprocessing

Mon. 7/2/18

- 1. Fix PWM resolution
- 2. Fix Chaser
- 3. Fix RPM spikes (if applicable)
- 4. Fix Processor thread-safety

Tue. 7/3/18 - Fri. 7/6/18

- 1. + Fix S-side data types
- 2. + Implement index resets (including dataIndex!)
- 3. + Strong processor checks
- 4. + PSU auto on/off setting
- 5. + Input and output sockets in Master
- 6. + Implement hotkeys
- 7. + Implement plotter
- 8. + Document
- 9. + Add "help" section
- $10.\ + \ Compile\ Master-side$
- $11.\ +$  Credits and licensing (And comments!)