

# Adam STENSON

## PERSONAL INFORMATION

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## EDUCATION

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### University of Rochester, Rochester, NY

MAY 2023	Master of Business Administration
<i>Expected</i>	Concentration: Technology Consulting
	Minor: Analytics
MAY 2019	Master of Science in Electrical Engineering
	Concentration: Computer Design & Computer Engineering
	Research Focus: Superconducting Single Photon Detector Digital Logic
MAY 2018	Bachelor of Science in Electrical & Computer Engineering
	Bachelor of Arts in Physics

## RECENT WORK EXPERIENCE

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### ALSTOM Signaling Inc

<i>Current</i>	Hardware Designer (January 2022 - Current)
JUN 2019	- Maintain and update PTC assembly engineering plans
	Software Designer (June 2021 - December 2021)
	- Performed code maintenance and upgrades to meet customer needs
	- Performed verification & validation activities through automated test scripts
	- Acted as a substitute Scrum Master
	Engineering Leadership Rotational Program (June 2019 - June 2021)
	- Rotational program to provide a system level understanding of Alstom products and interdisciplinary experience beneficial to engineering project management.
	- Rotations:
	- Software (June 2019 - January 2020)
	- Hardware (January 2020 - July 2020)
	- Verification & Validation (July 2020 - December 2020)
	- Factory Acceptance Test (January 2021 - June 2021)

### AS&E Information Technology Center, University of Rochester

JUN 2019	Head Student Supervisor (May 2017 - June 2019)
AUG 2014	- Oversee the operation of the student supervisor team
	- Interview job applicants and make hiring suggestions to full-time staff
	- Assist the full-time staff supervisors in the general management of the IT Center
	Technical Training Coordinator (May 2016 - May 2017)
	- Plan and execute training events for student staff
	- Assist with hiring and evaluation of student staff
	ResNet Consultant, Level 3 (Aug 2015 - Aug 2016), Level 1 (Aug 2014 - Aug)
	- Perform software and hardware troubleshooting on student, staff, and faculty devices

## COMPUTER SKILLS

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Programming Languages:	C, C++, Python, R, Verilog, VHDL
Other:	Microsoft Office, Mathematica, L <sup>A</sup> T <sub>E</sub> X, NI TestStand, Subversion