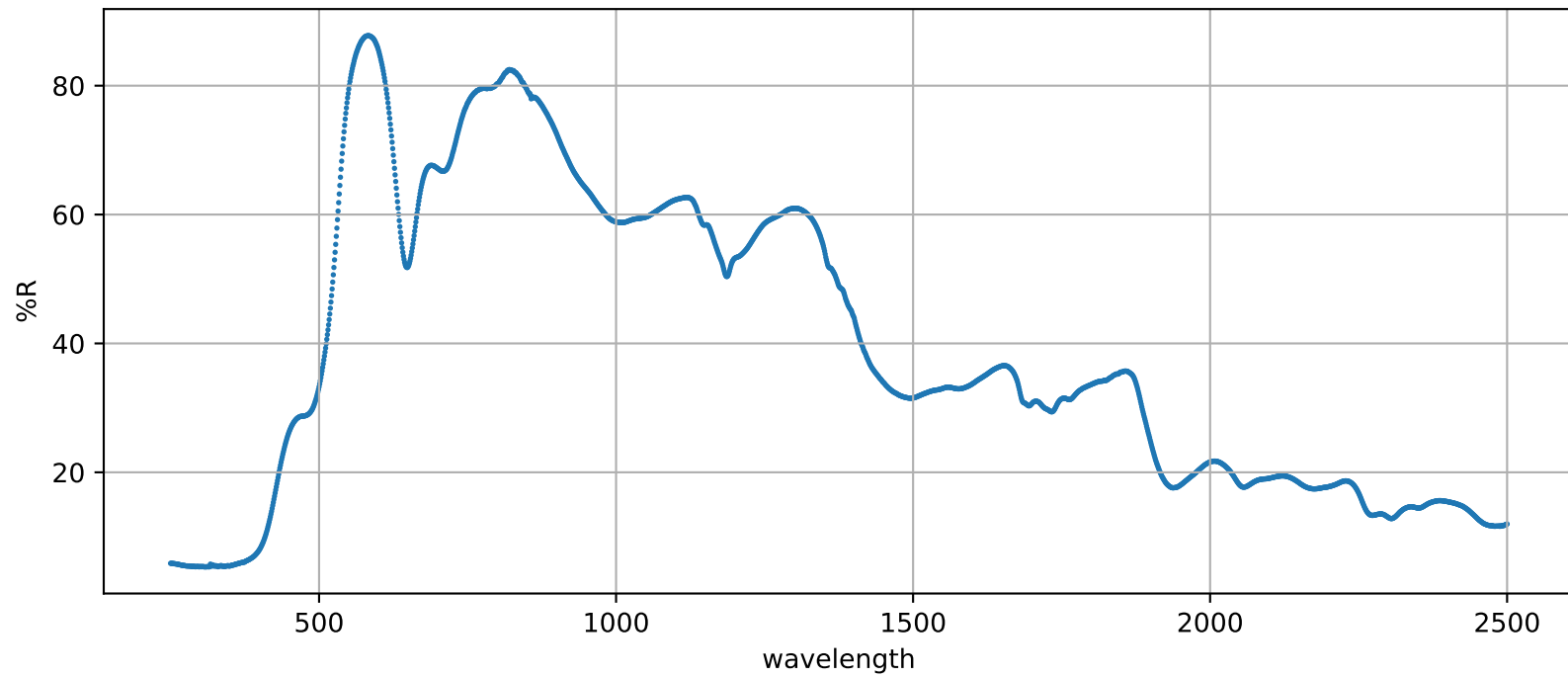
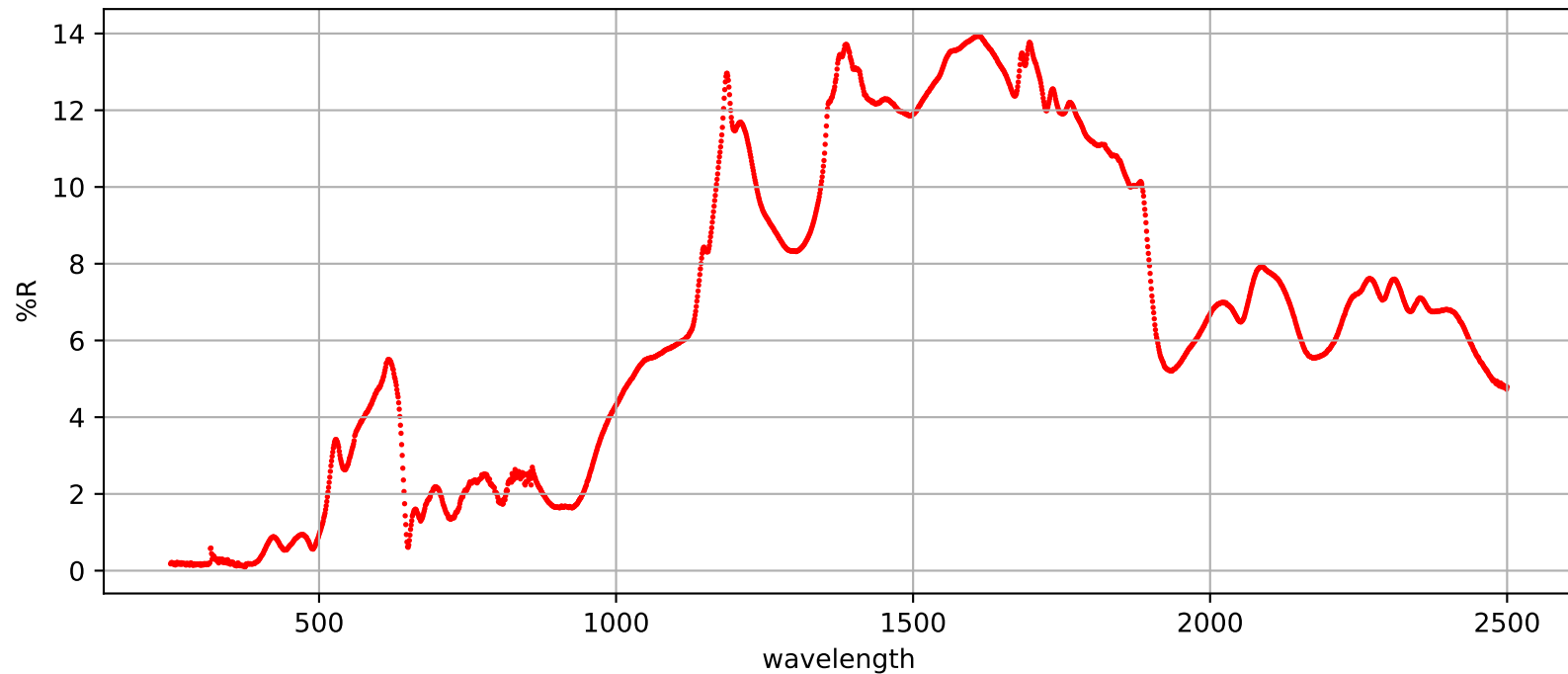


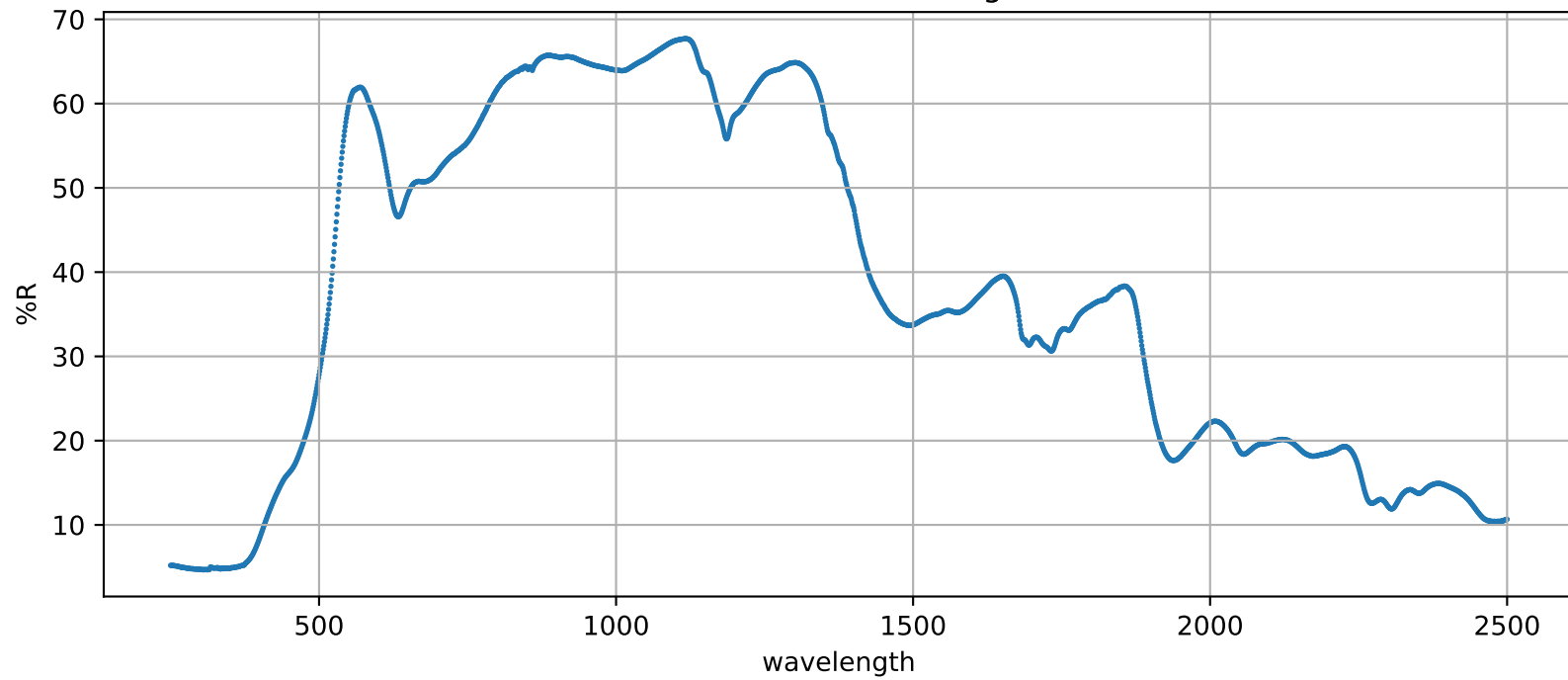
CICIMAUCR0108 avg



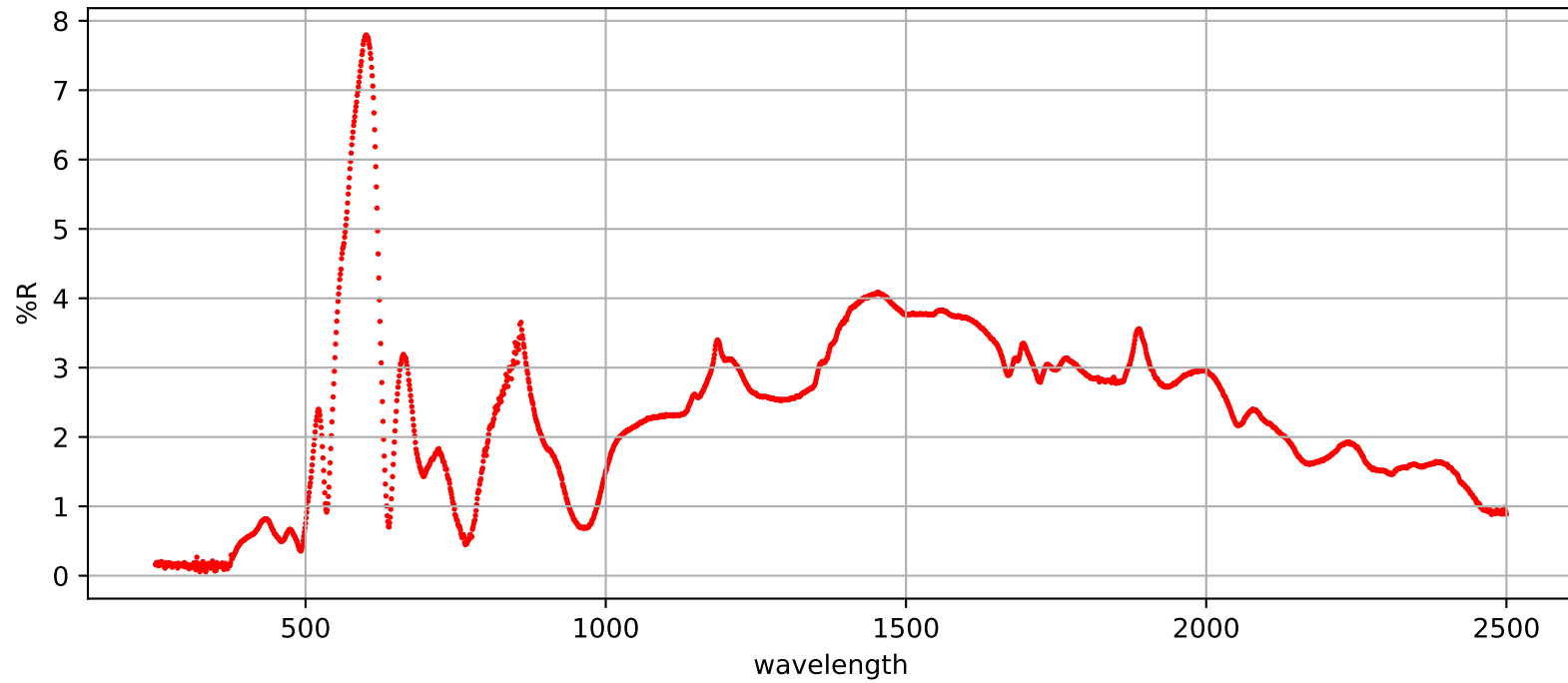
CICIMAUCR0108 std dev



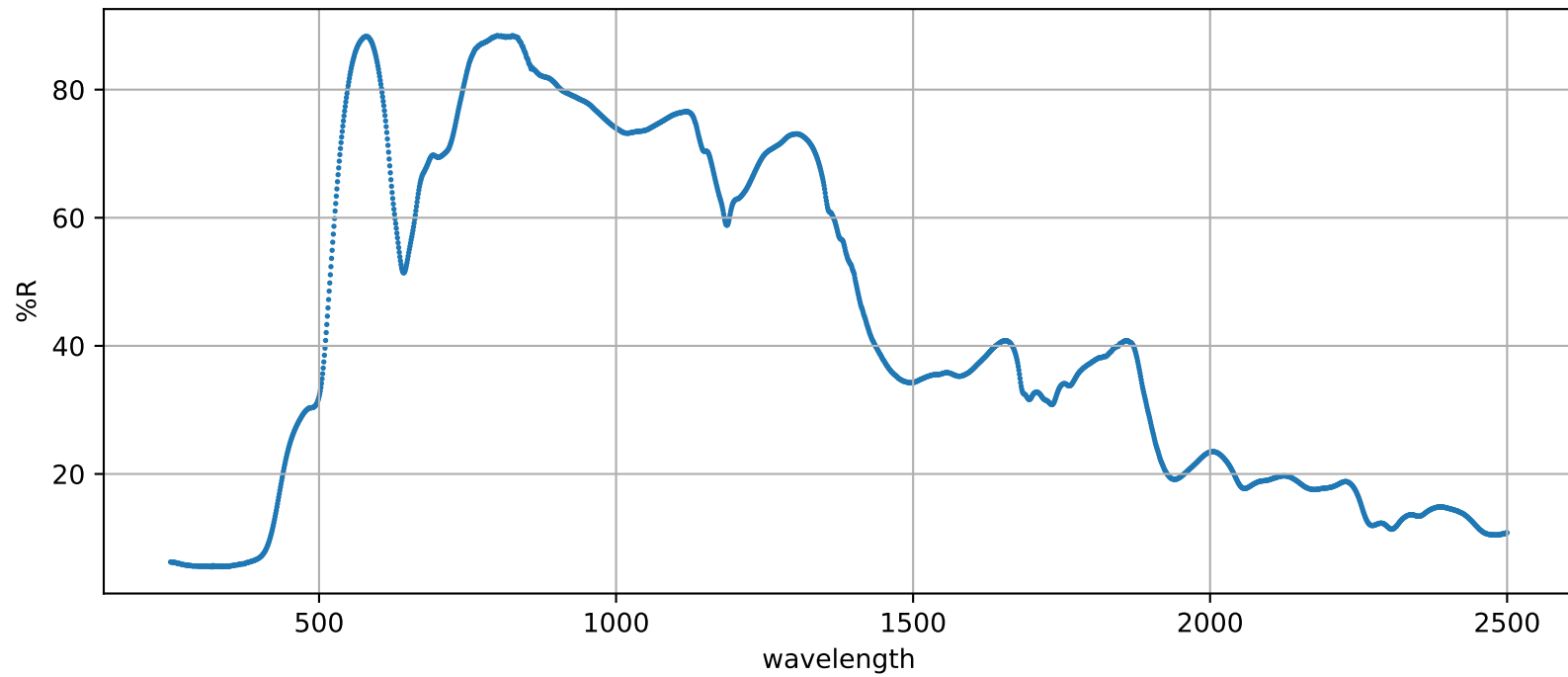
CICIMAUCR0100 avg



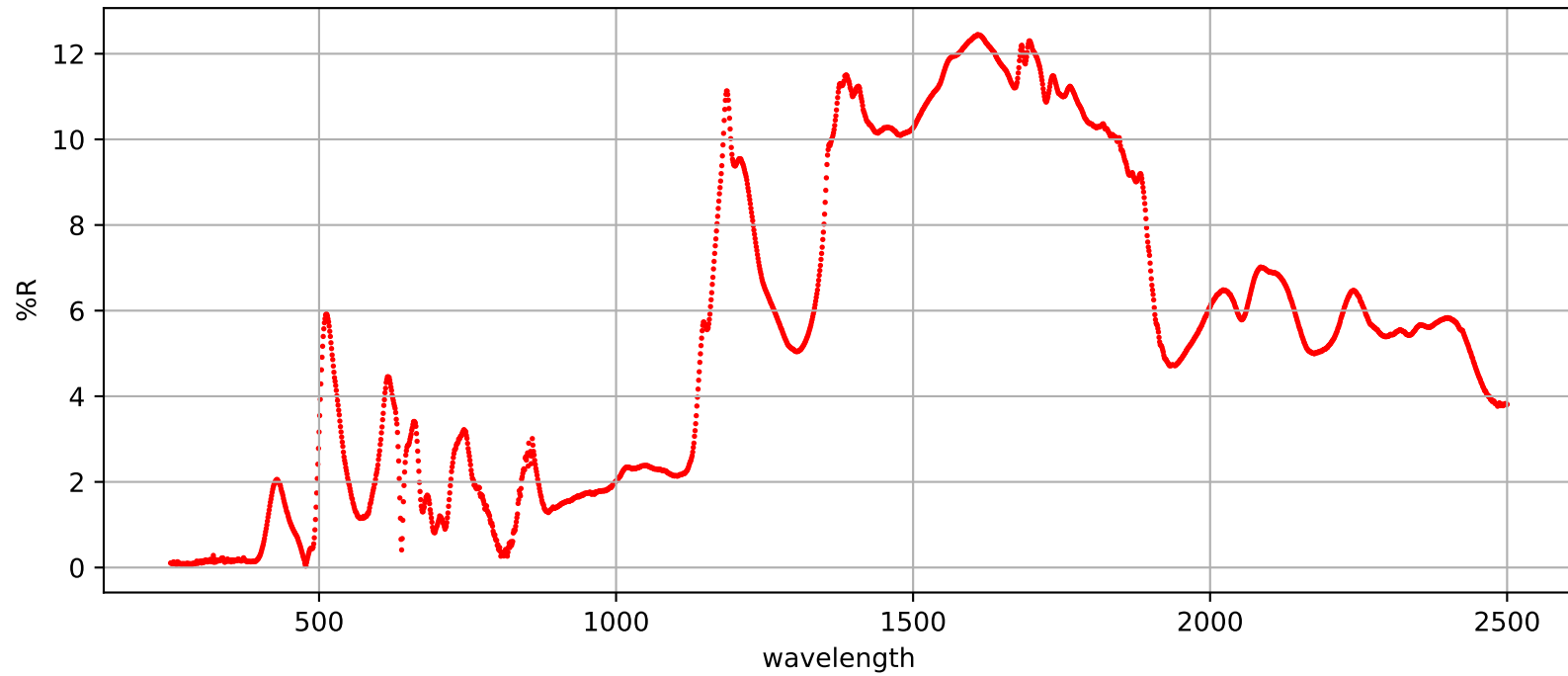
CICIMAUCR0100 std dev



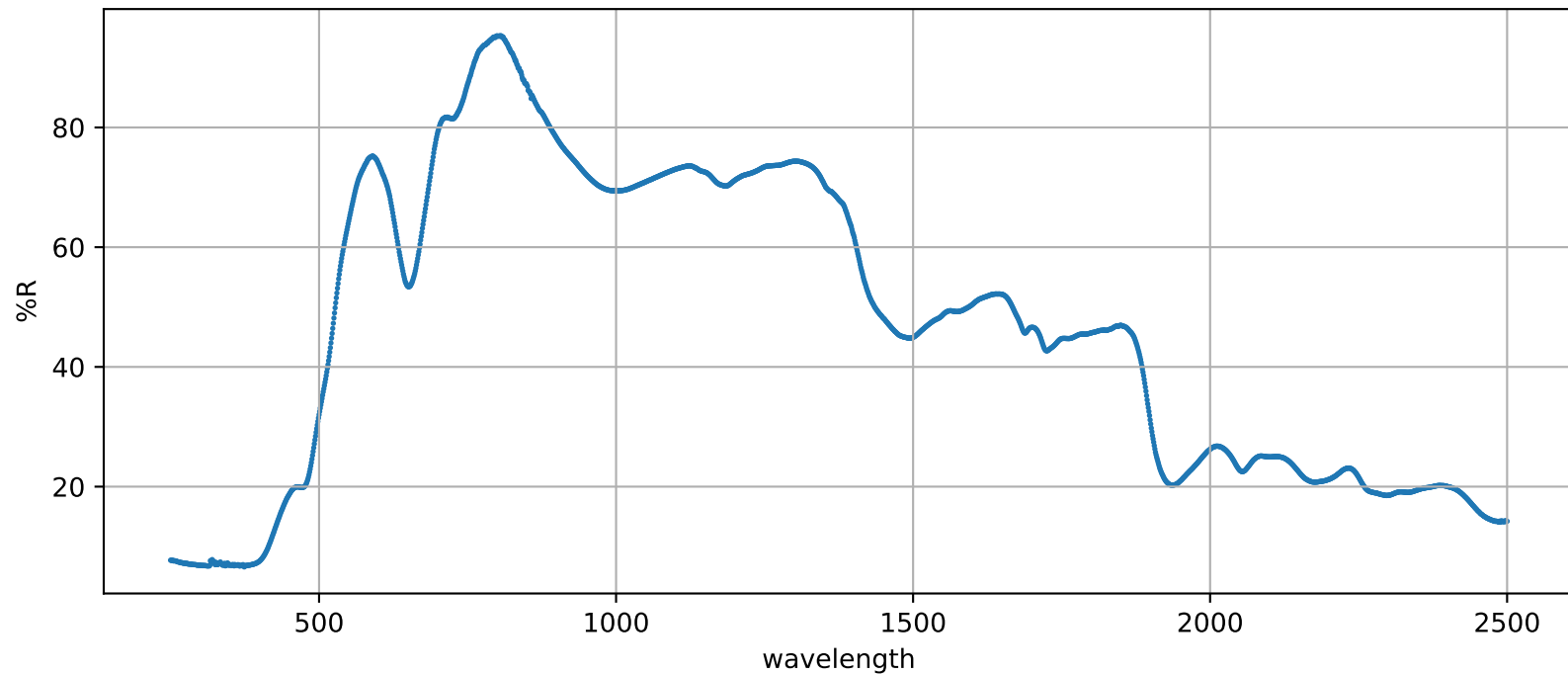
CICIMAUCR0112 avg



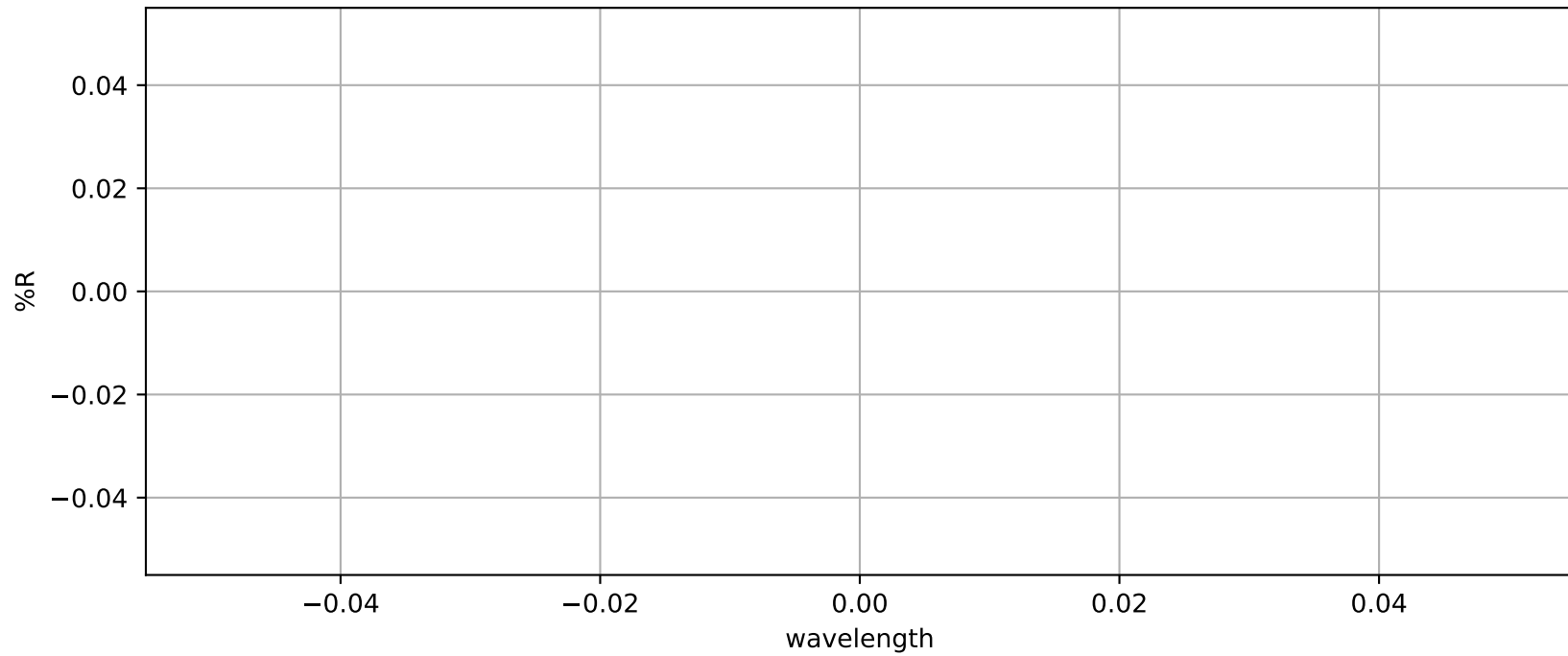
CICIMAUCR0112 std dev



BIOUCRA1 avg

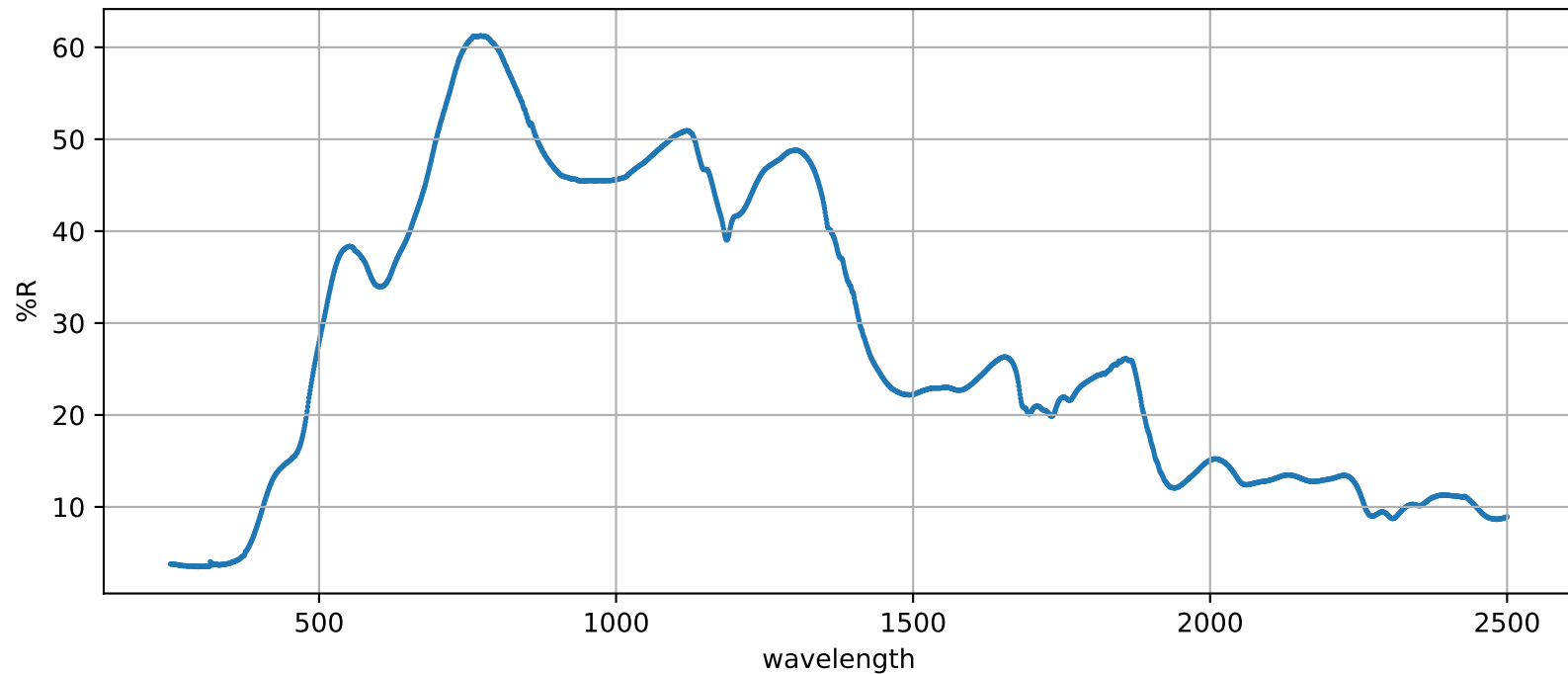


BIOUCRA1 std dev

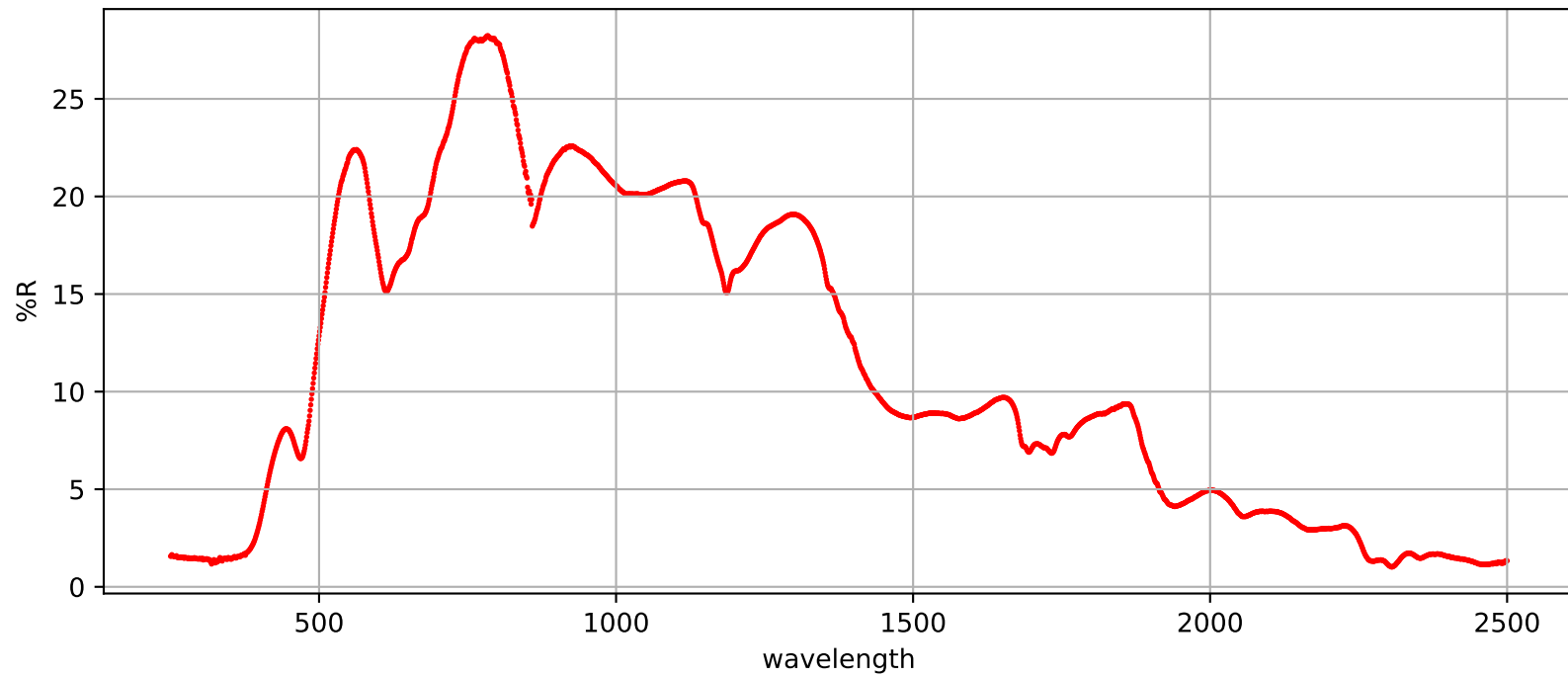




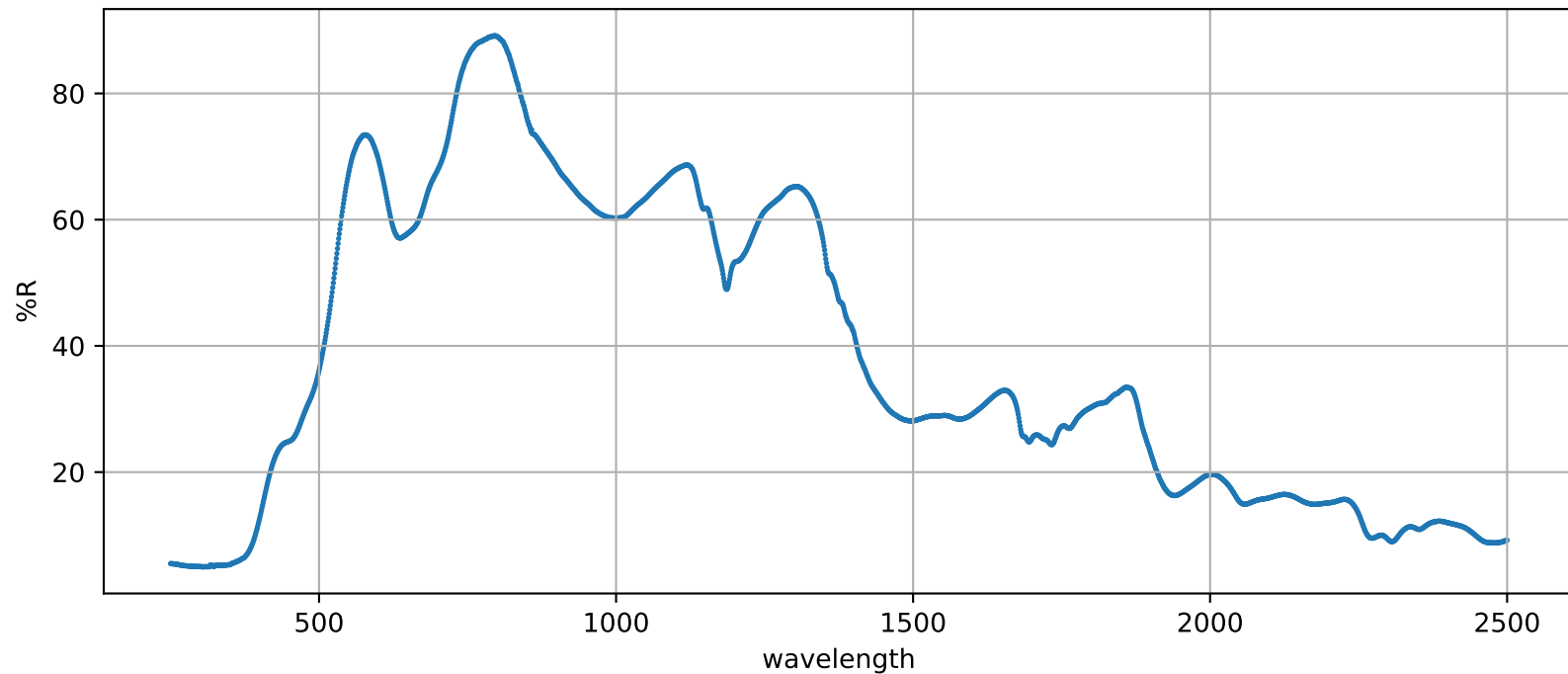
CICIMAUCR0006 avg



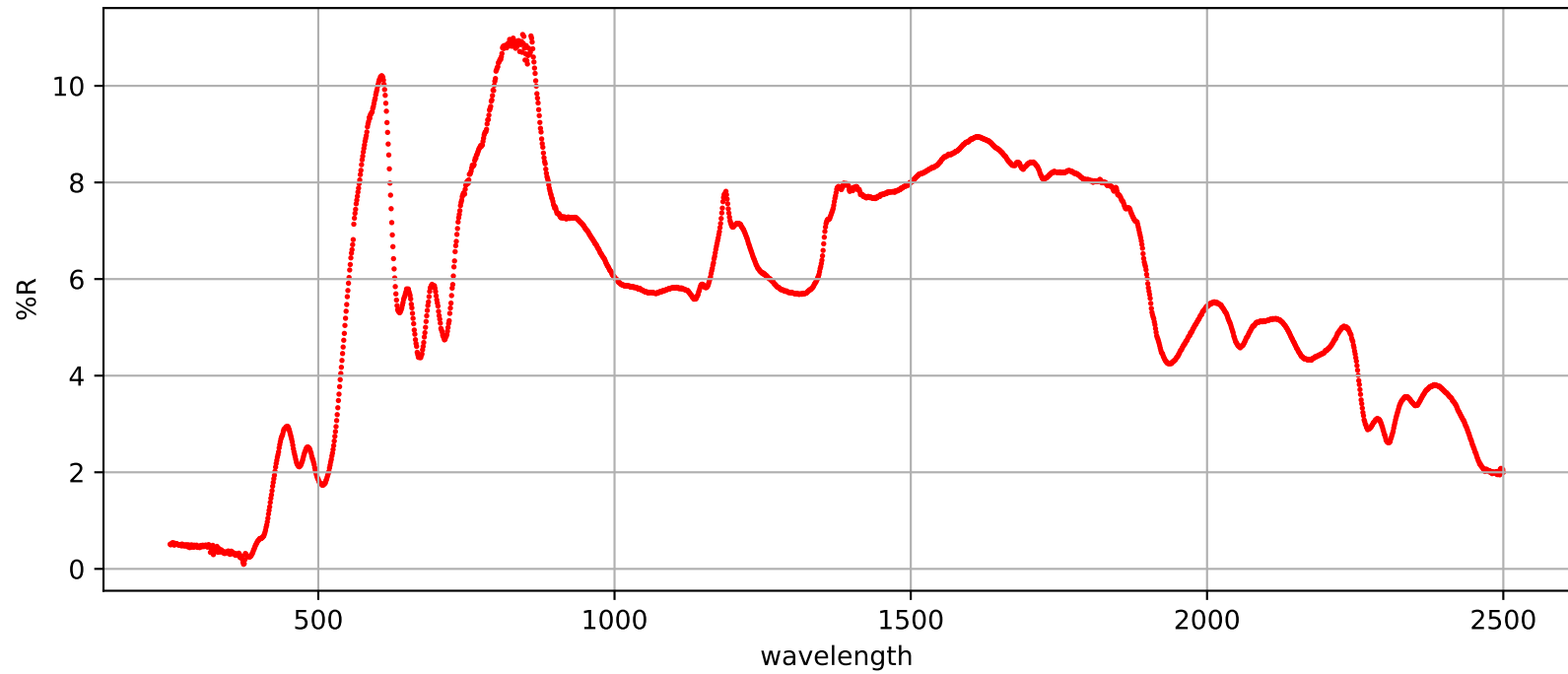
CICIMAUCR0006 std dev



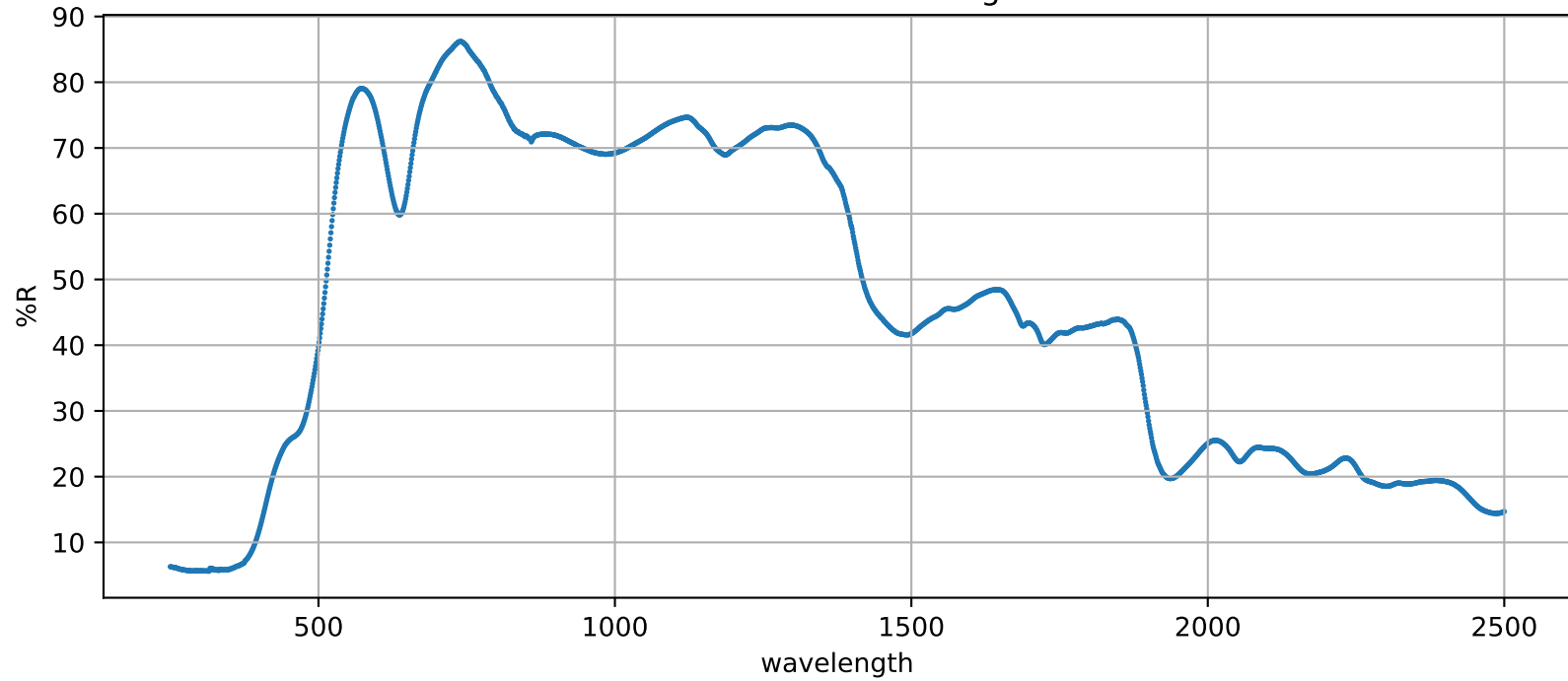
CICIMAUCR0009 avg



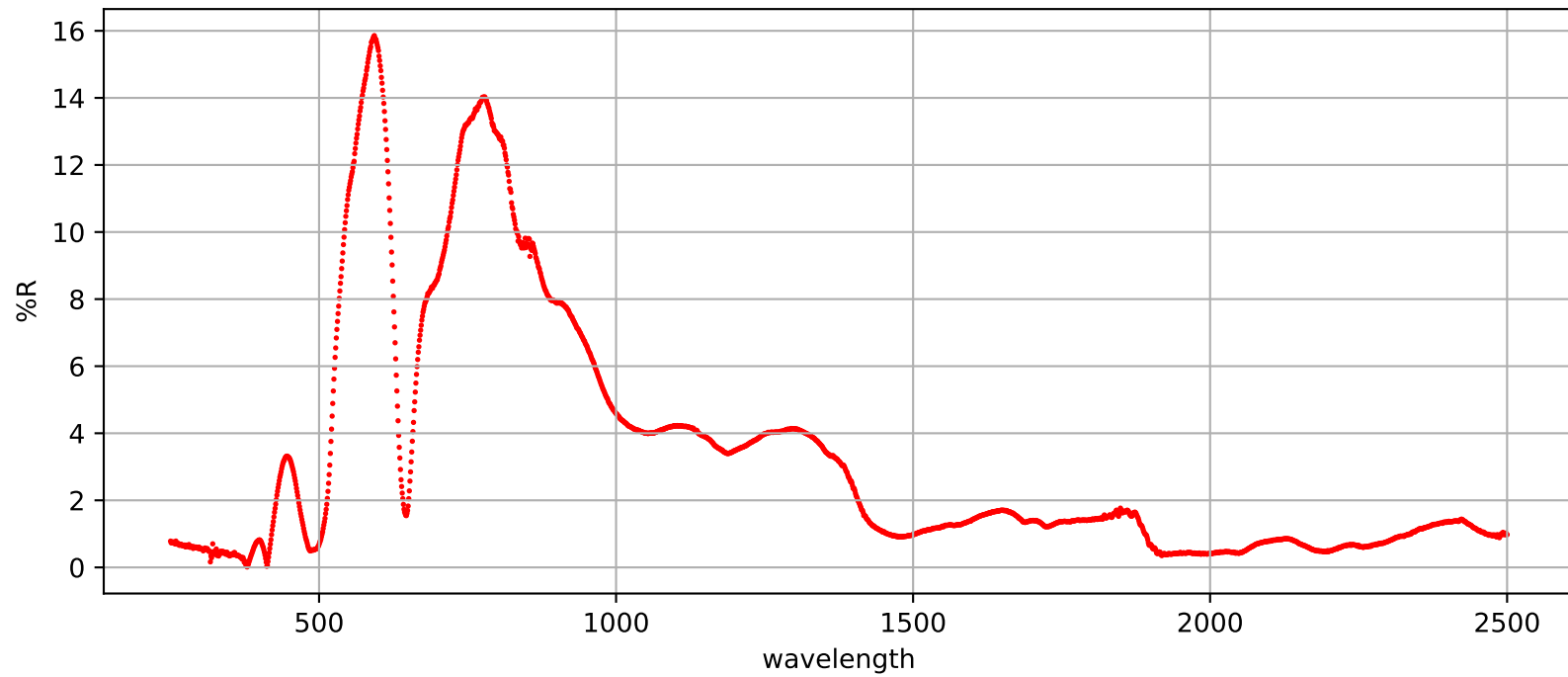
CICIMAUCR0009 std dev



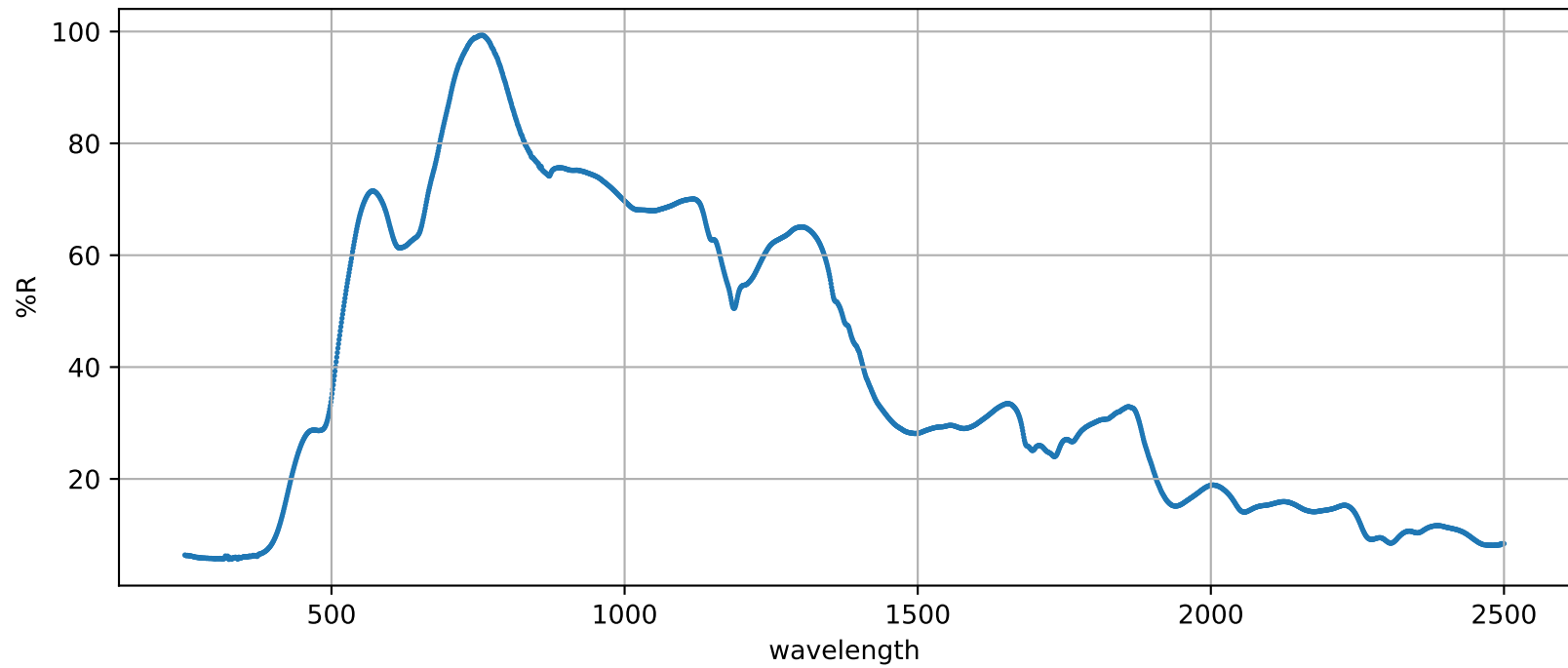
CICIMAUCR0071 avg



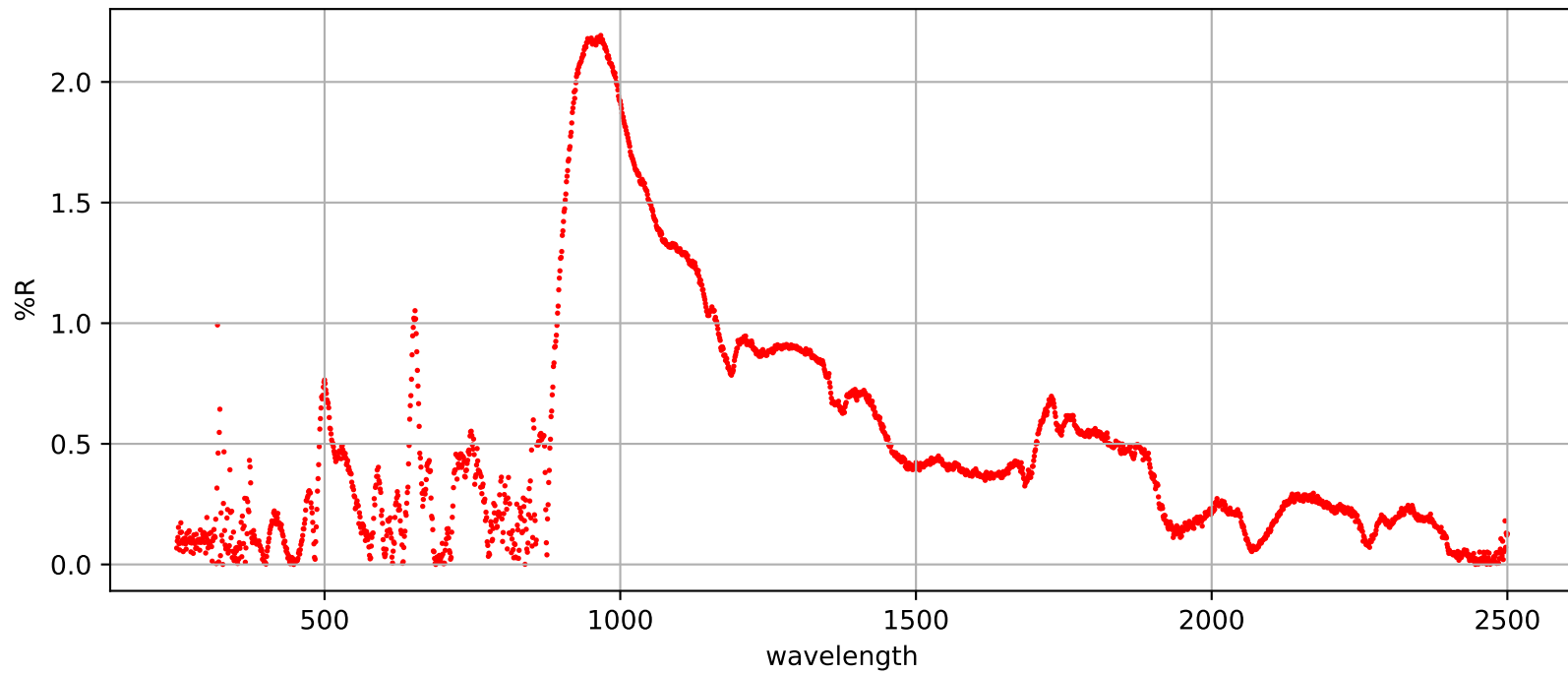
CICIMAUCR0071 std dev



CICIMAUCR0002 avg

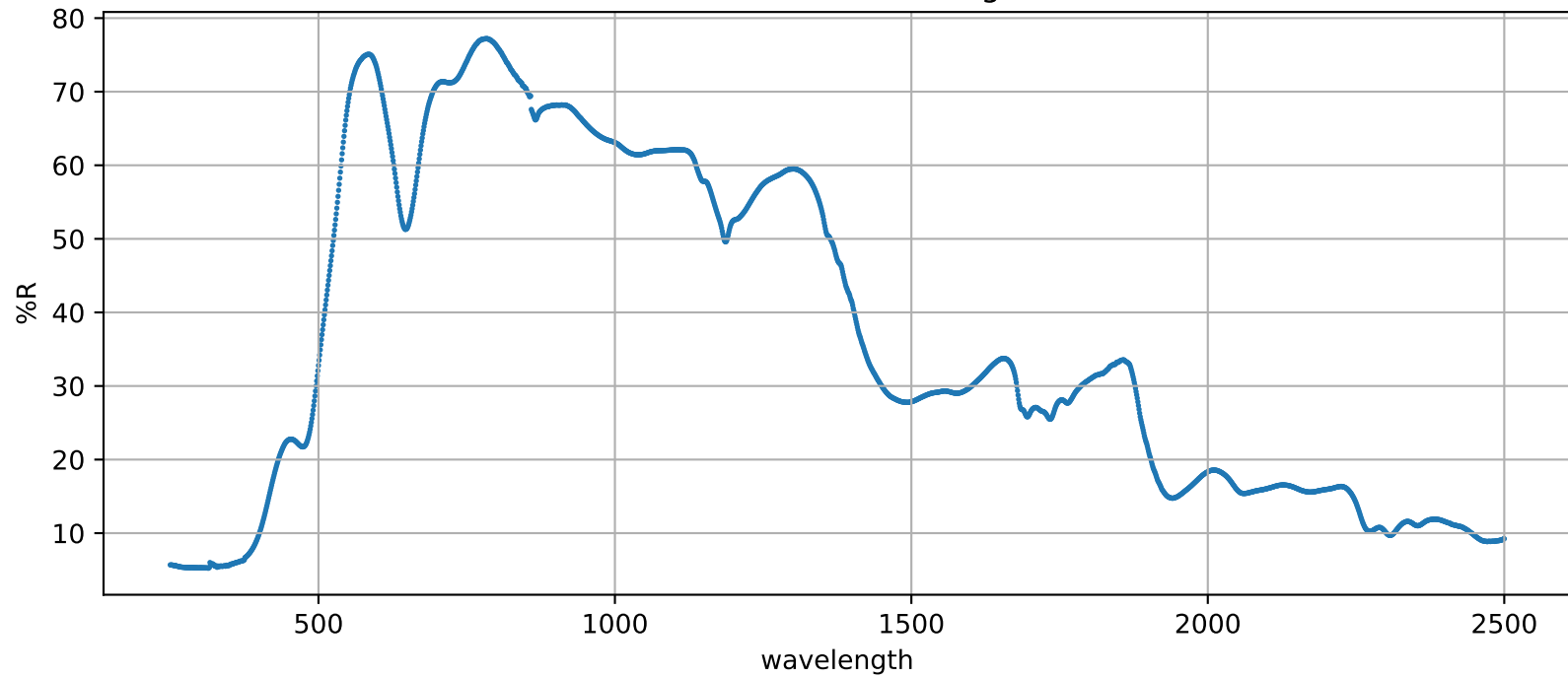


CICIMAUCR0002 std dev

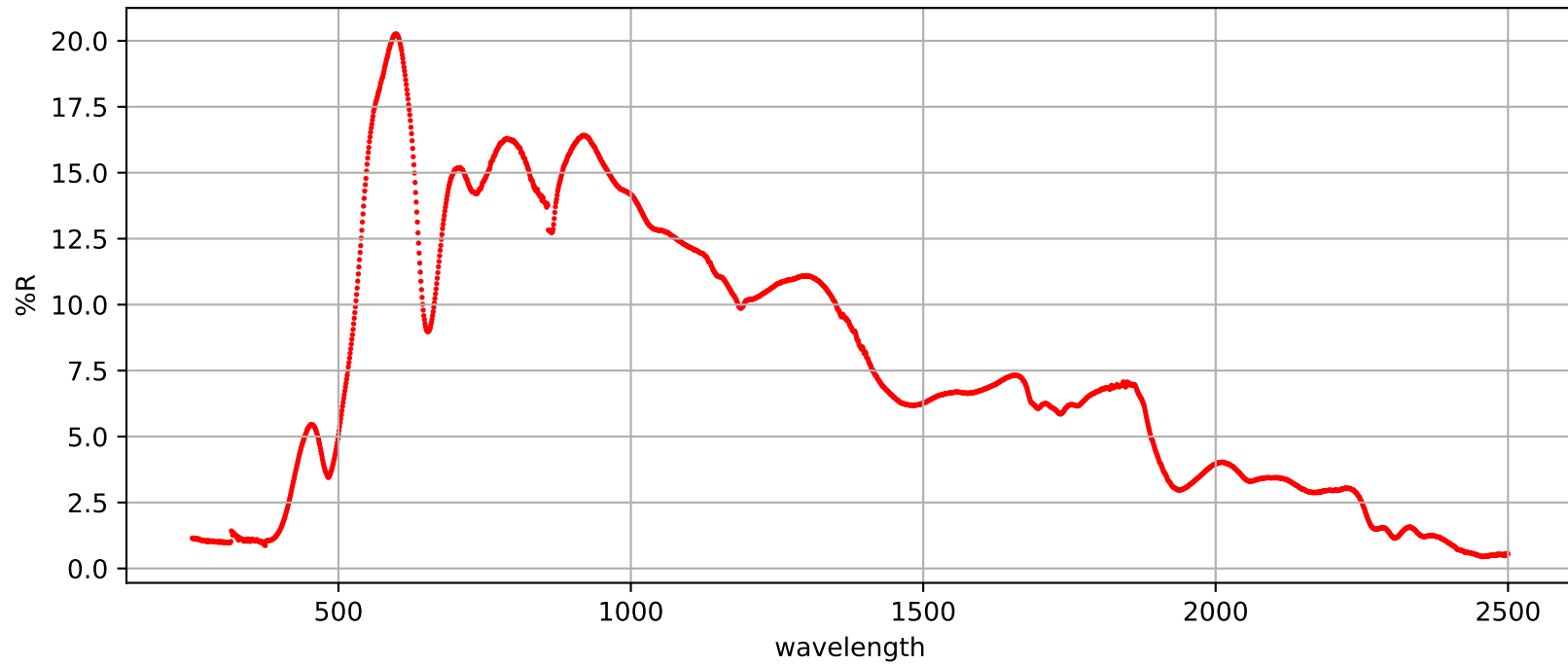




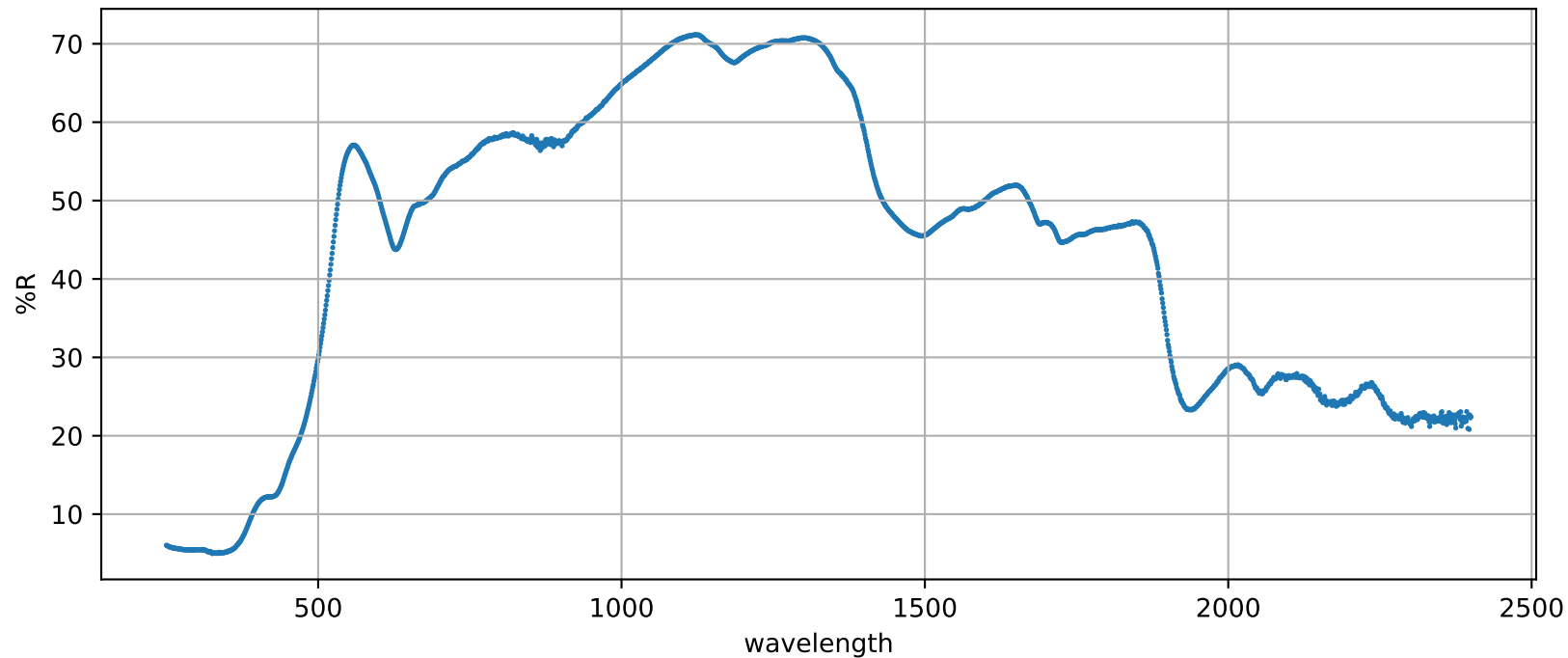
CICIMAUCR0020 avg



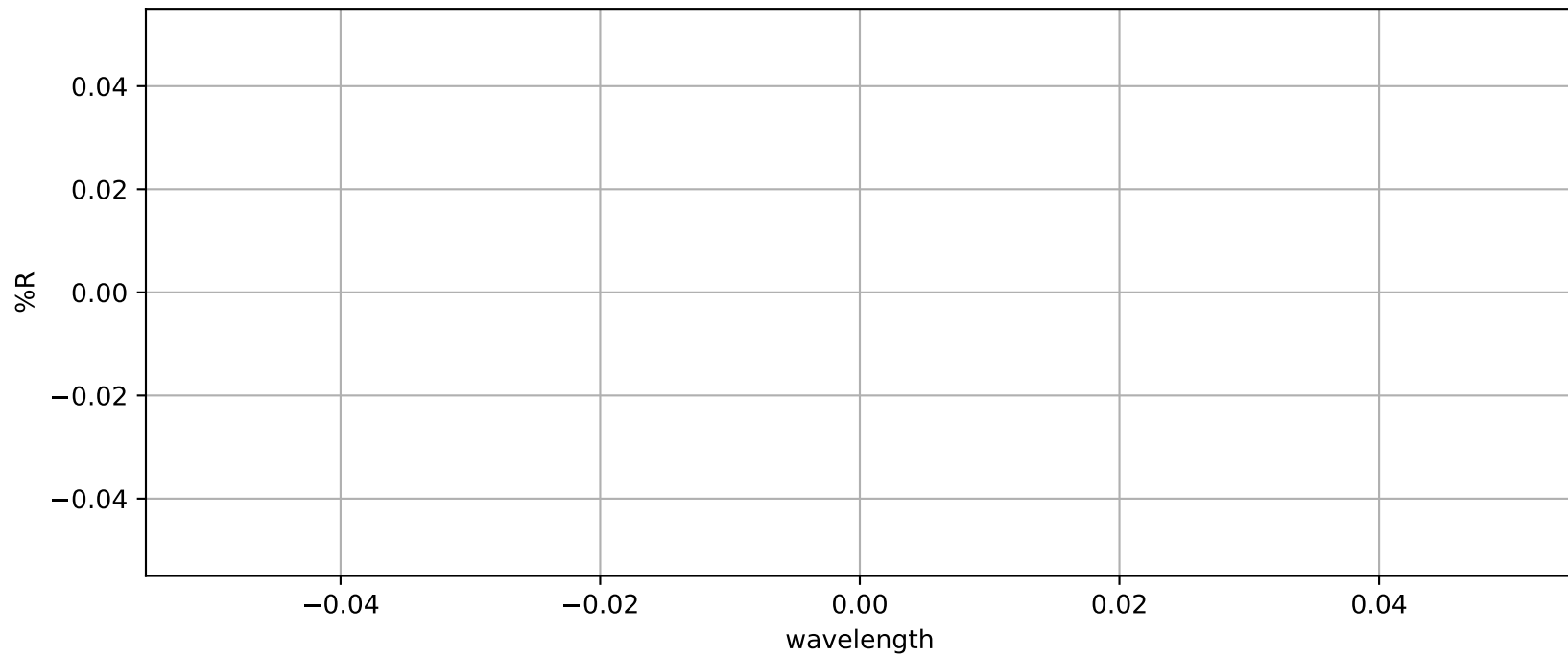
CICIMAUCR0020 std dev



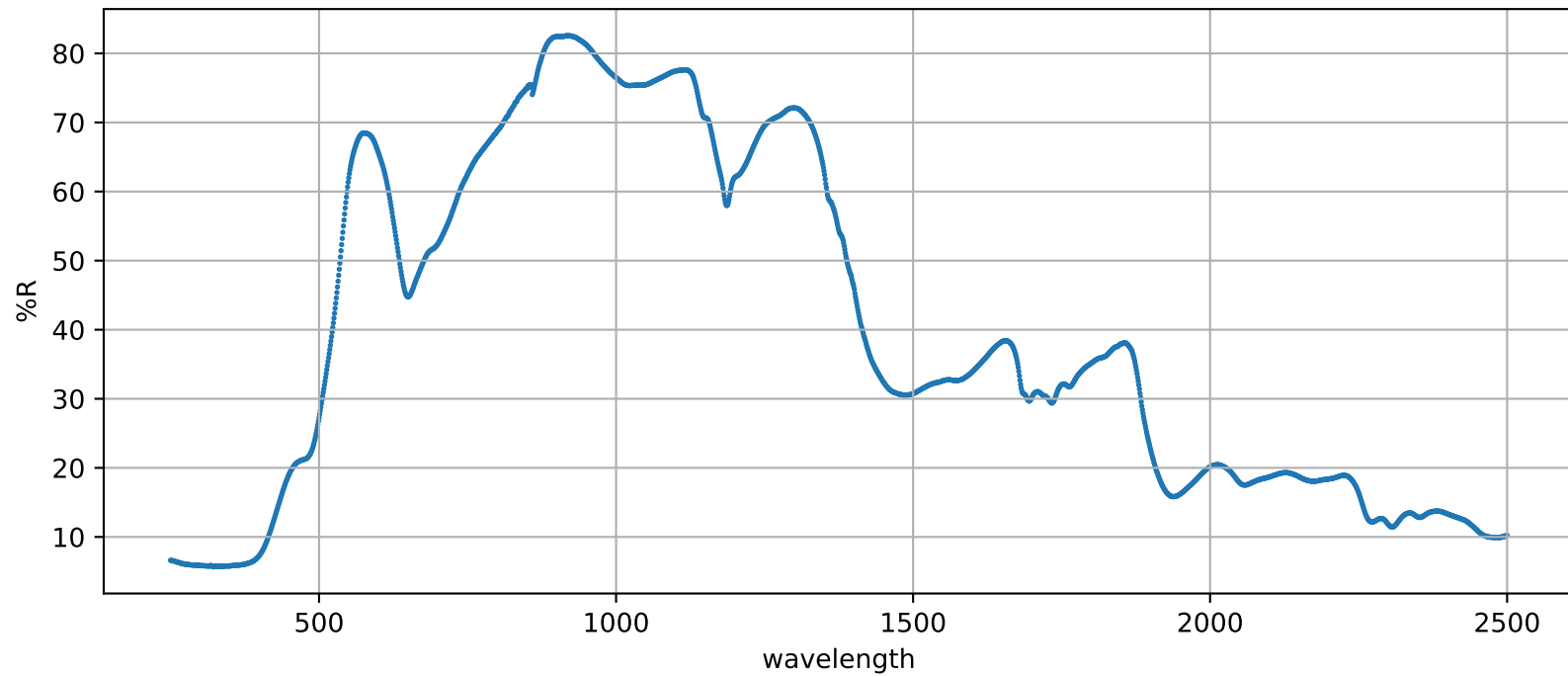
Sample2198 avg



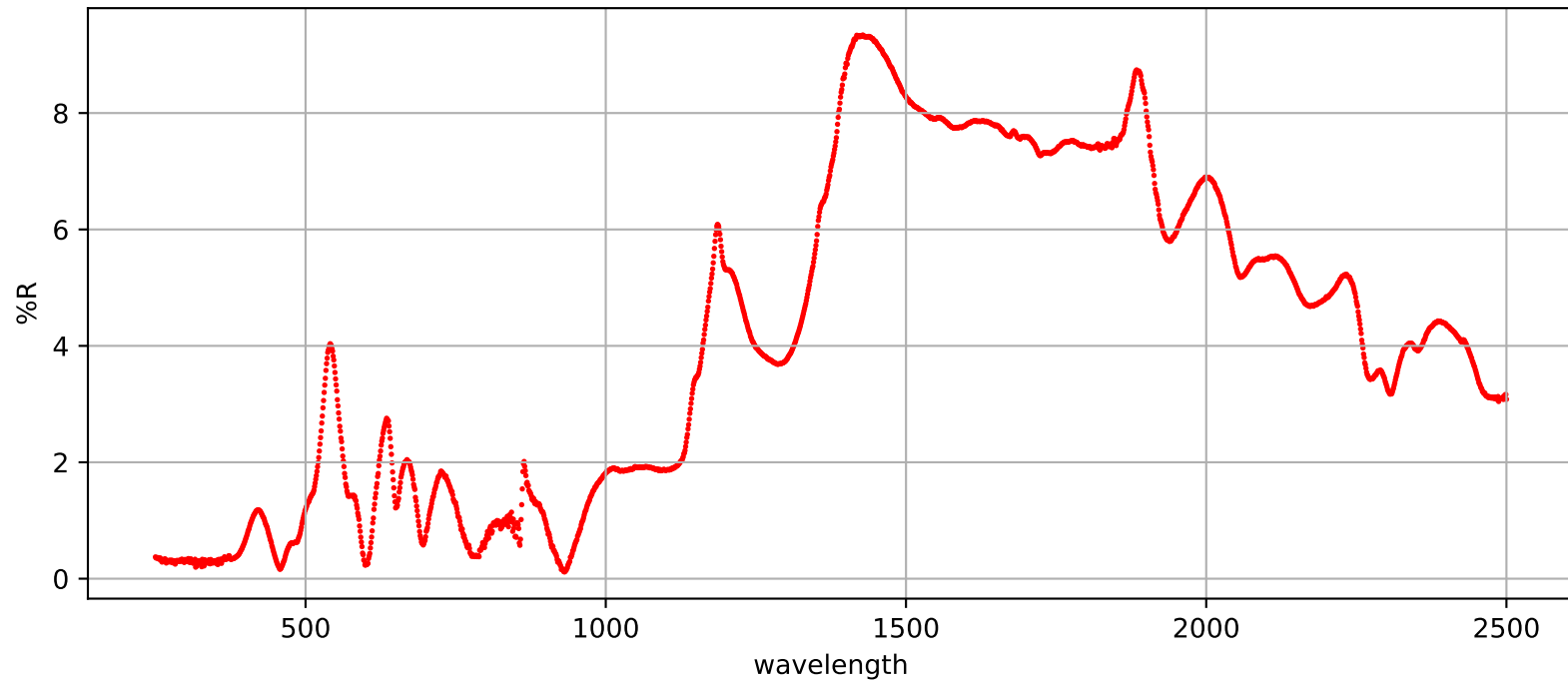
Sample2198 std dev



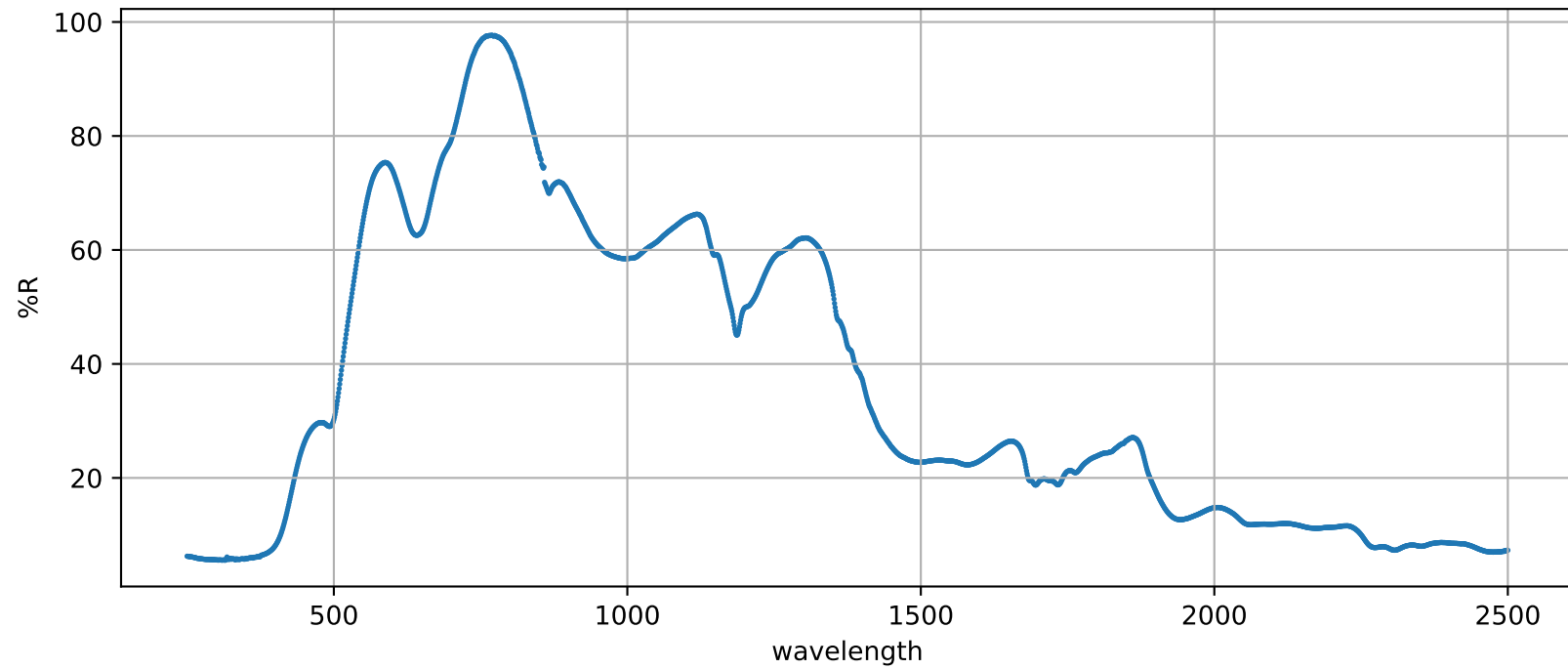
CICIMAUCR0014 avg



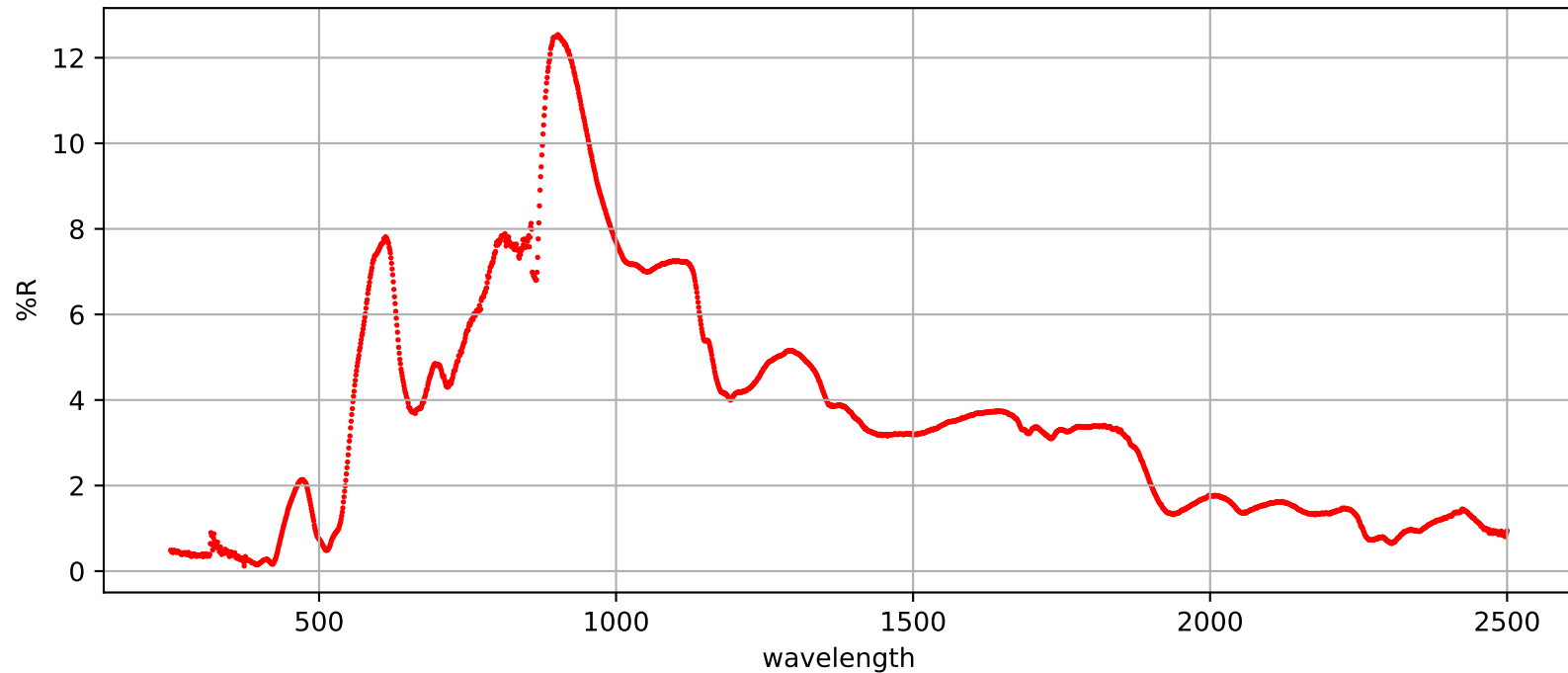
CICIMAUCR0014 std dev



CICIMAUCR0113 avg

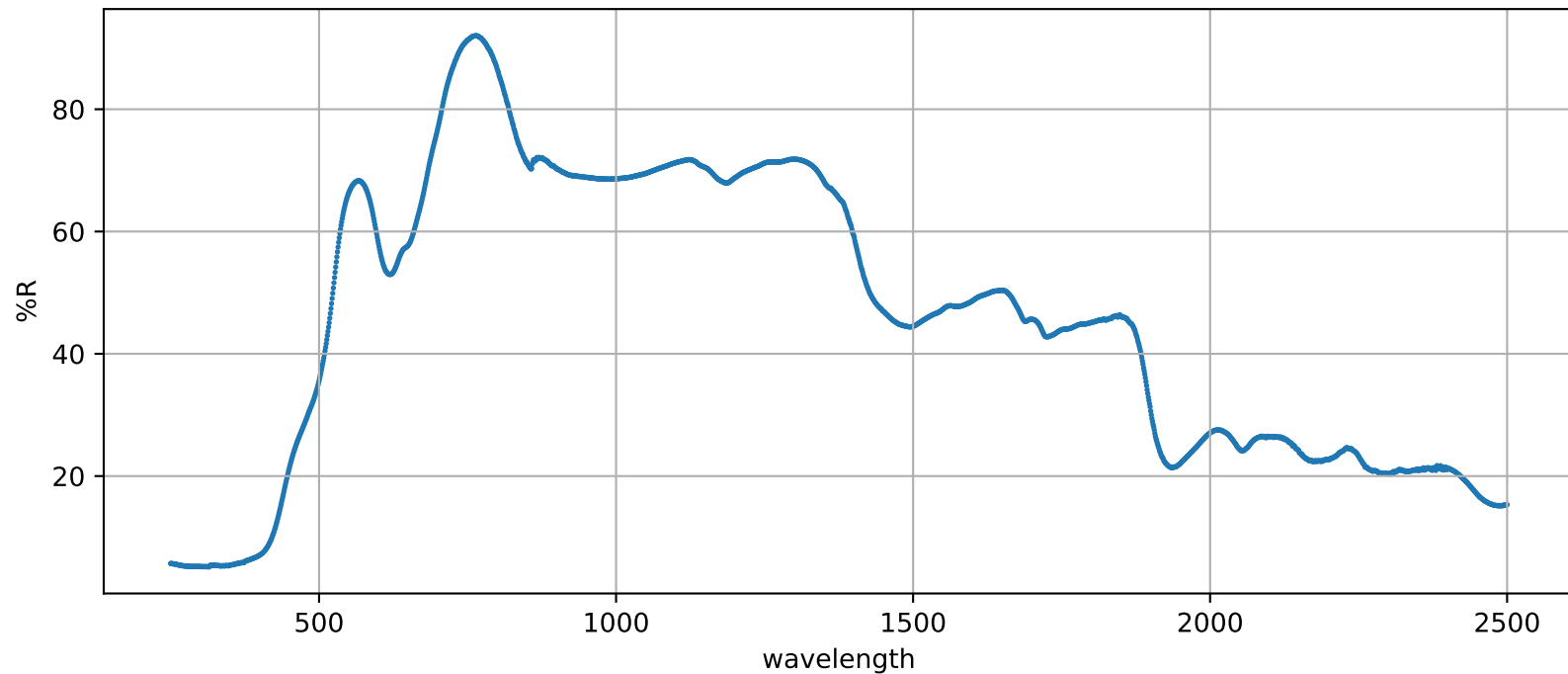


CICIMAUCR0113 std dev

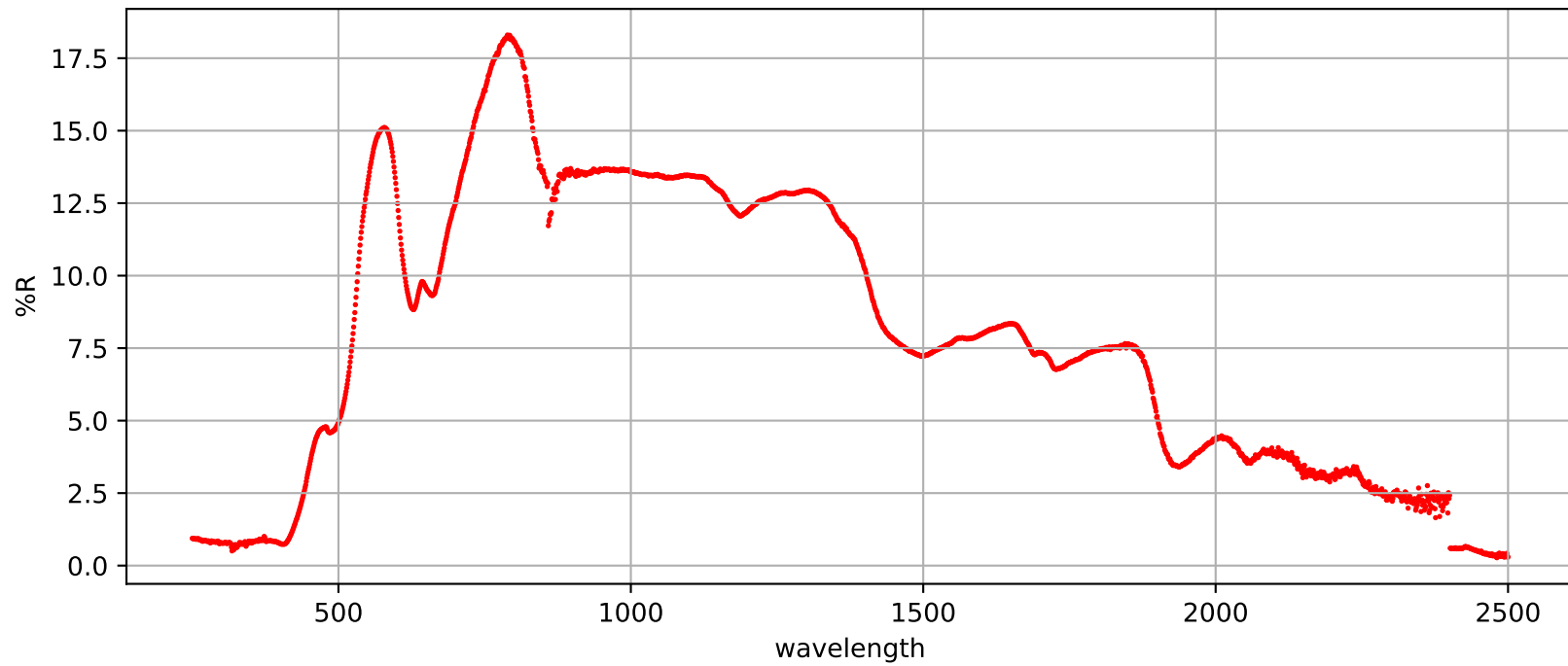




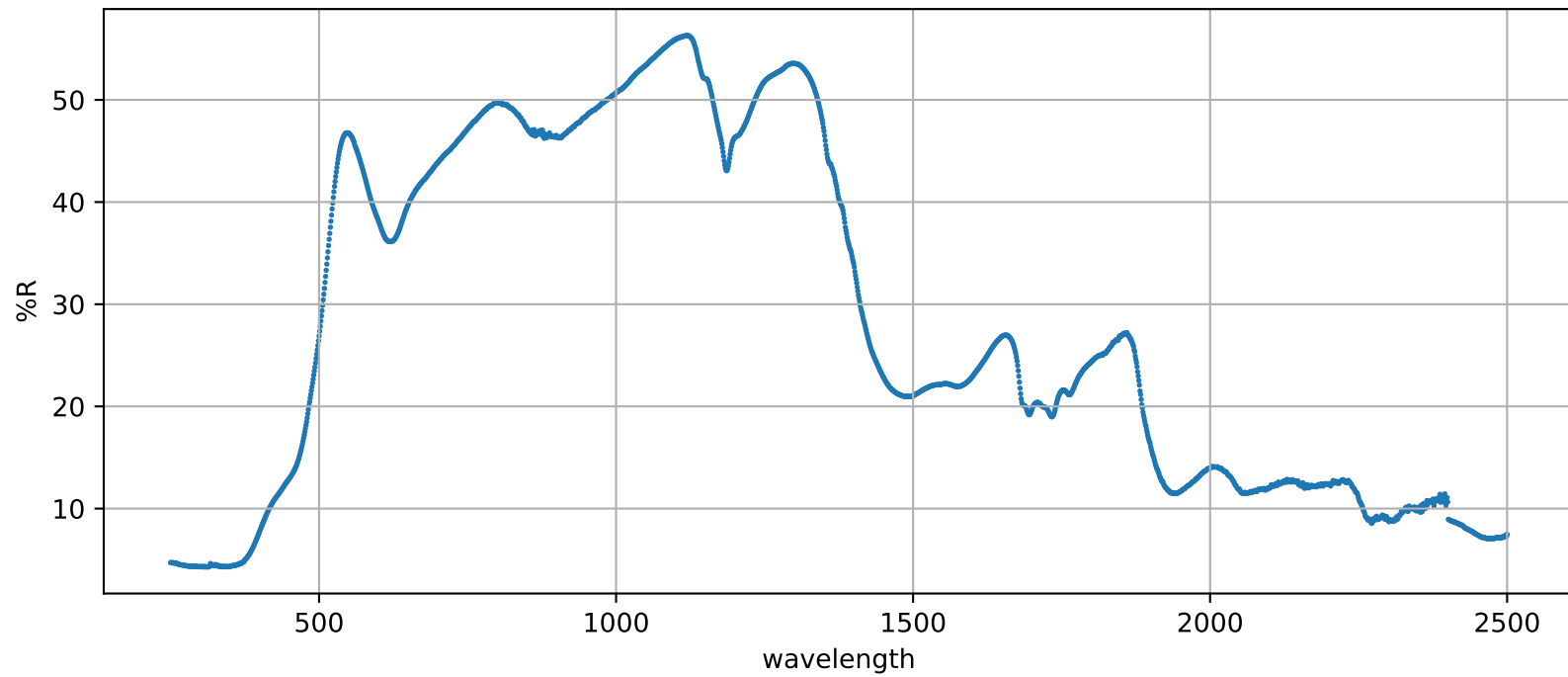
CICIMAUCR0001 avg



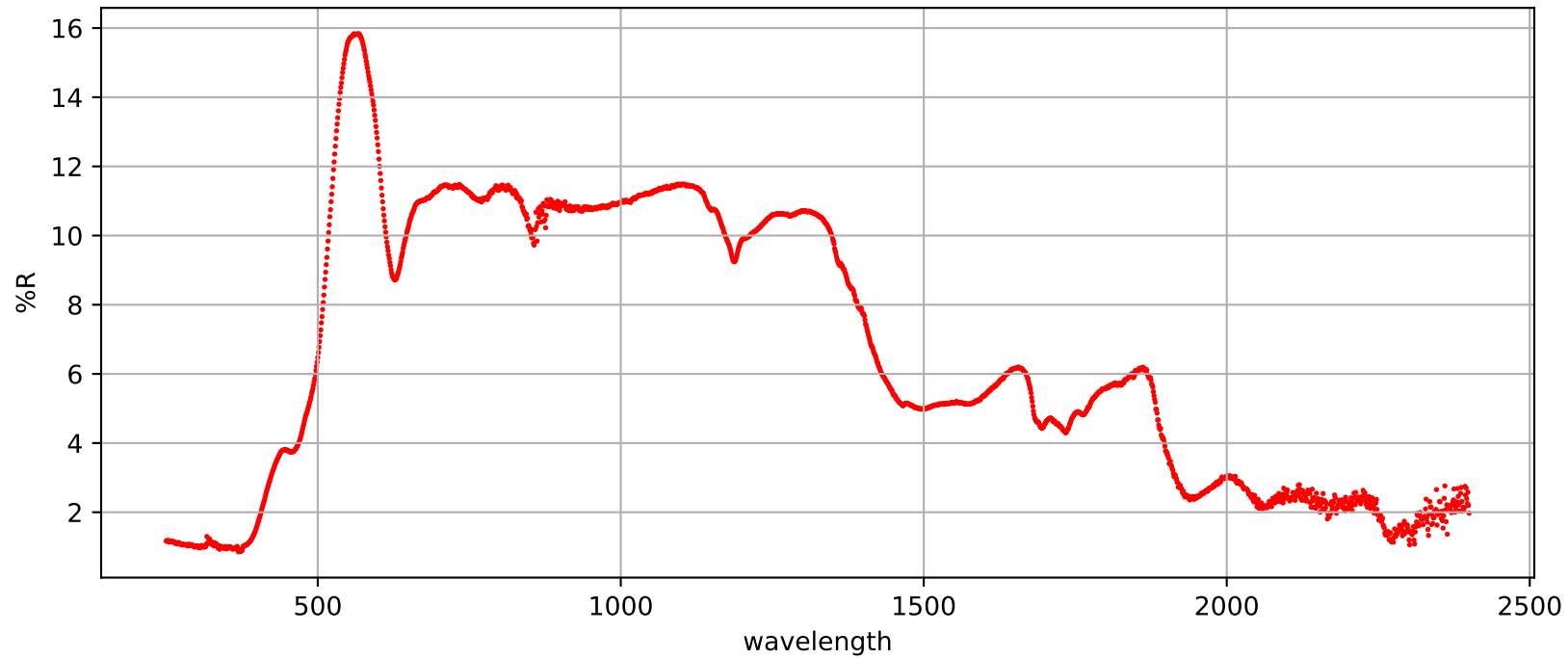
CICIMAUCR0001 std dev



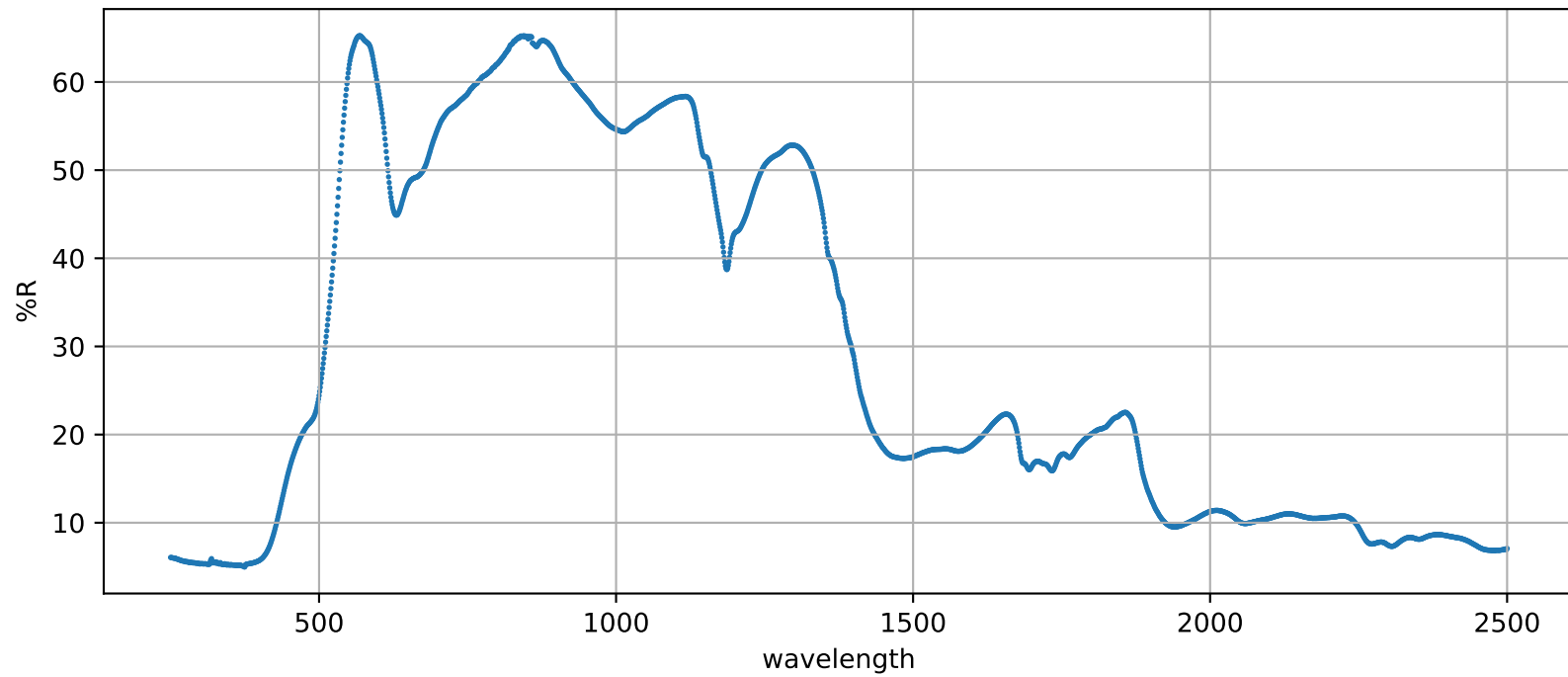
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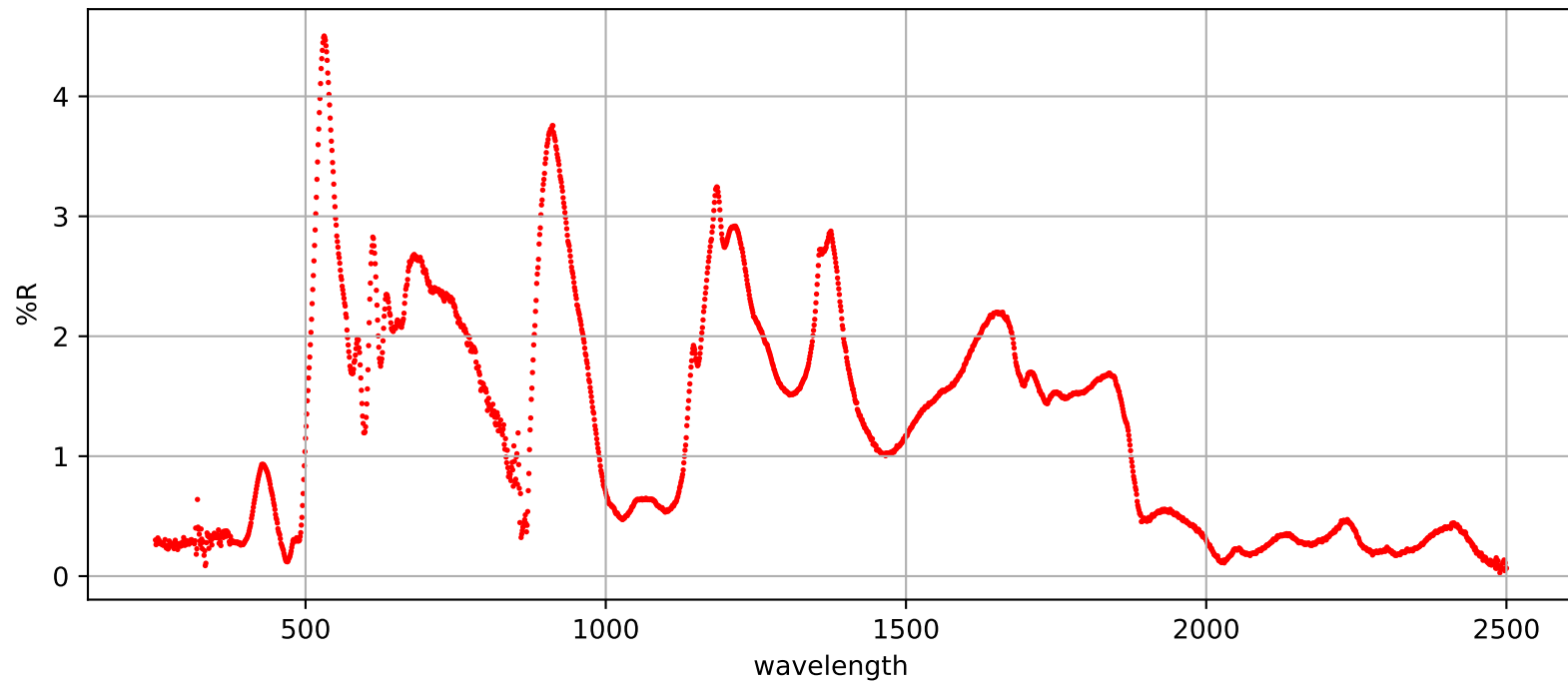
CICIMAUCR0012 std dev



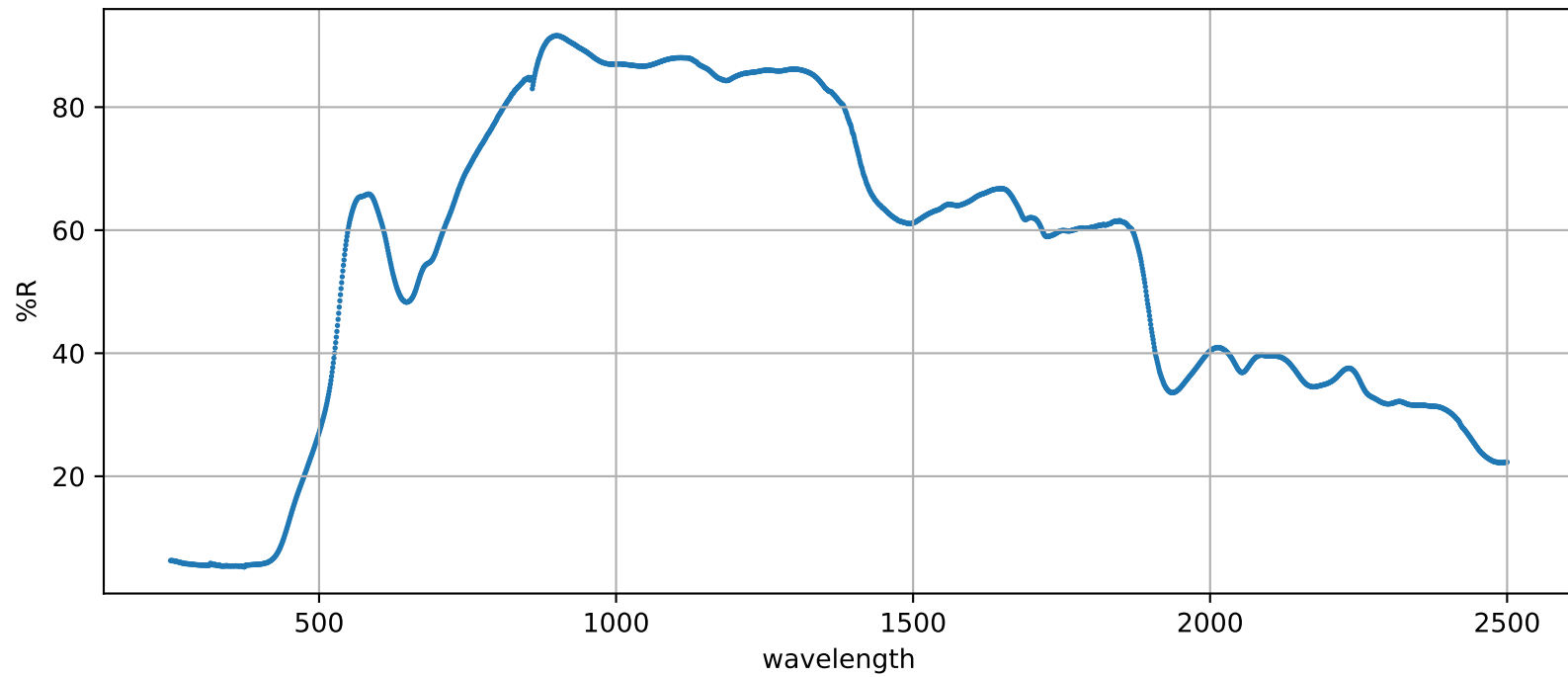
CICIMAUCR0098 avg



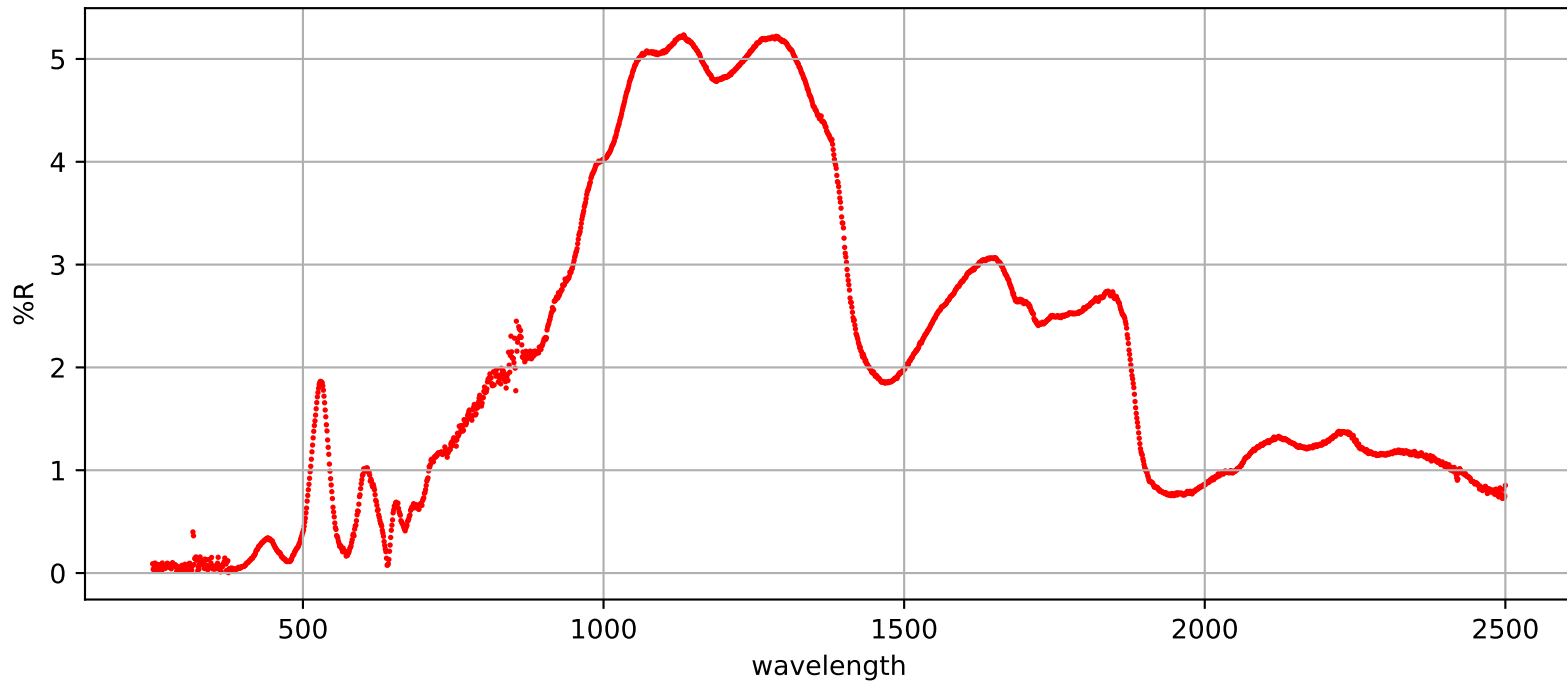
CICIMAUCR0098 std dev



CICIMAUCR0070 avg

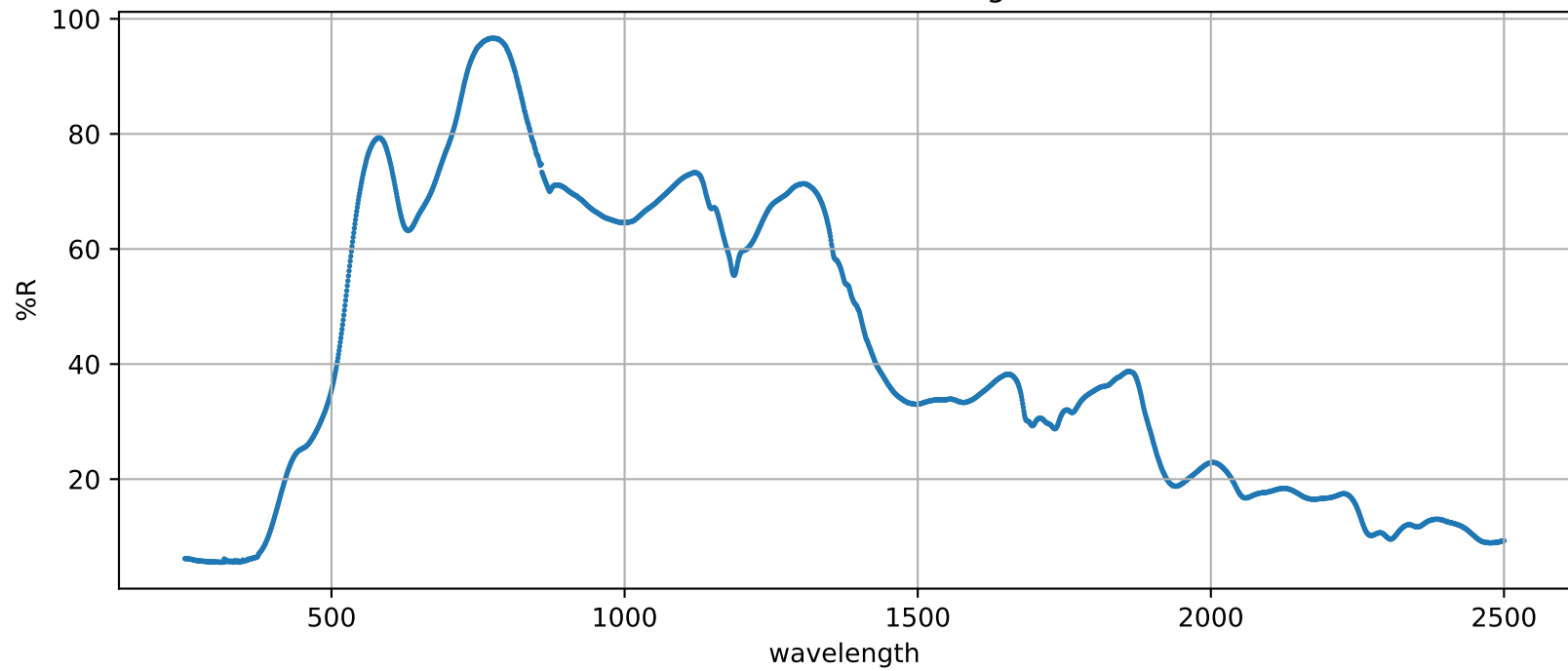


CICIMAUCR0070 std dev

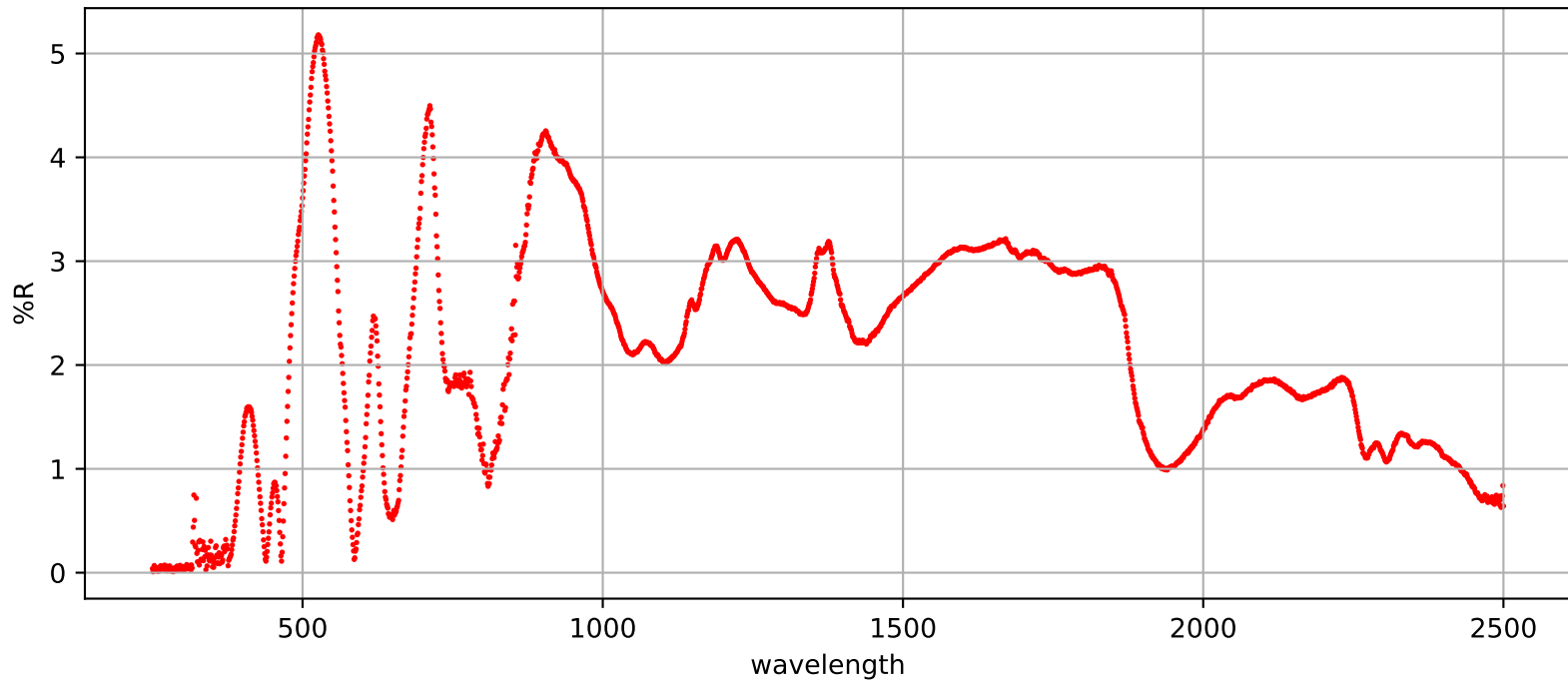




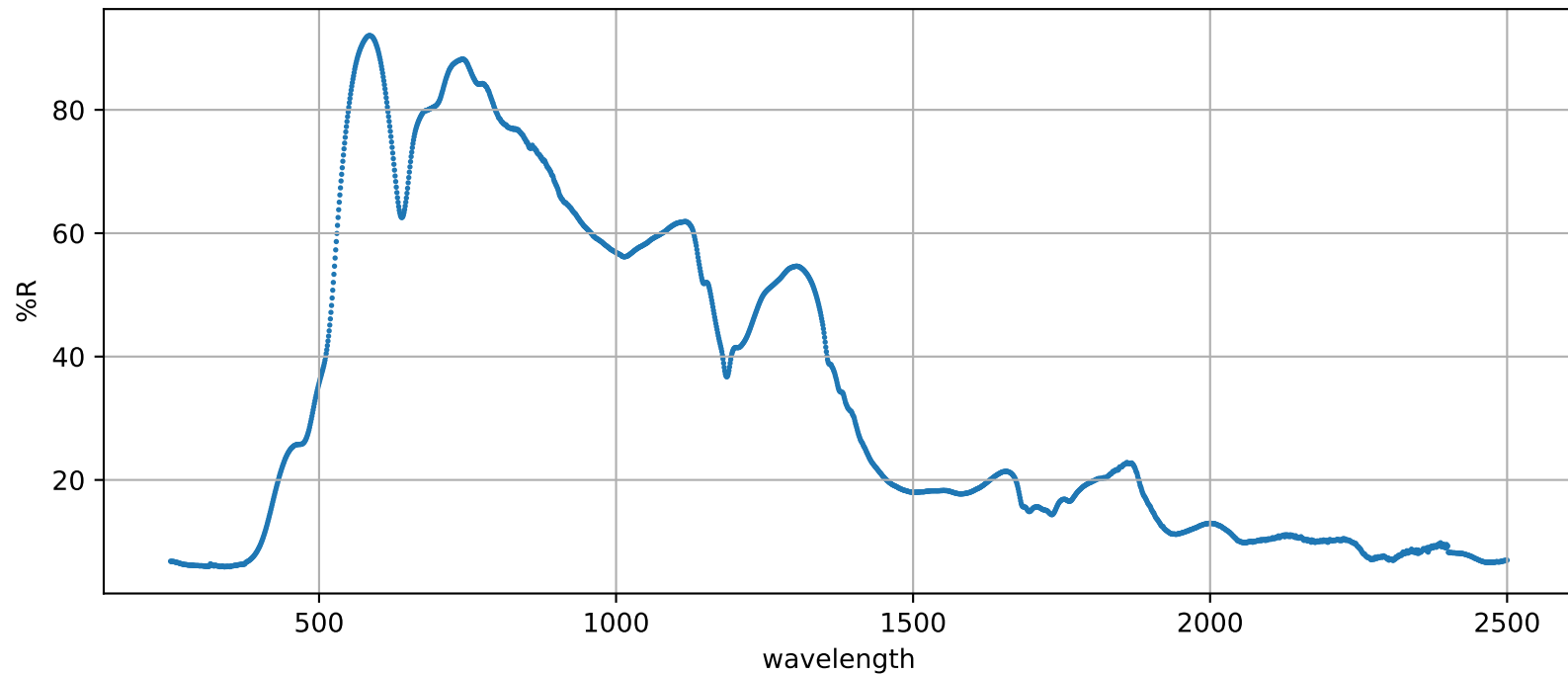
CICIMAUCR0004 avg



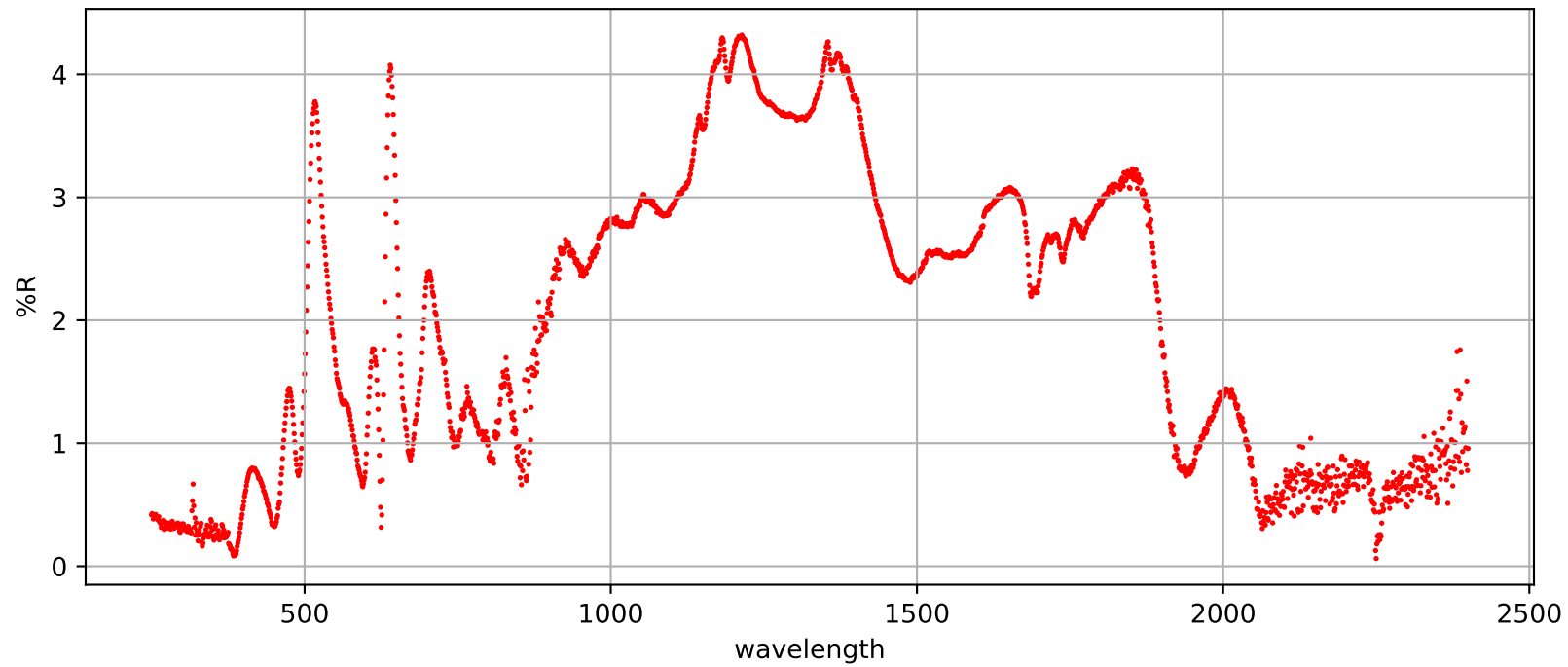
CICIMAUCR0004 std dev



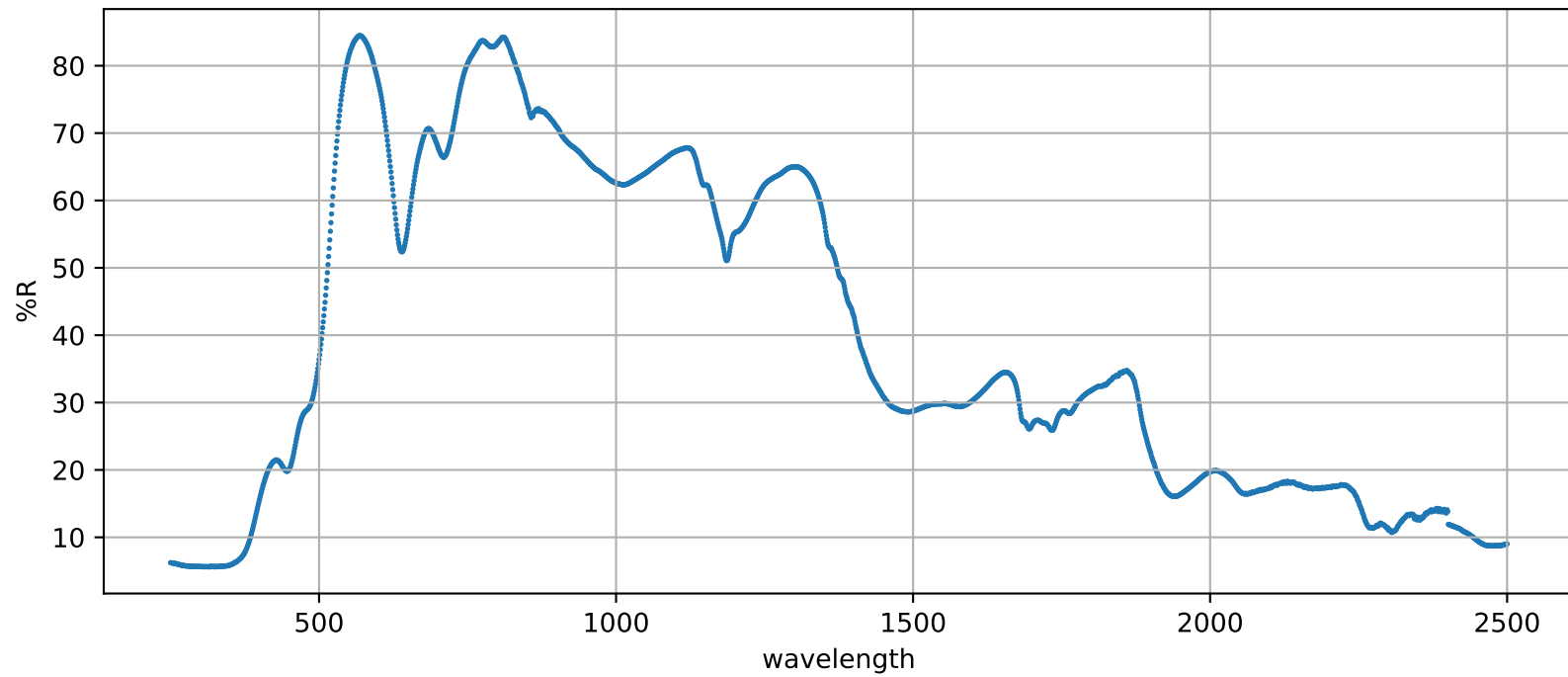
CICIMAUCR0101 avg



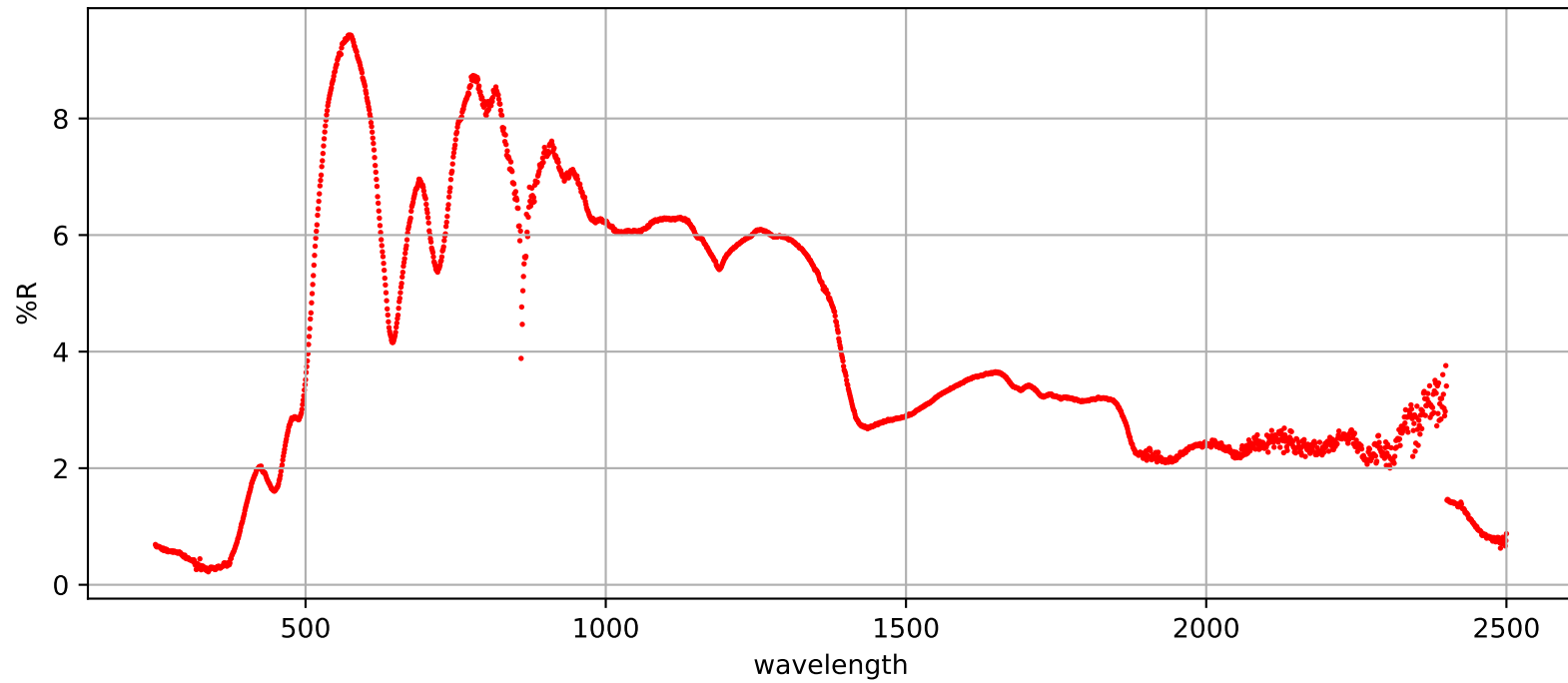
CICIMAUCR0101 std dev



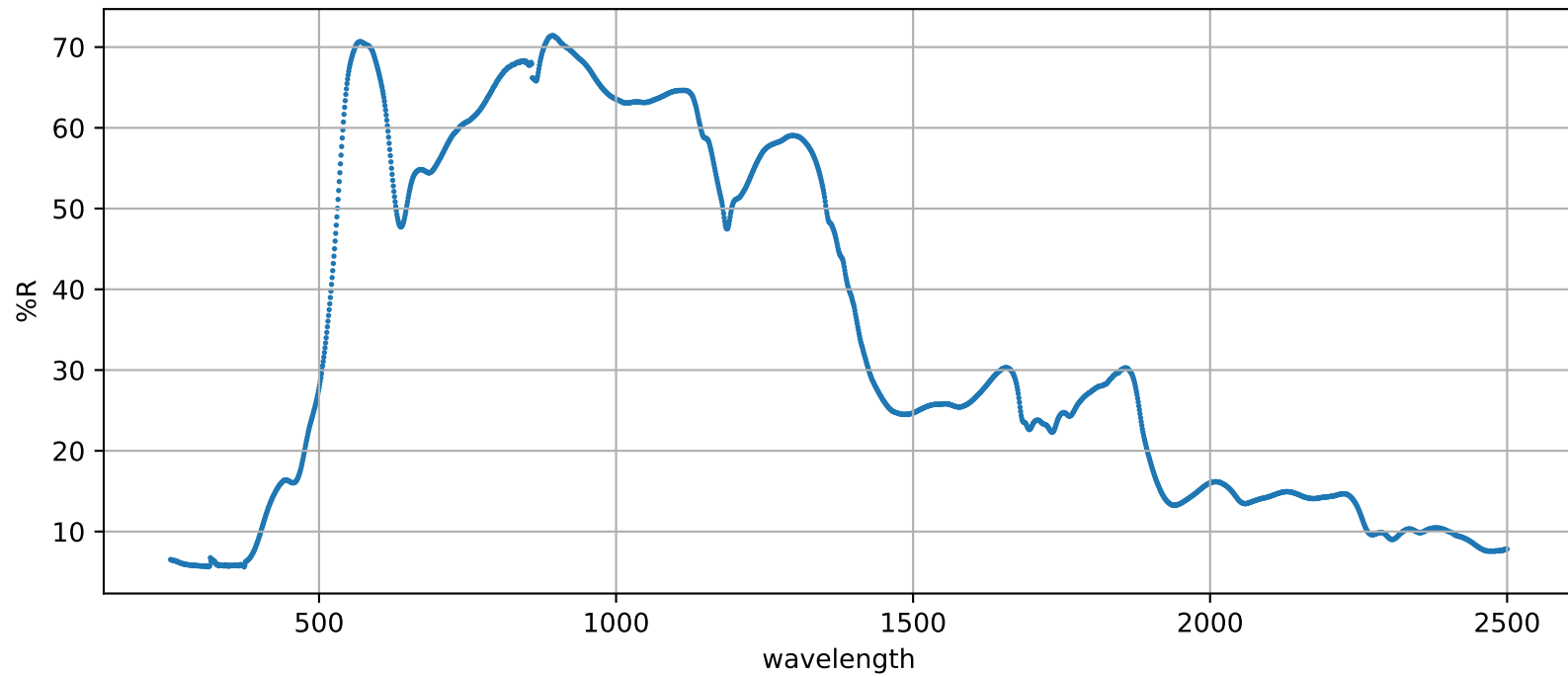
CICIMAUCR0013 avg



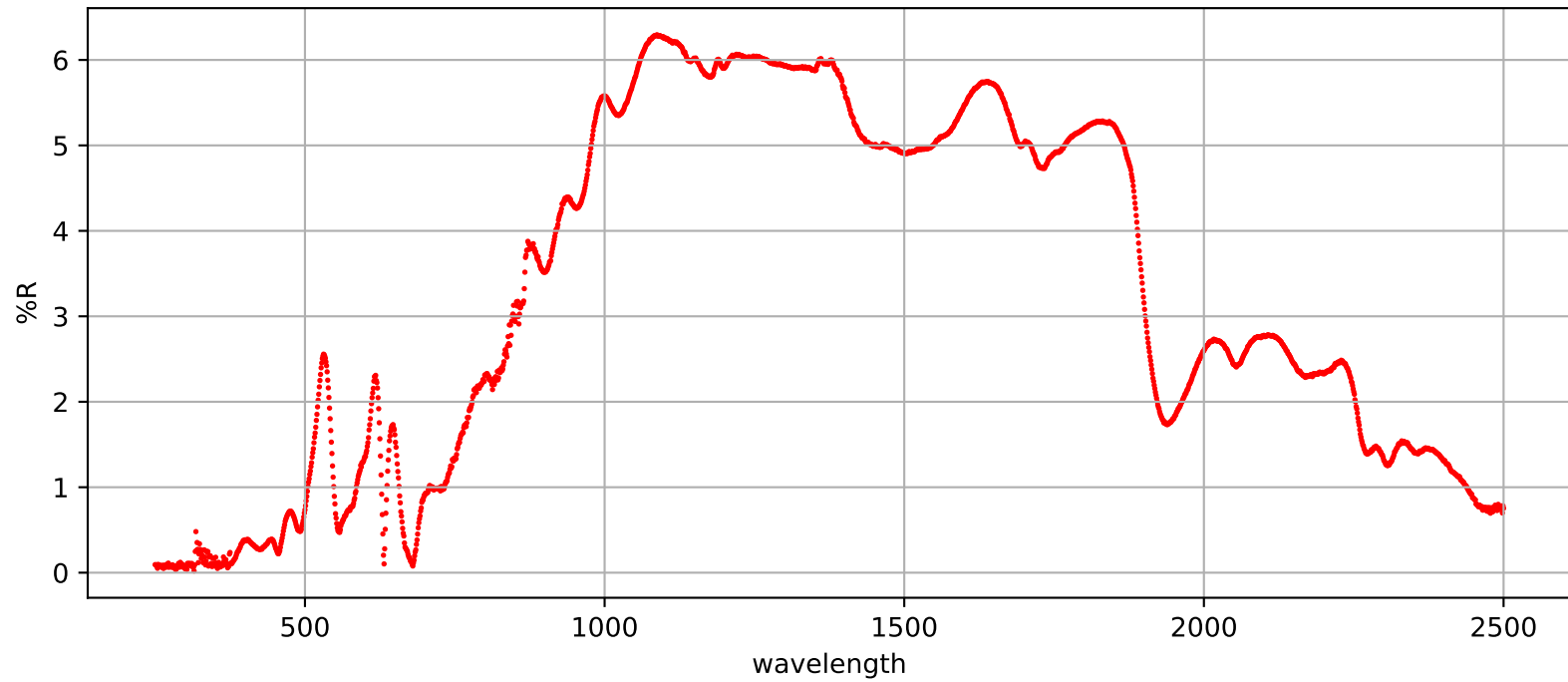
CICIMAUCR0013 std dev



CICIMAUCR0018 avg

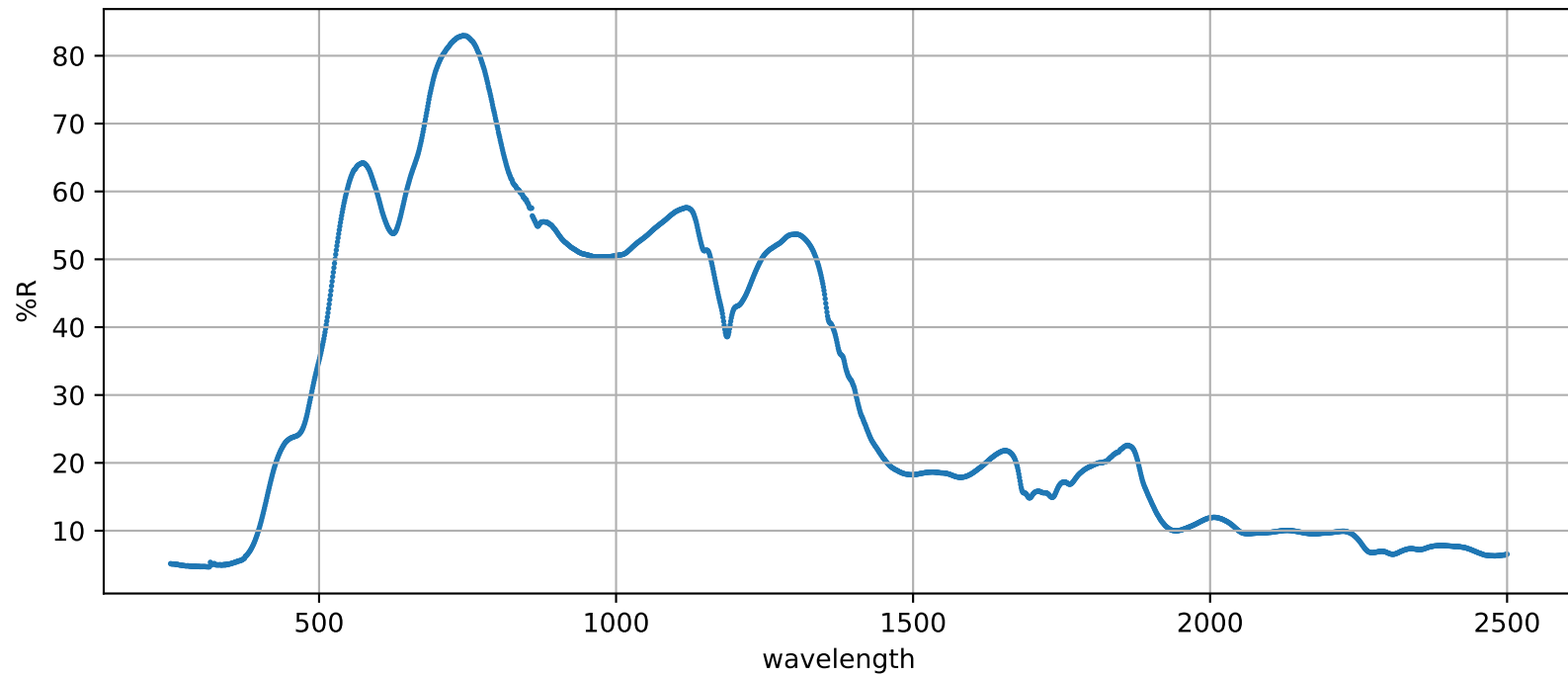


CICIMAUCR0018 std dev

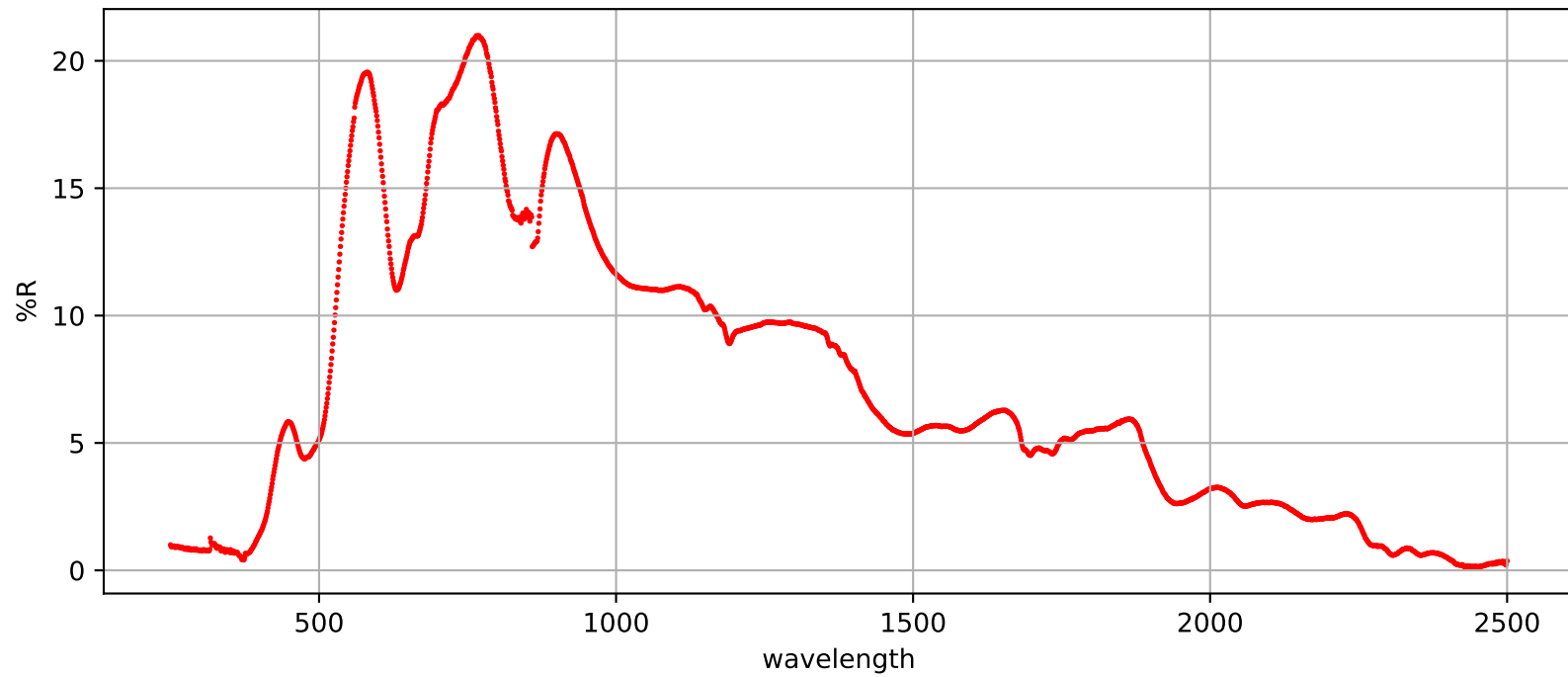




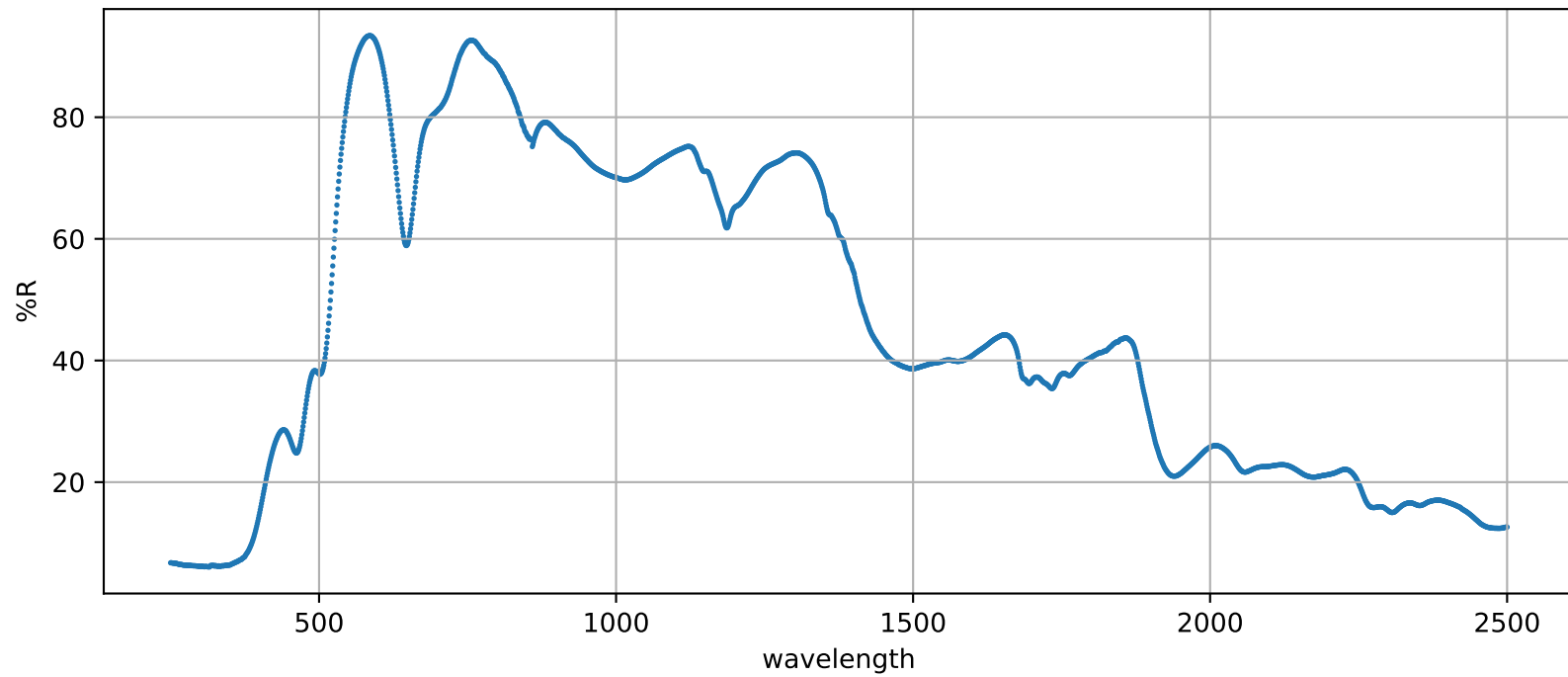
CICIMAUCR0008 avg



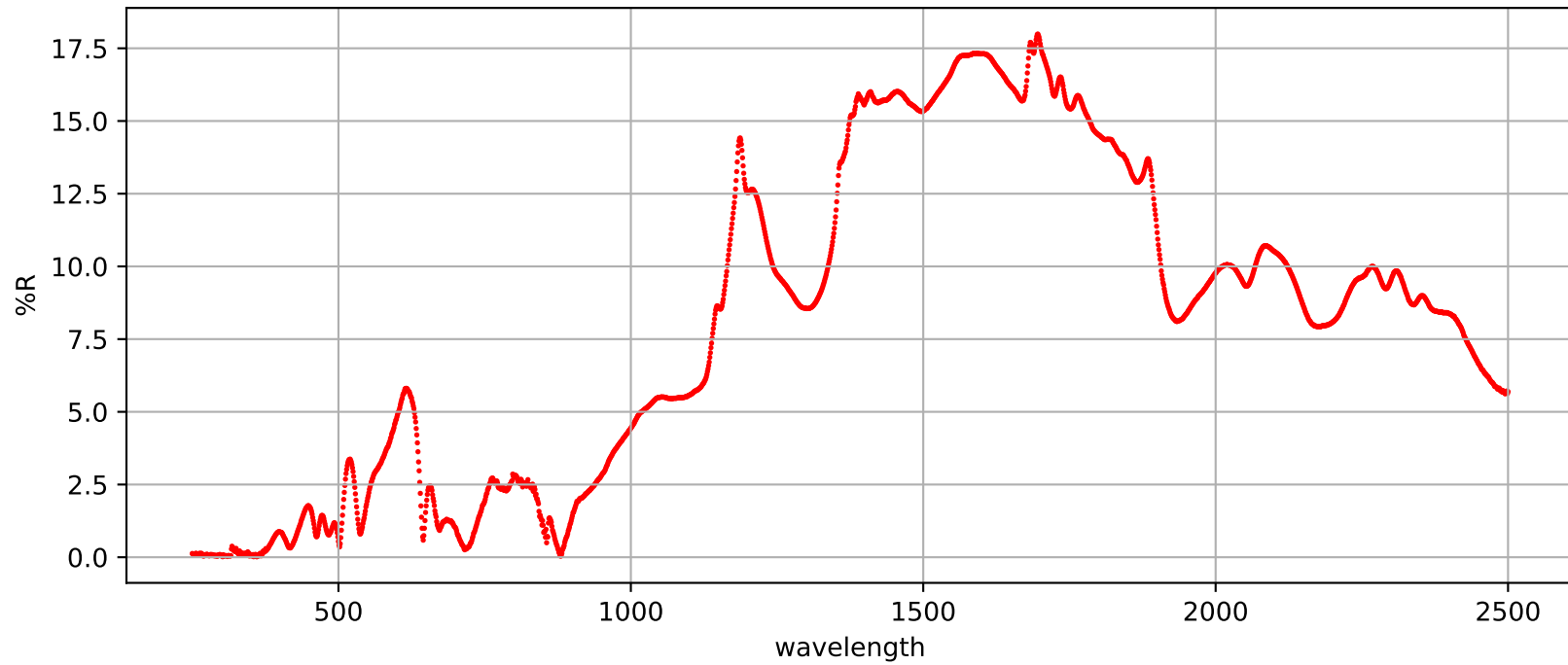
CICIMAUCR0008 std dev



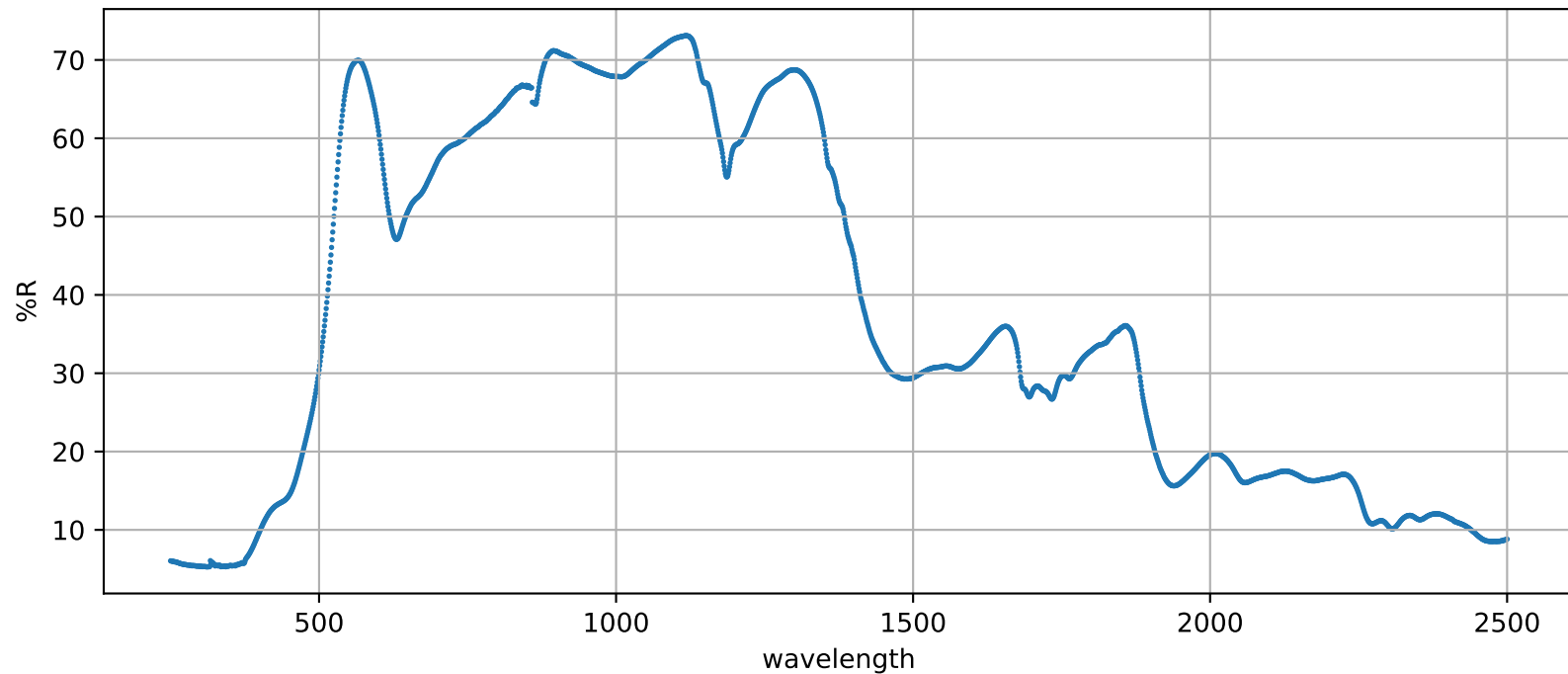
CICIMAUCR0104 avg



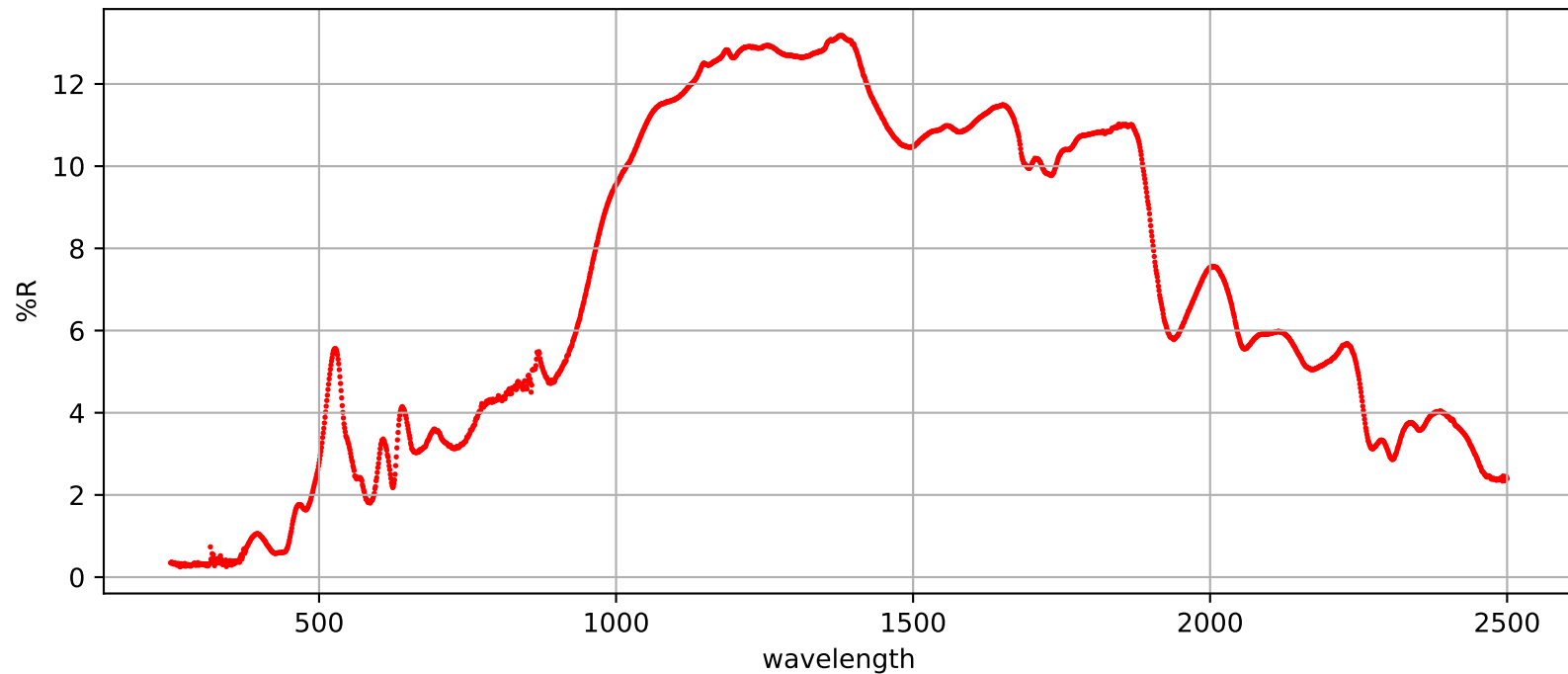
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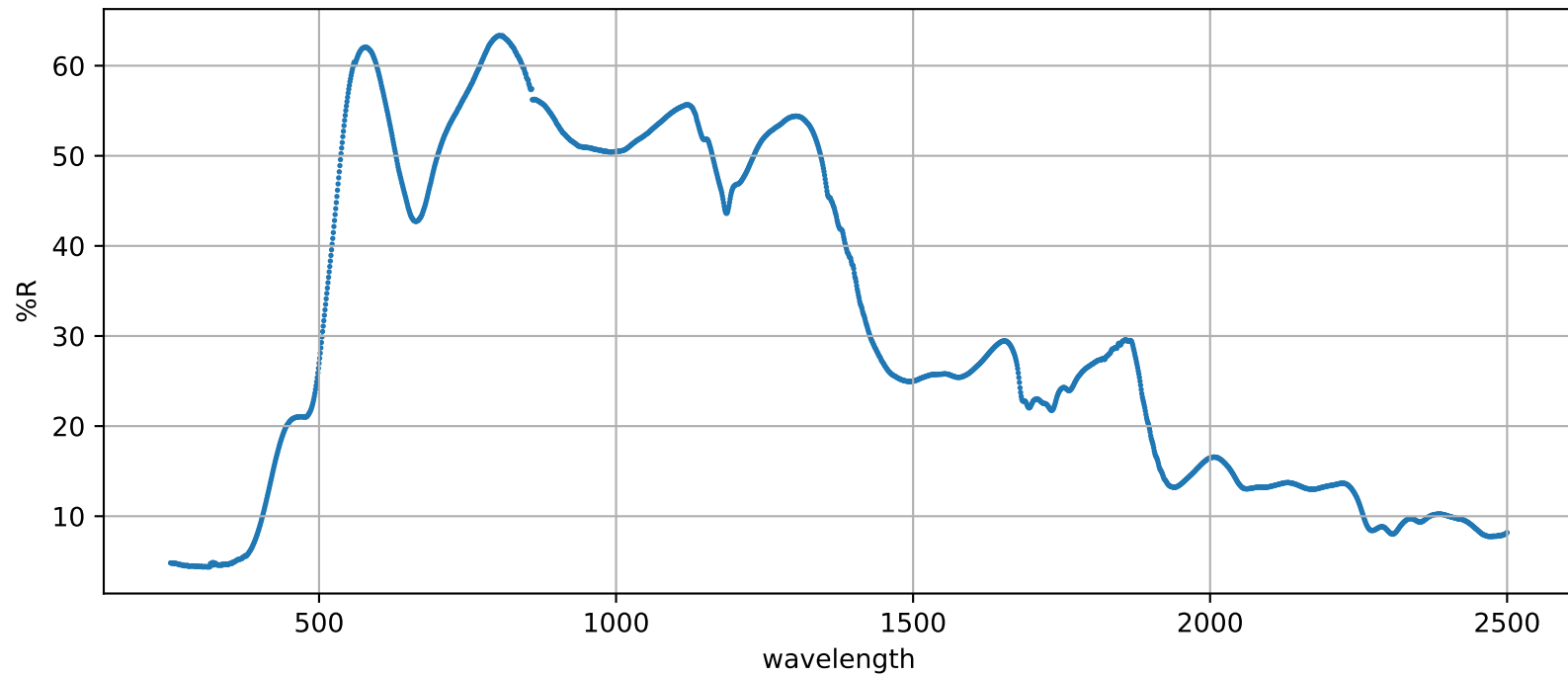
CICIMAUCR0019 avg



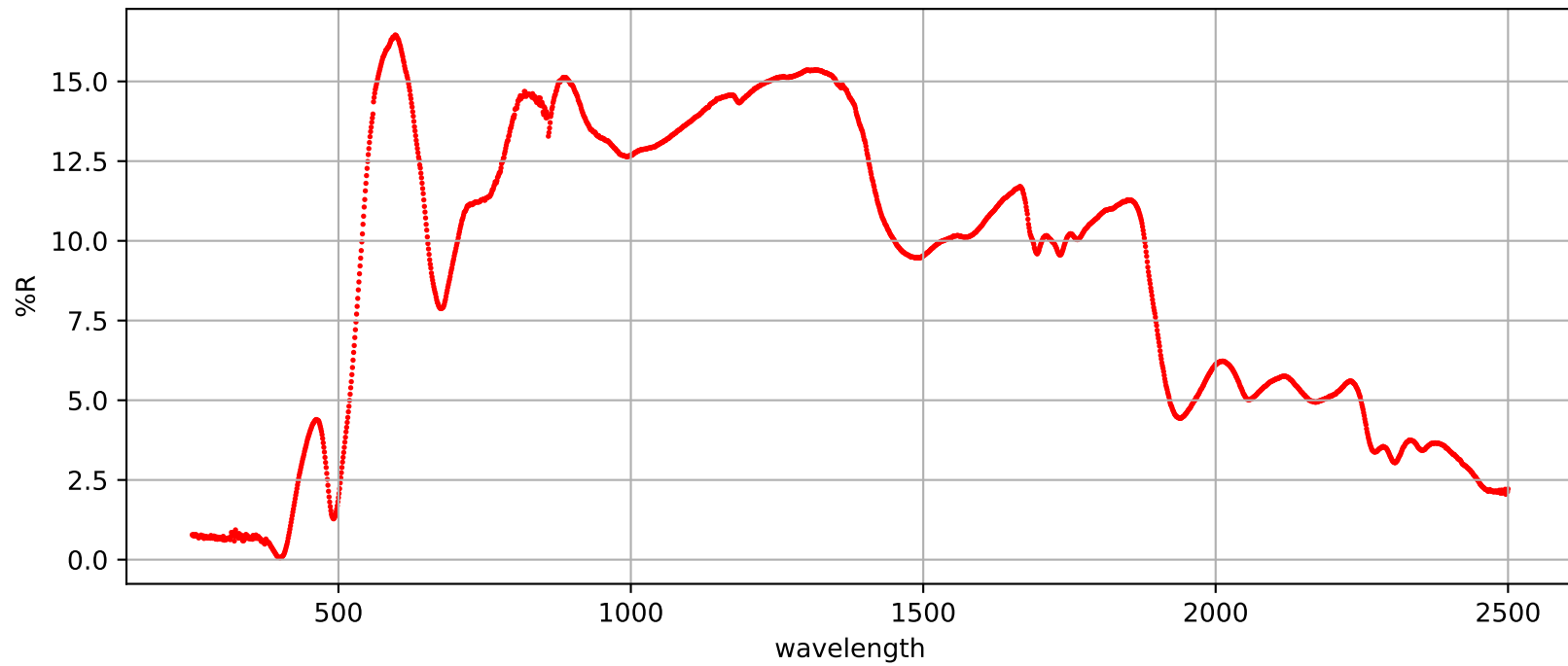
CICIMAUCR0019 std dev



CICIMAUCR0021 avg

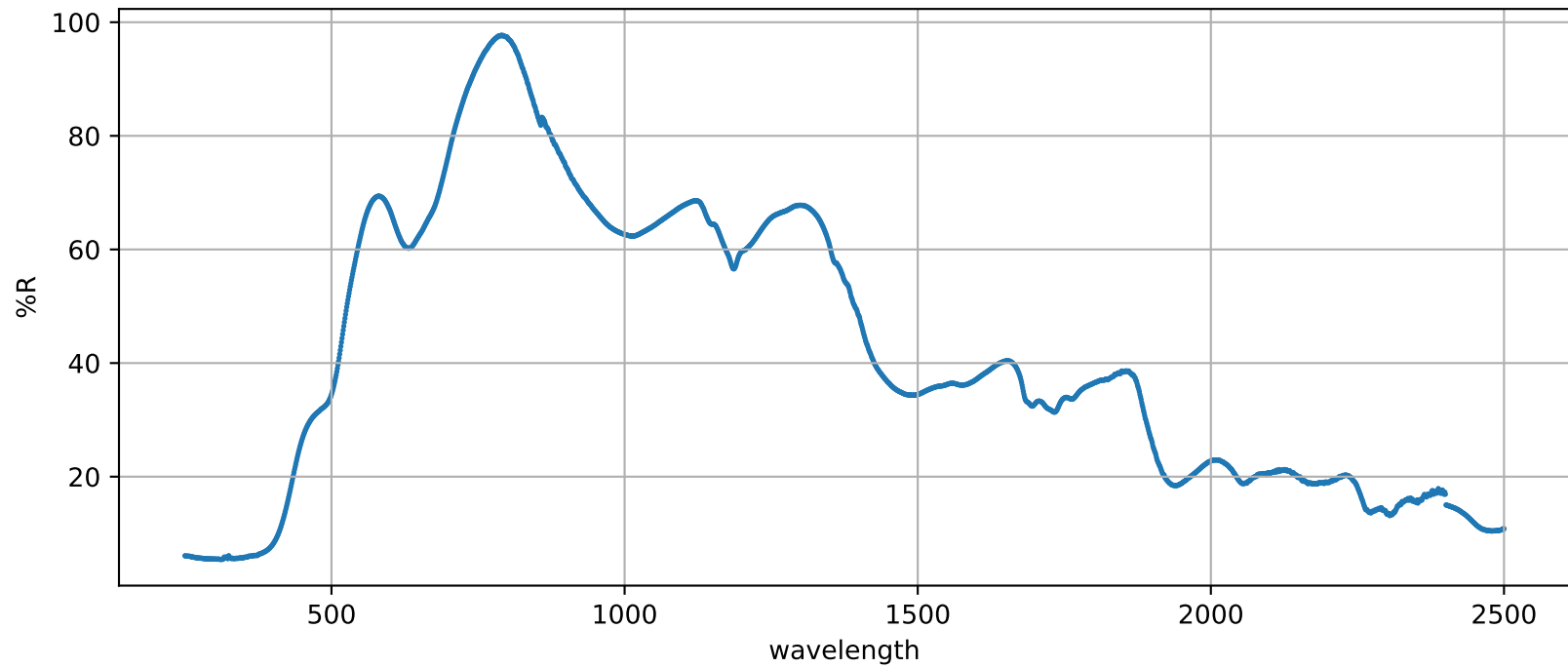


CICIMAUCR0021 std dev

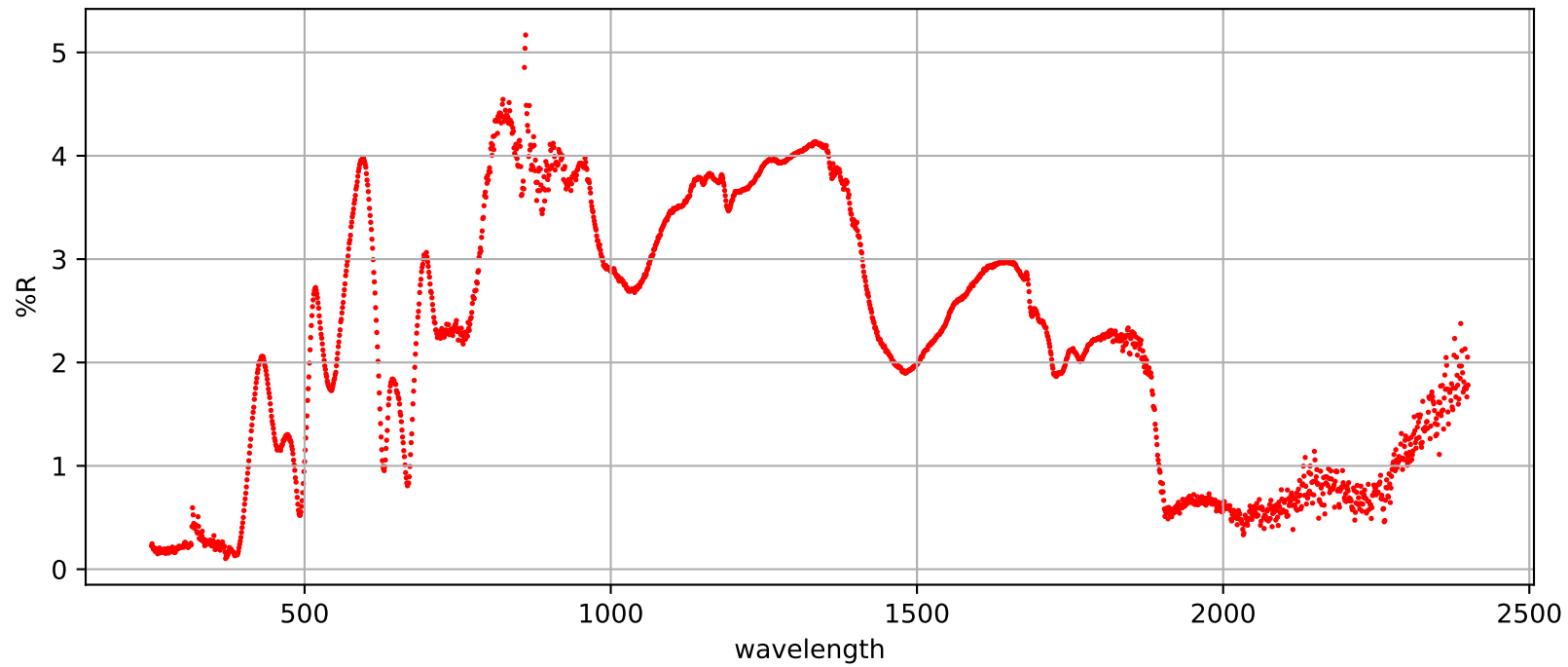




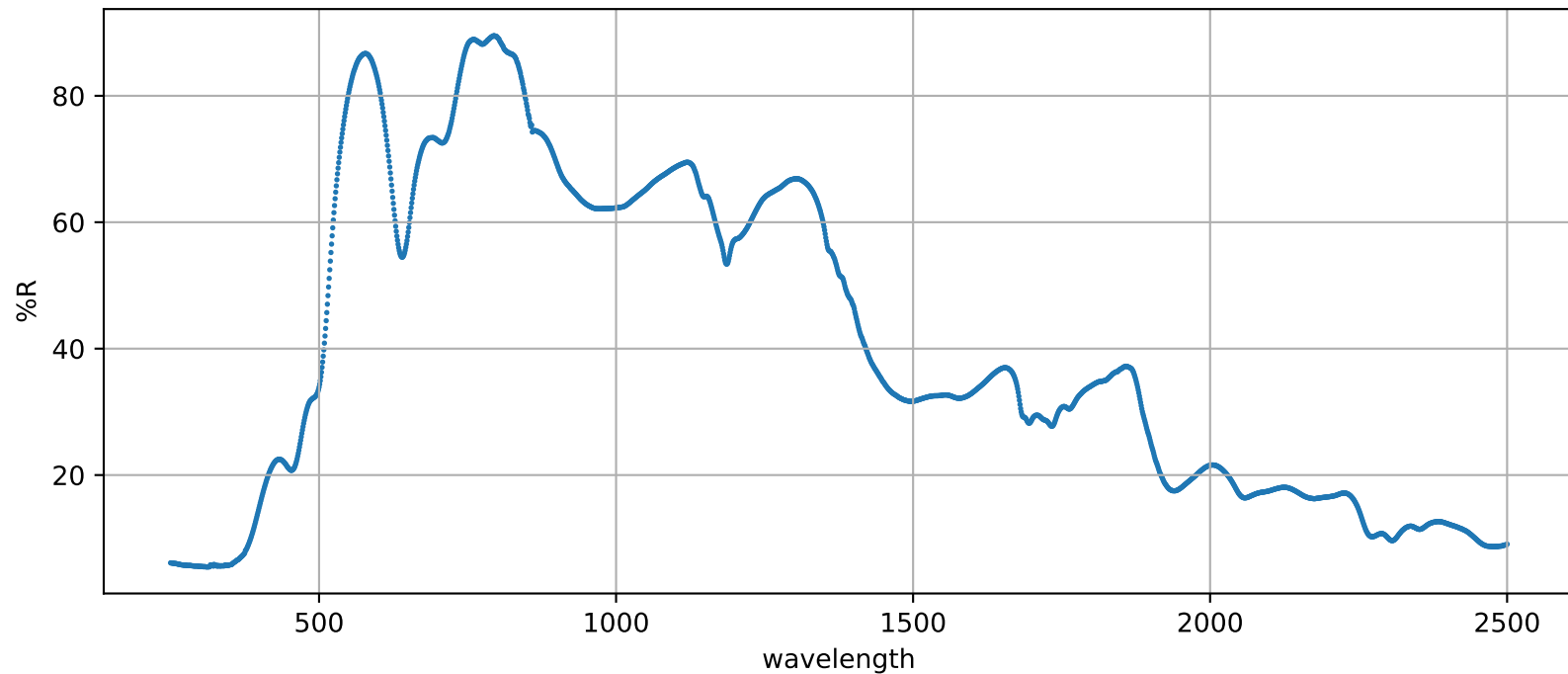
CICIMAUCR0116 avg



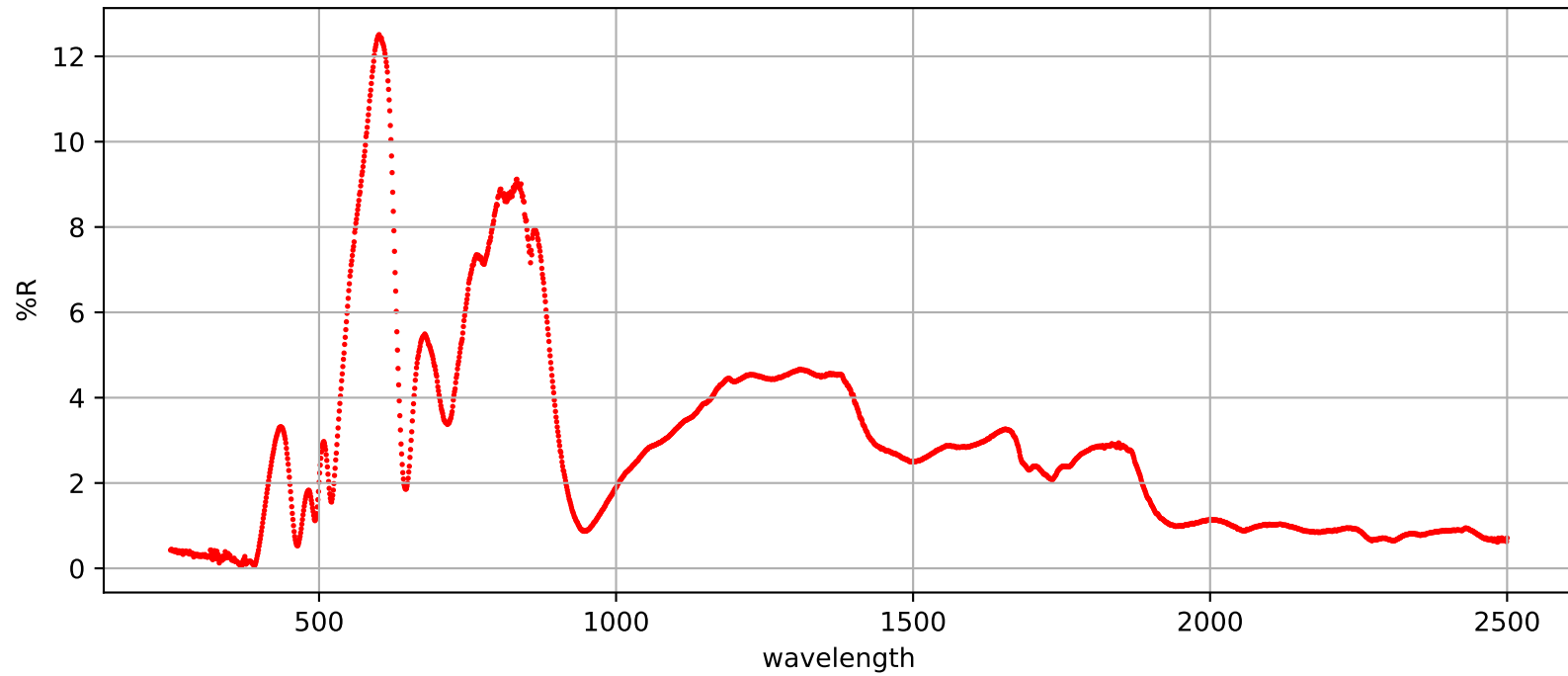
CICIMAUCR0116 std dev



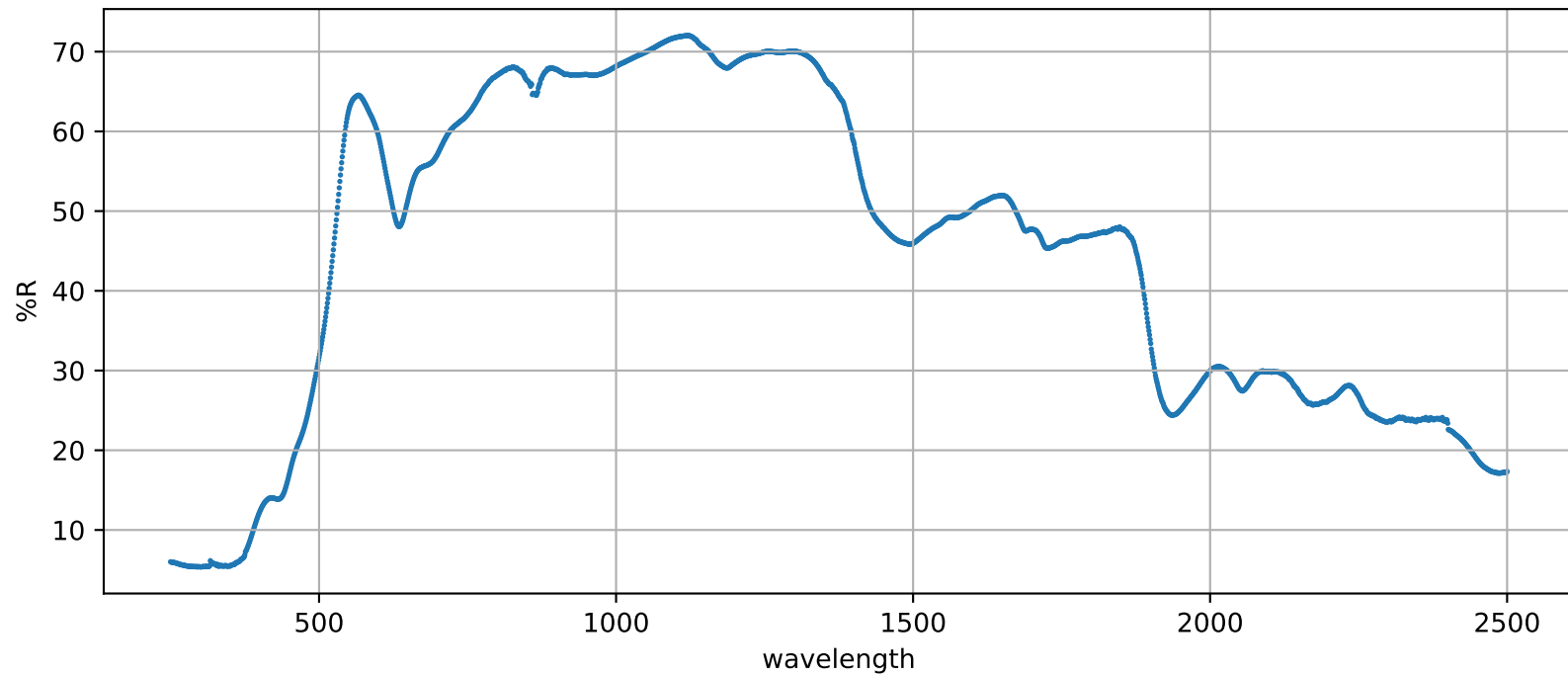
CICIMAUCR0015 avg



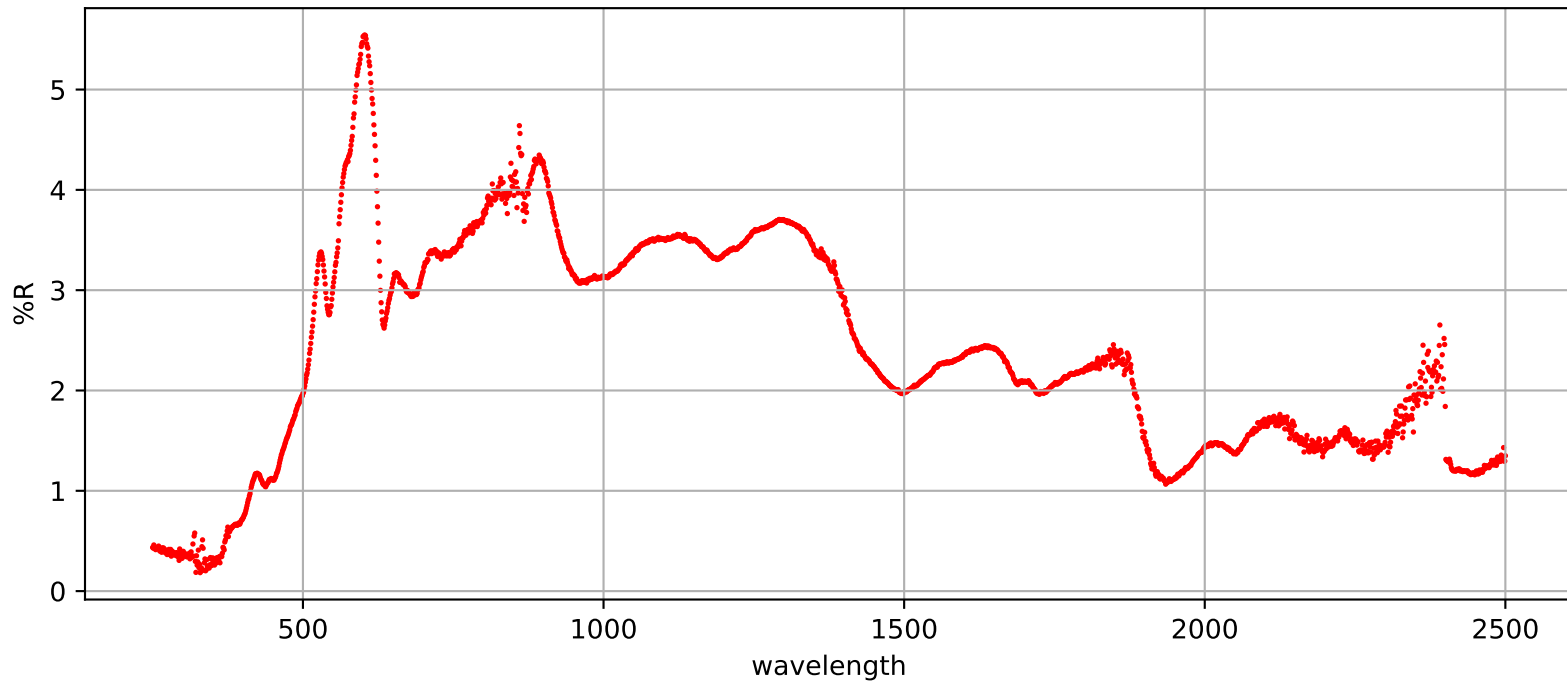
CICIMAUCR0015 std dev



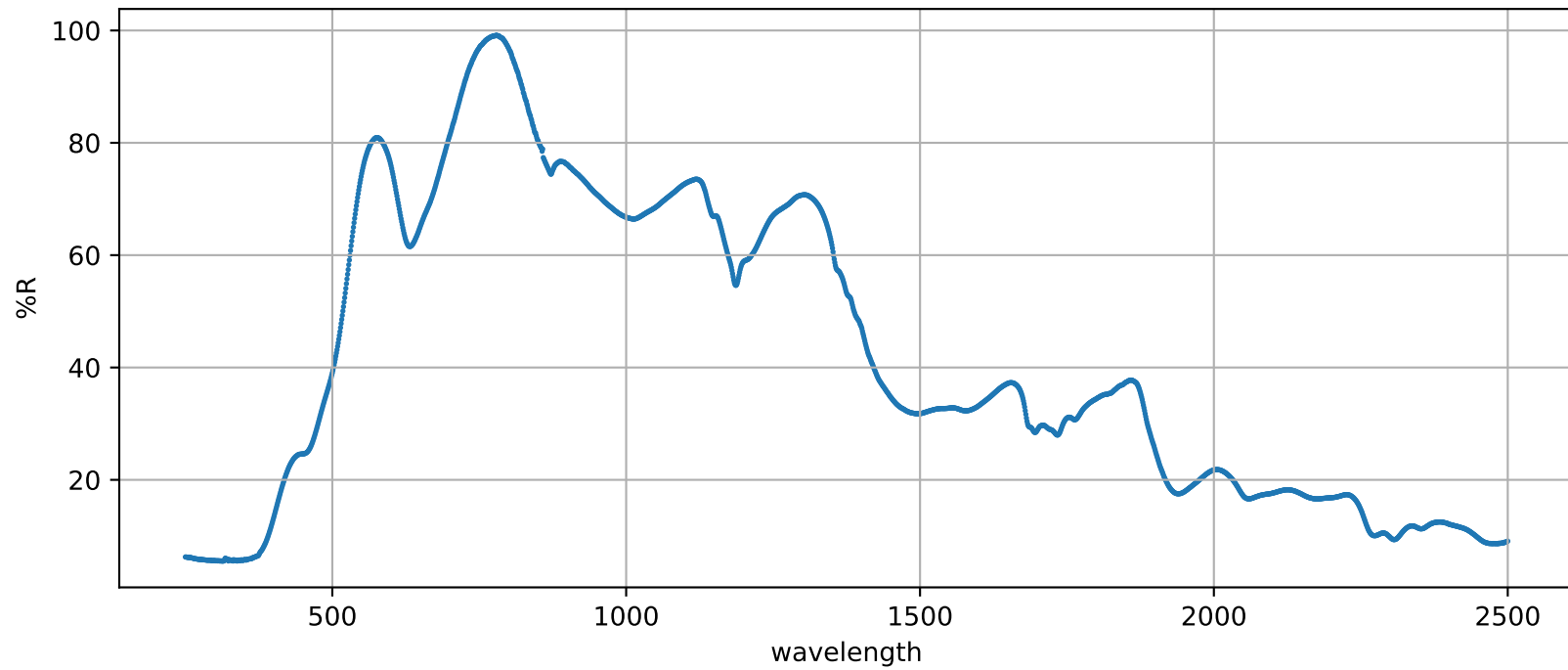
CICIMAUCR0158 avg



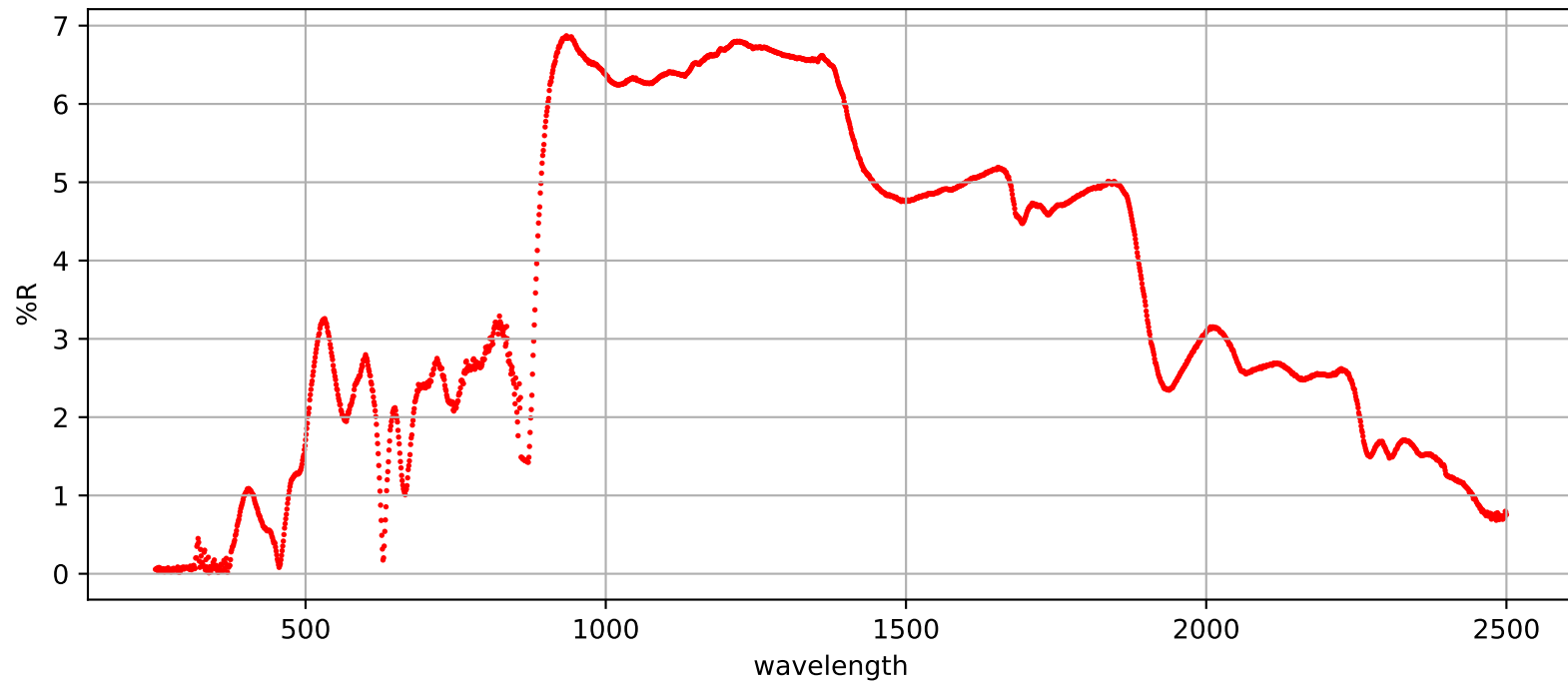
CICIMAUCR0158 std dev



CICIMAUCR0003 avg

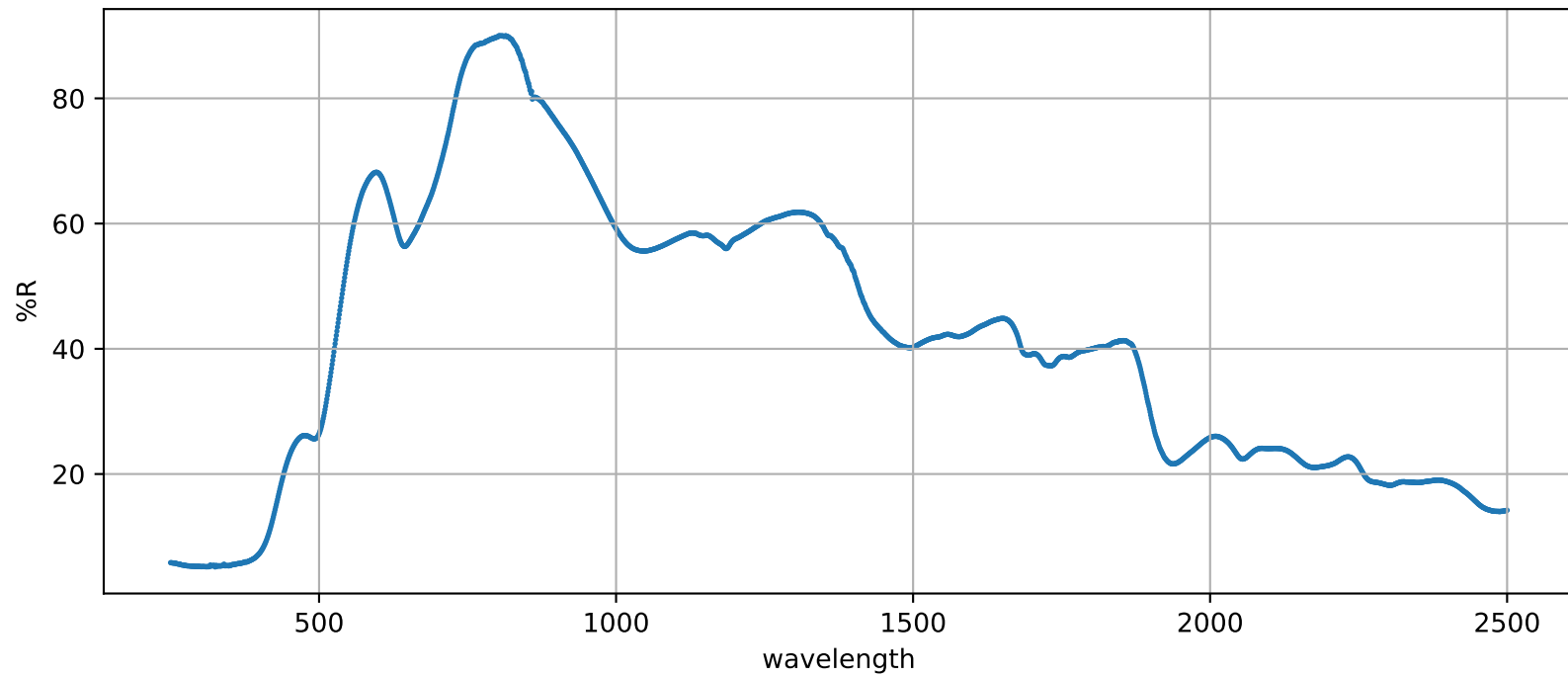


CICIMAUCR0003 std dev

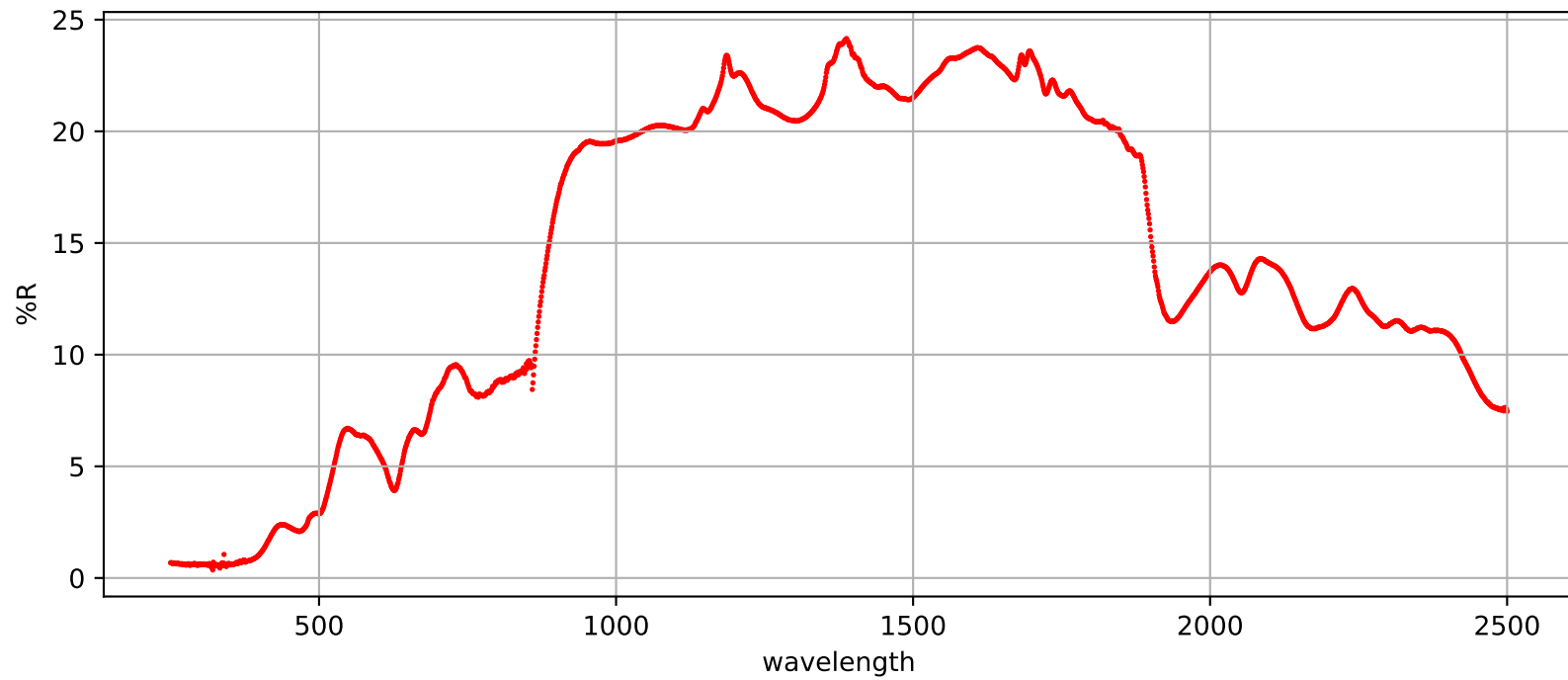




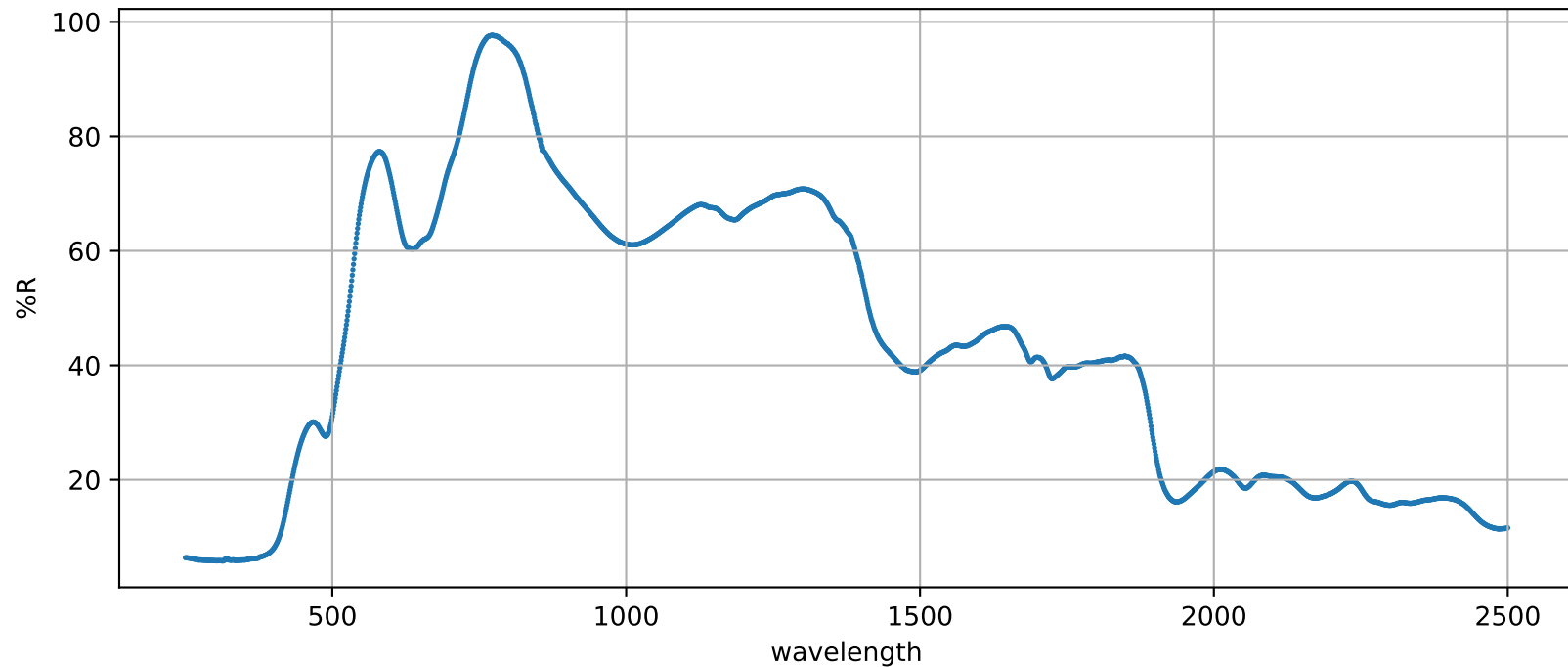
CICIMAUCR0097 avg



CICIMAUCR0097 std dev



CICIMAUCR0141 avg



CICIMAUCR0141 std dev

