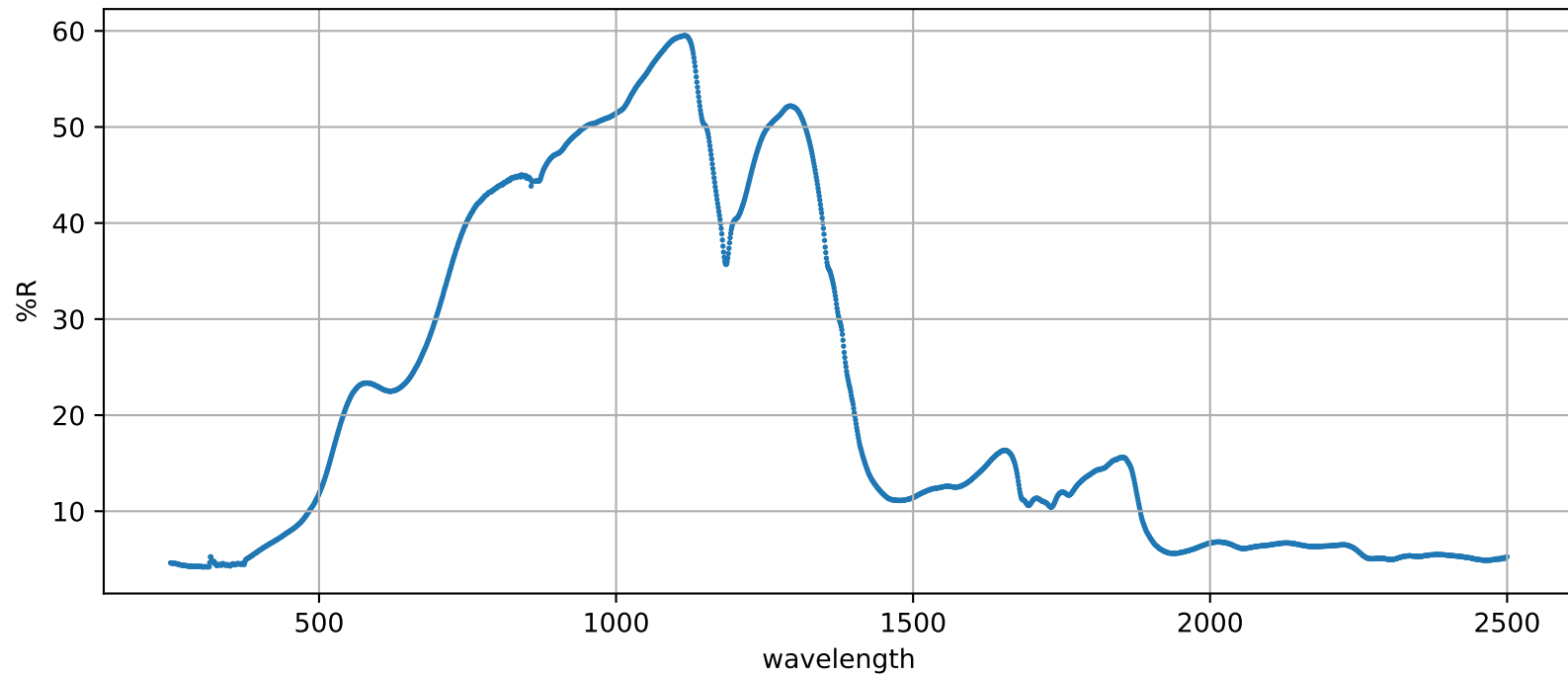
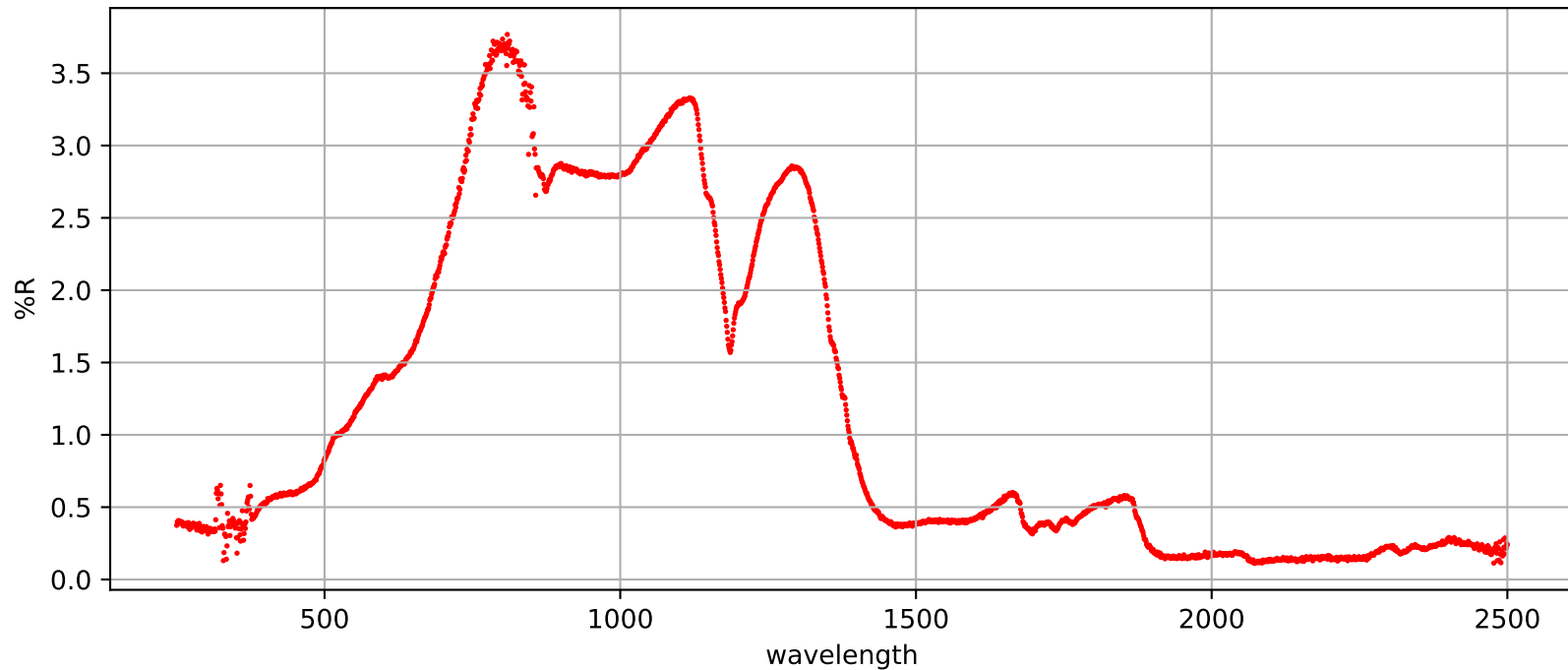


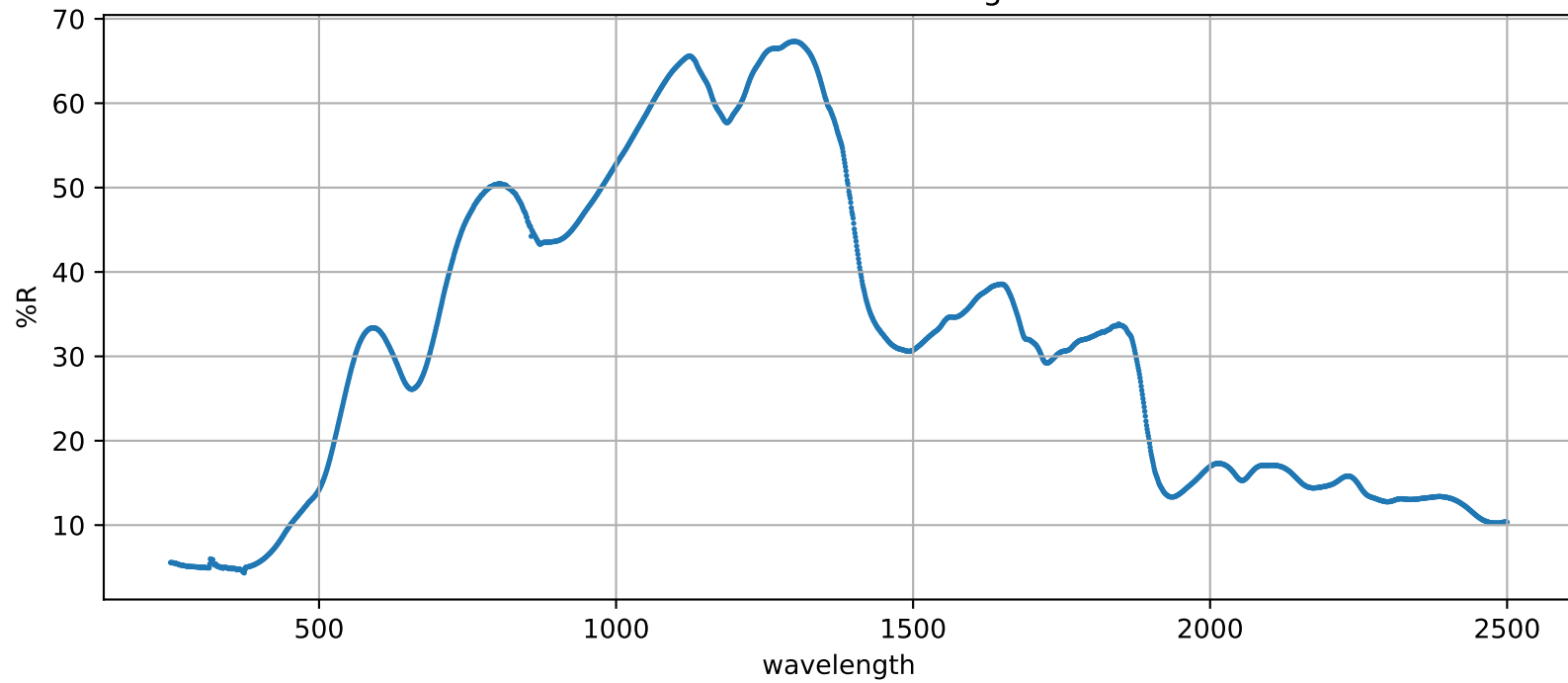
CICIMAUCR0225 avg



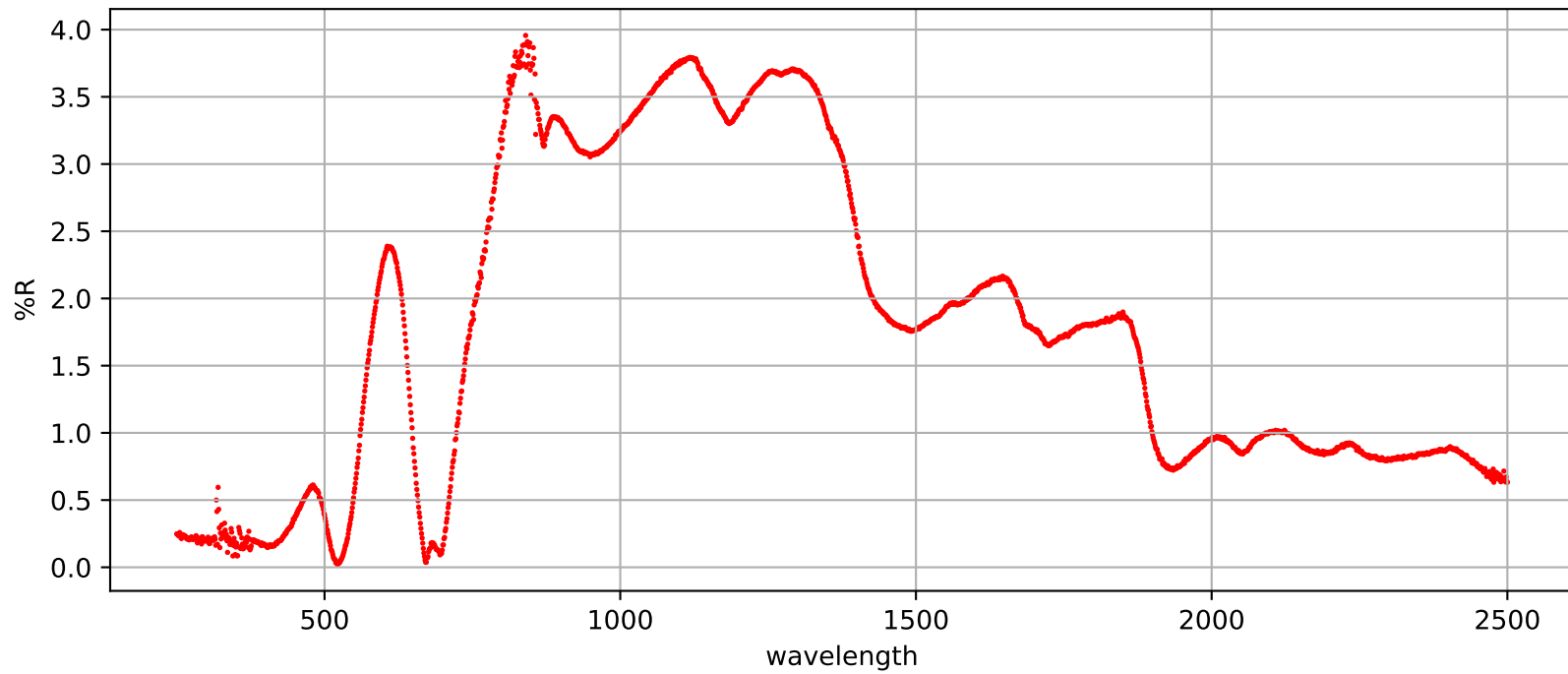
CICIMAUCR0225 std dev



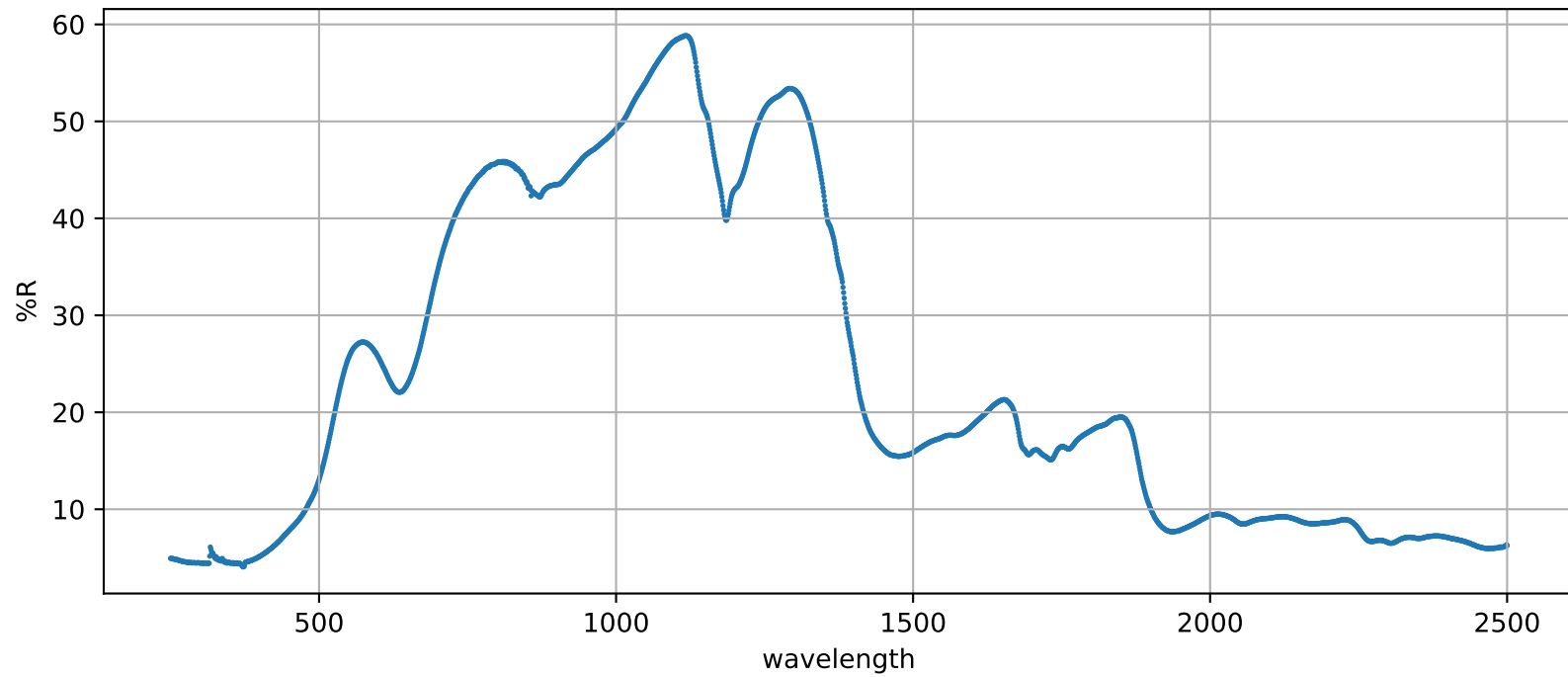
CICIMAUCR0205 avg



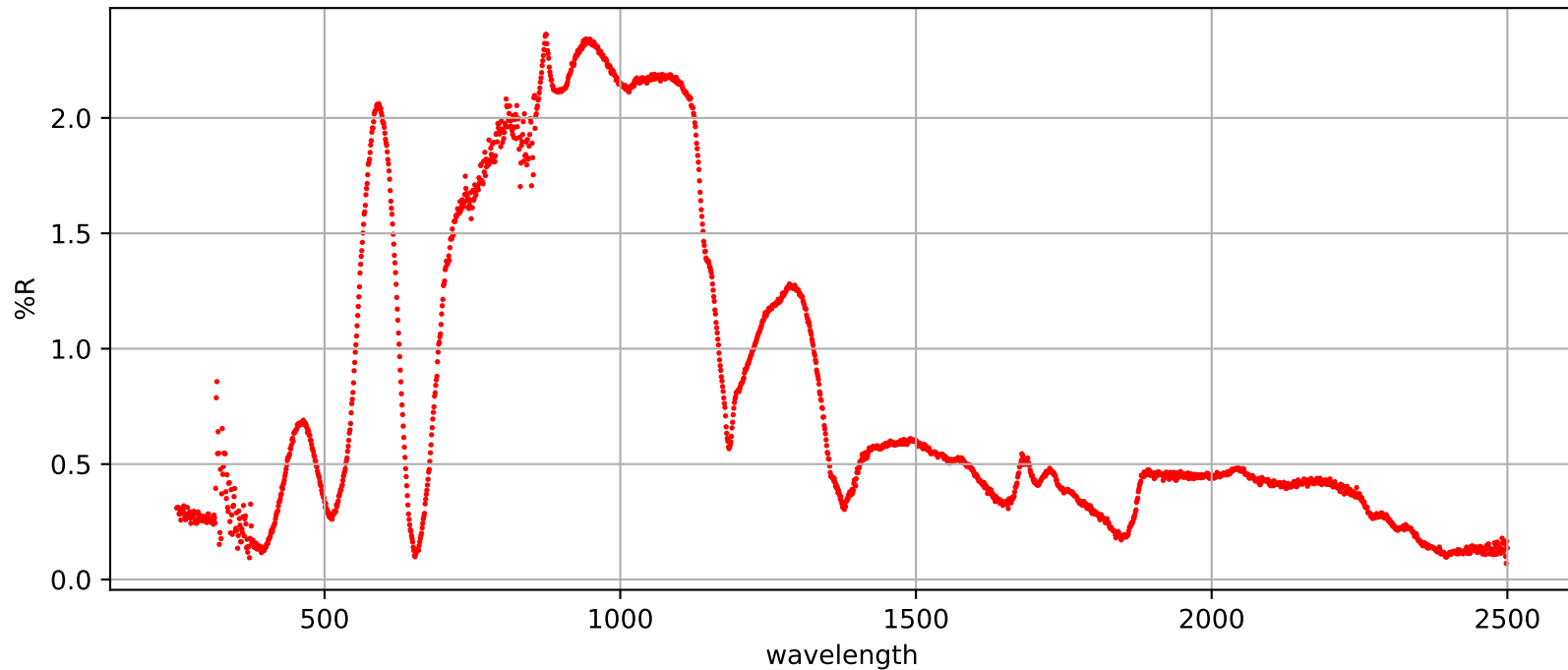
CICIMAUCR0205 std dev



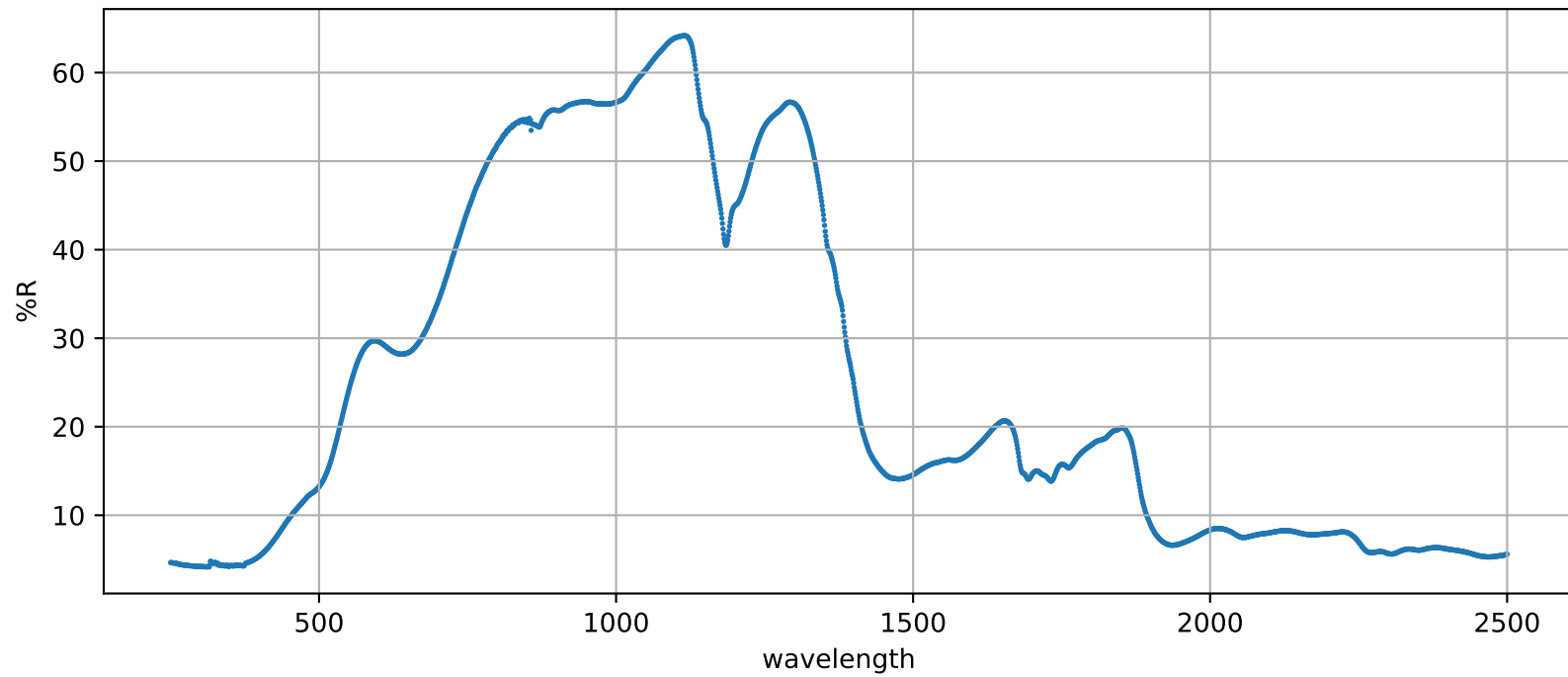
CICIMAUCR0201 avg



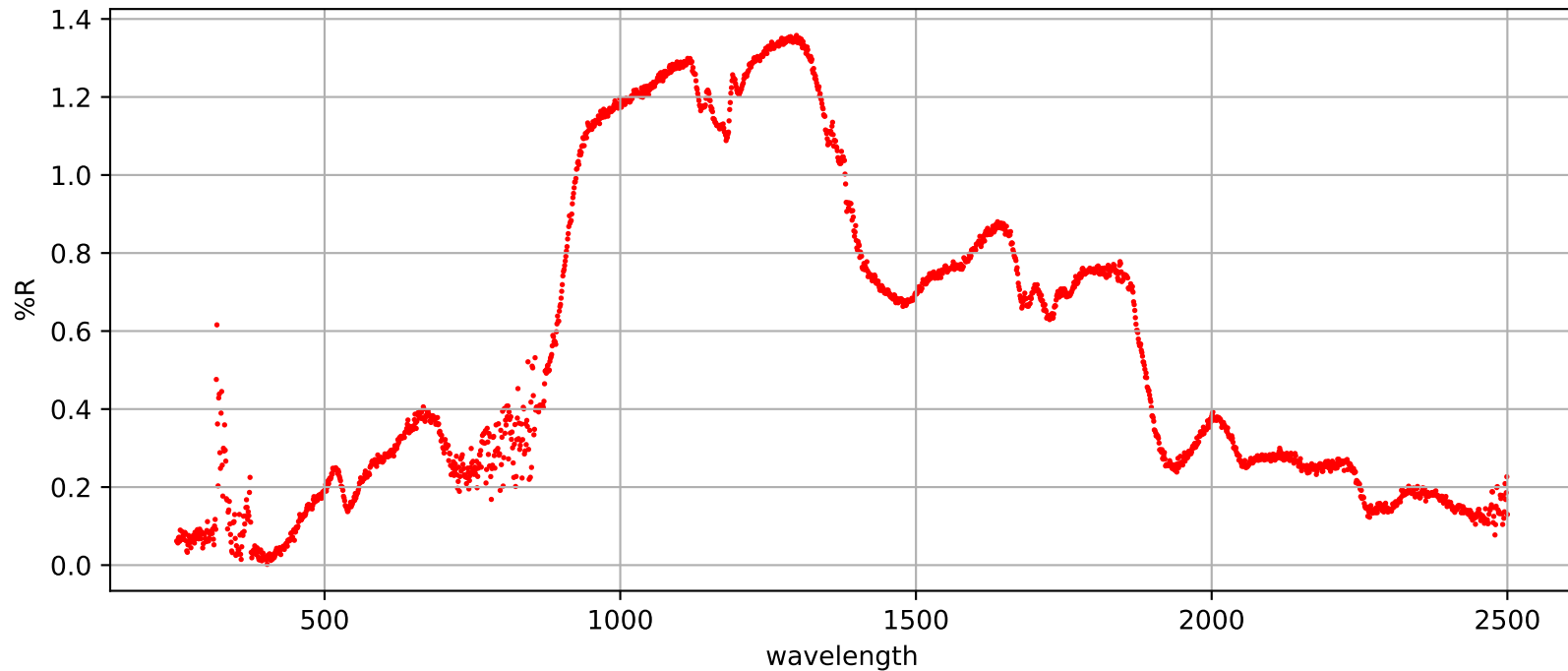
CICIMAUCR0201 std dev



CICIMAUCR0250 avg

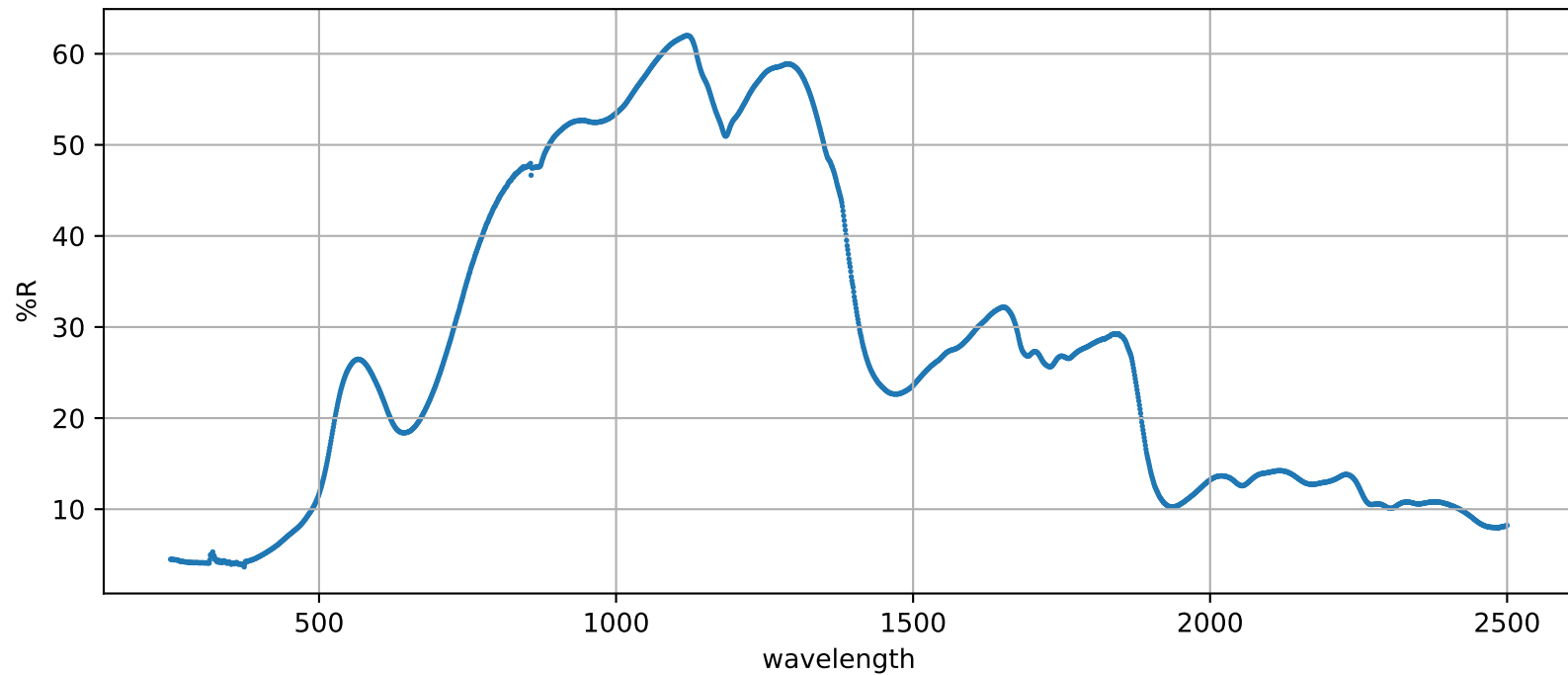


CICIMAUCR0250 std dev

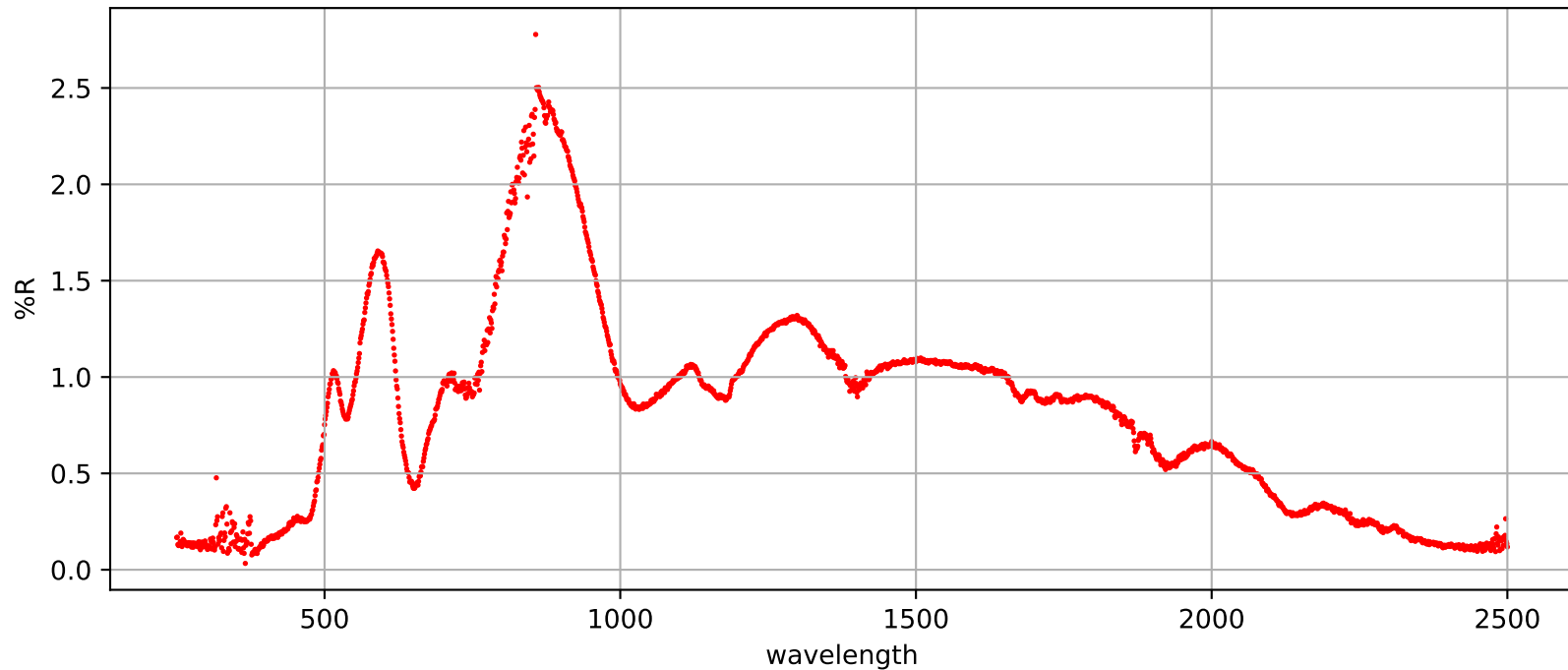




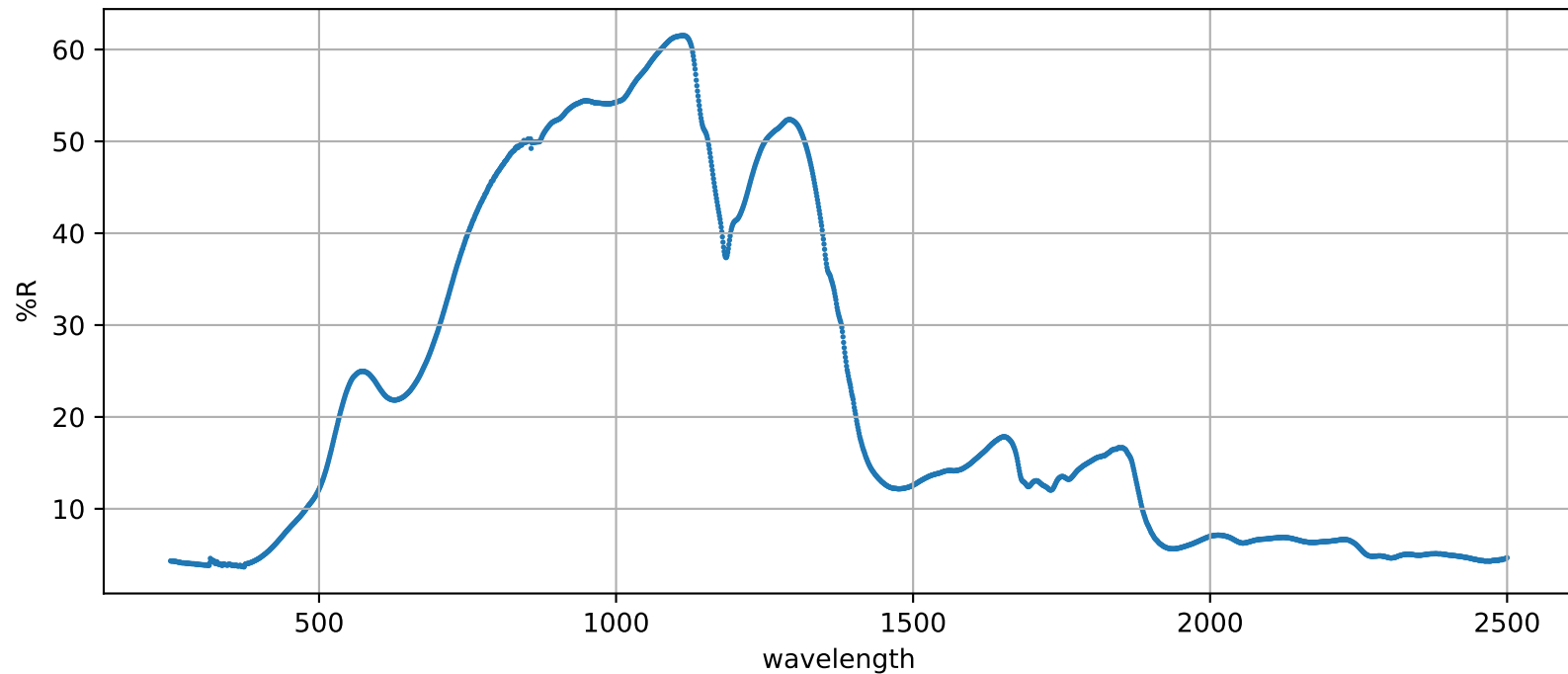
CICIMAUCR0204 avg



CICIMAUCR0204 std dev



CICIMAUCR0251 avg



CICIMAUCR0251 std dev

