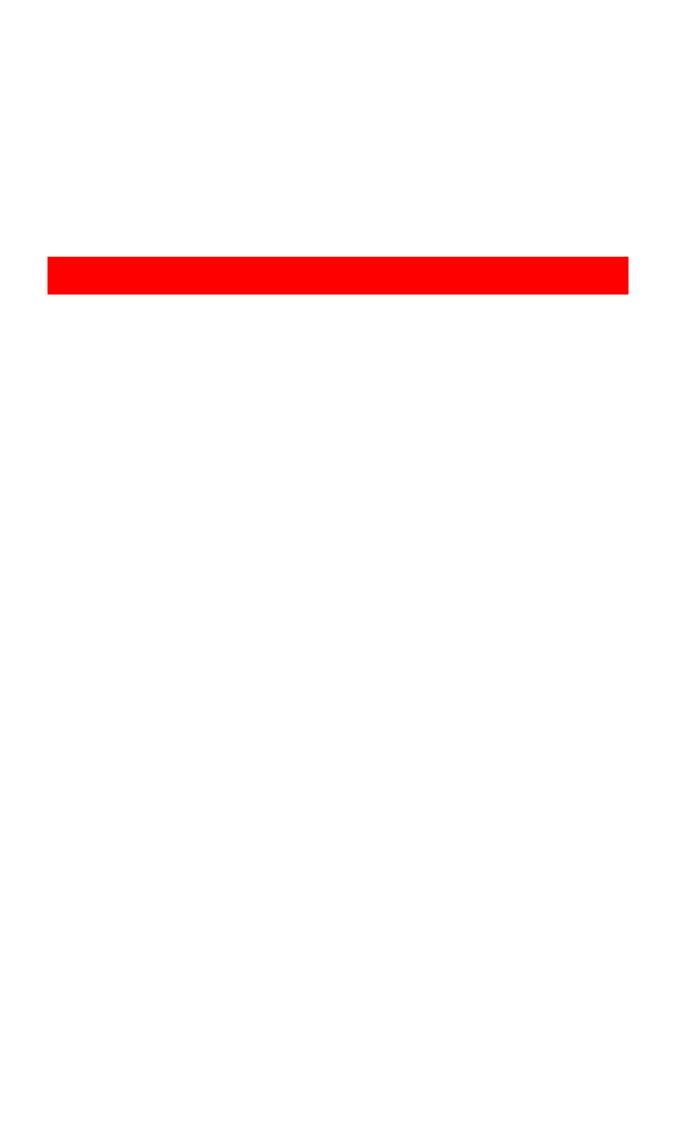
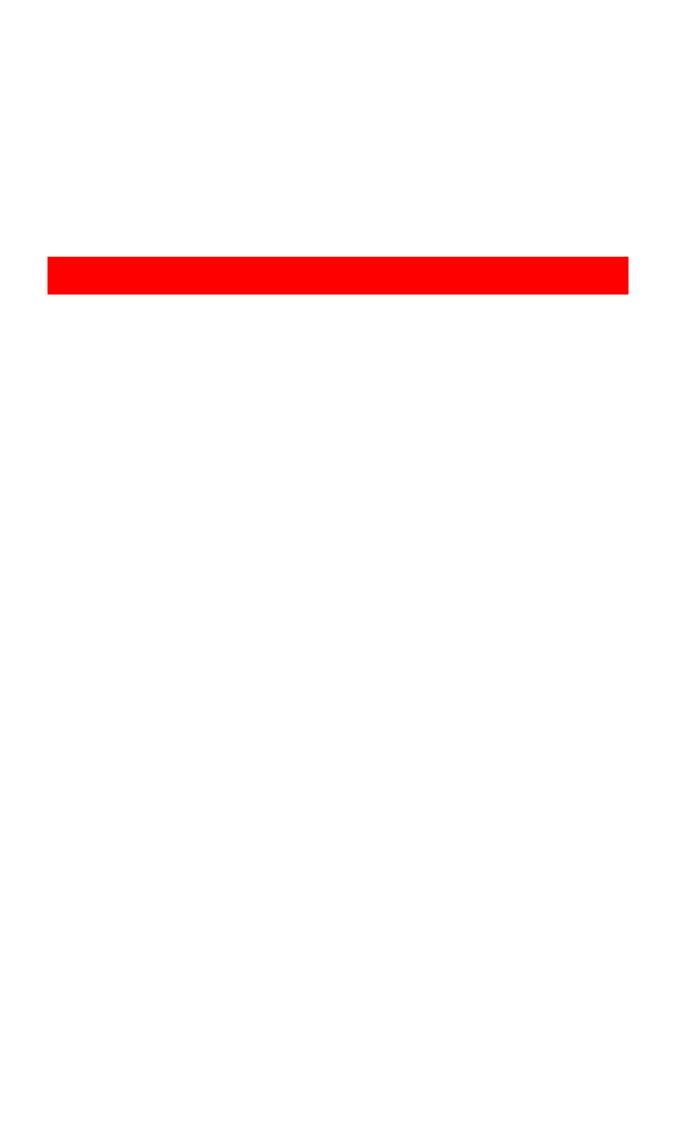


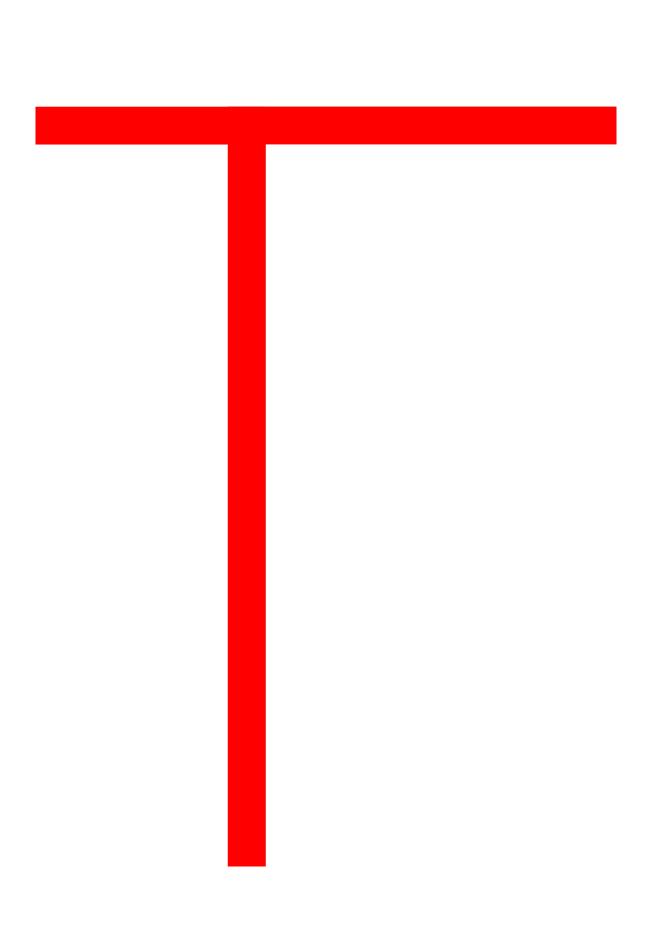


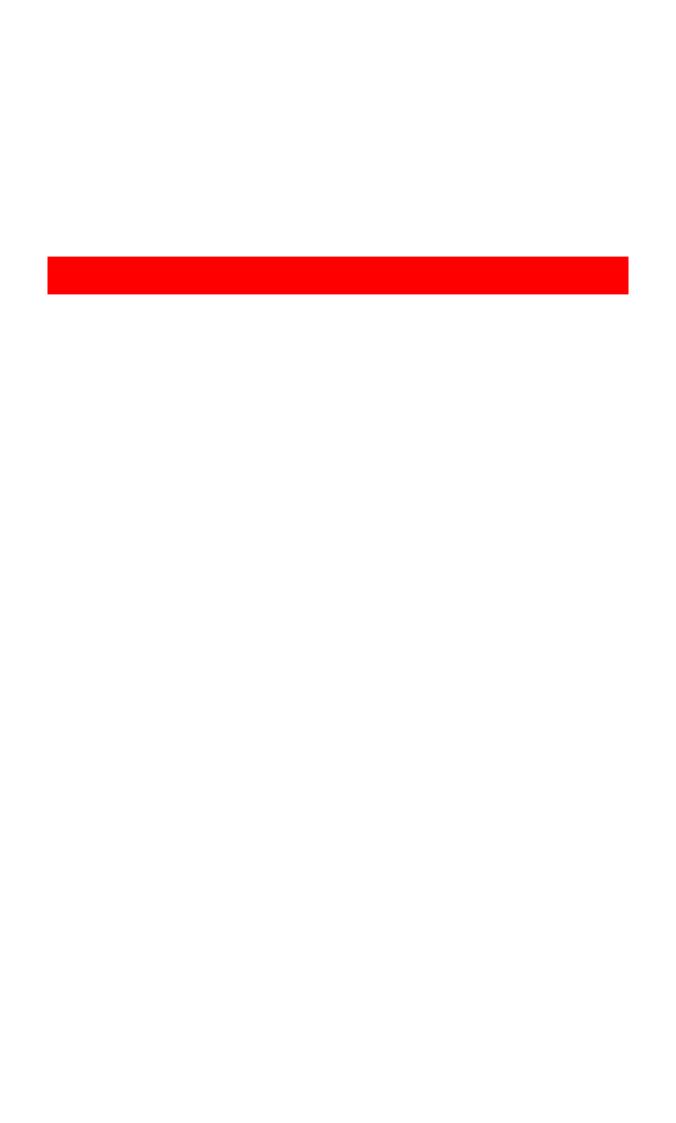
nmediat

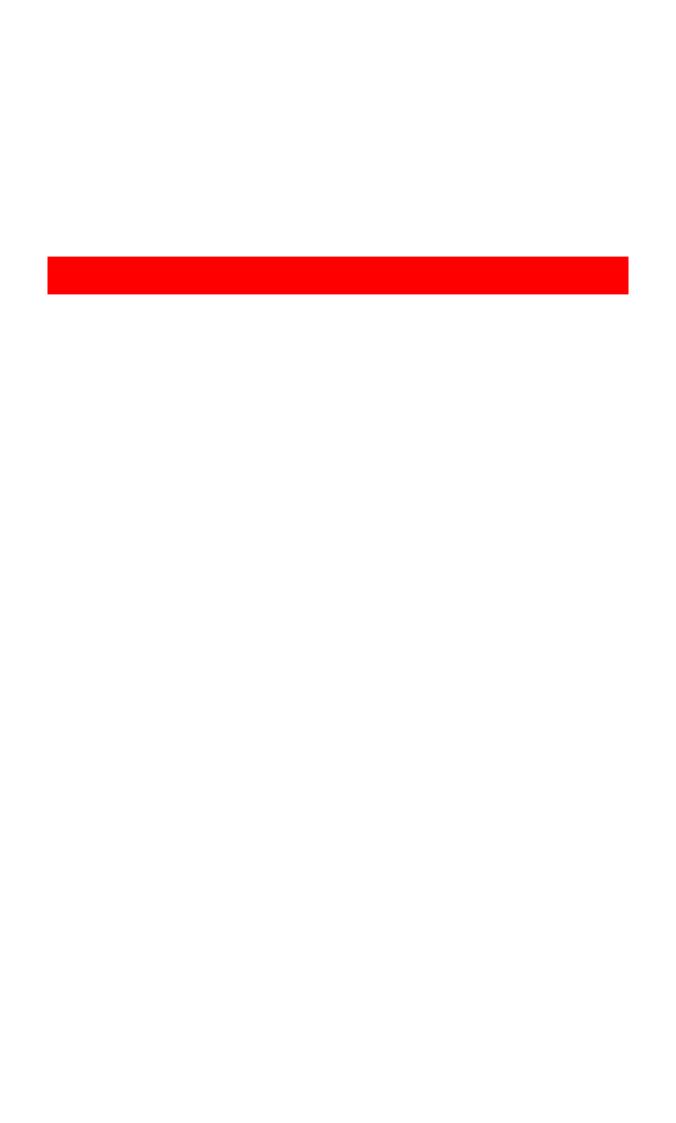
e

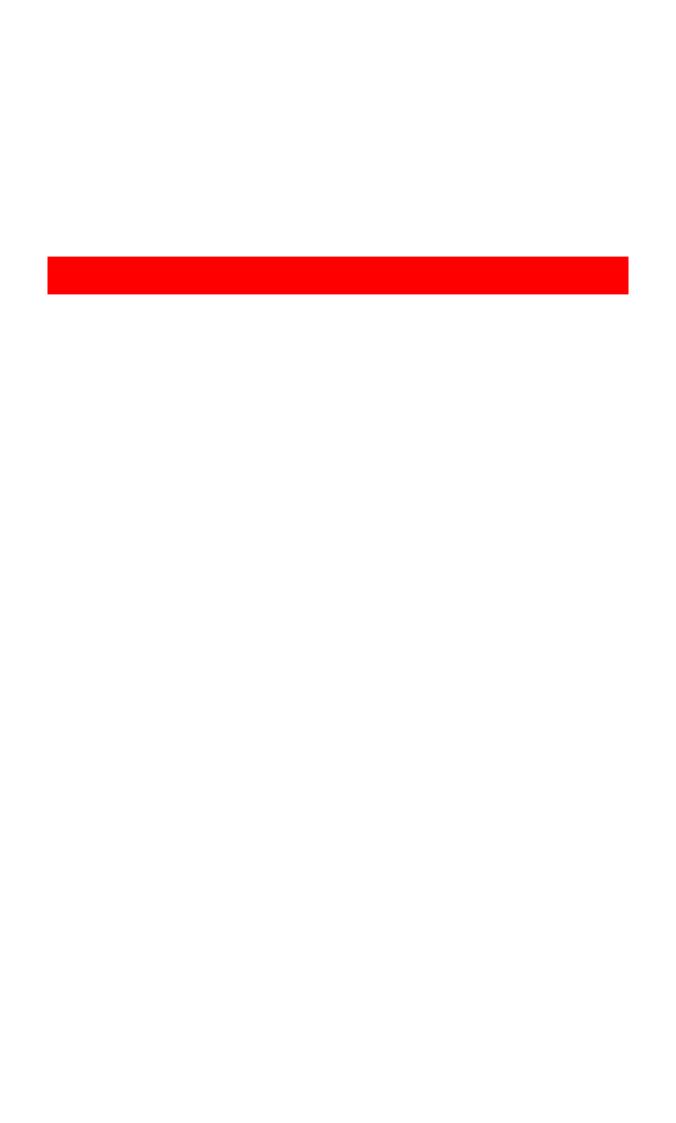


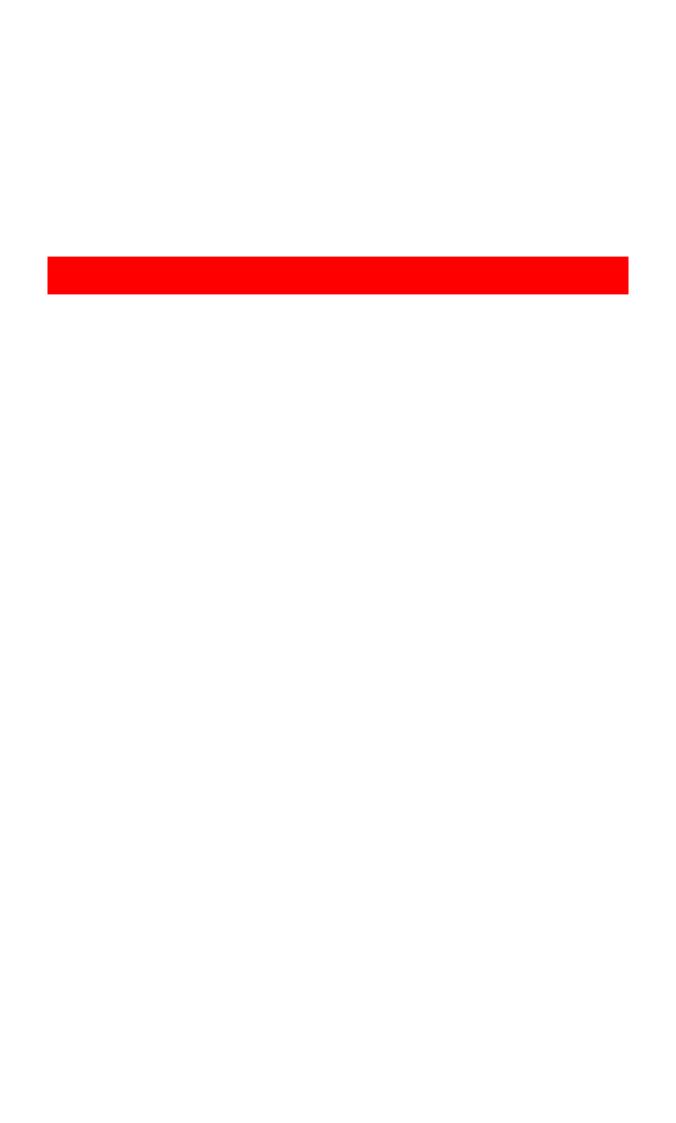








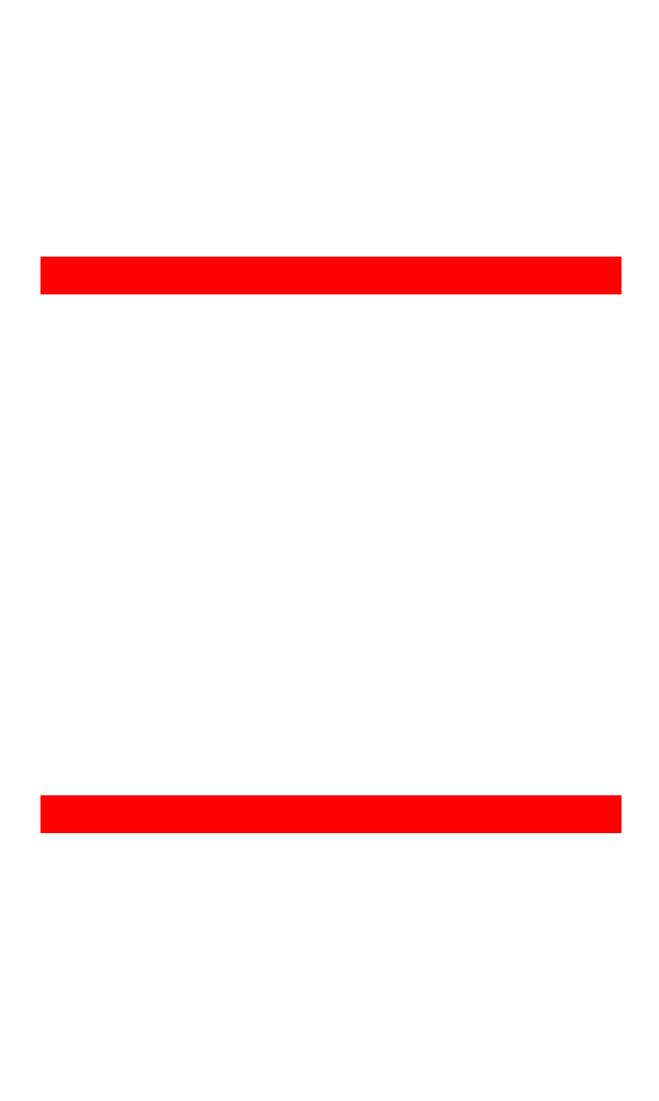


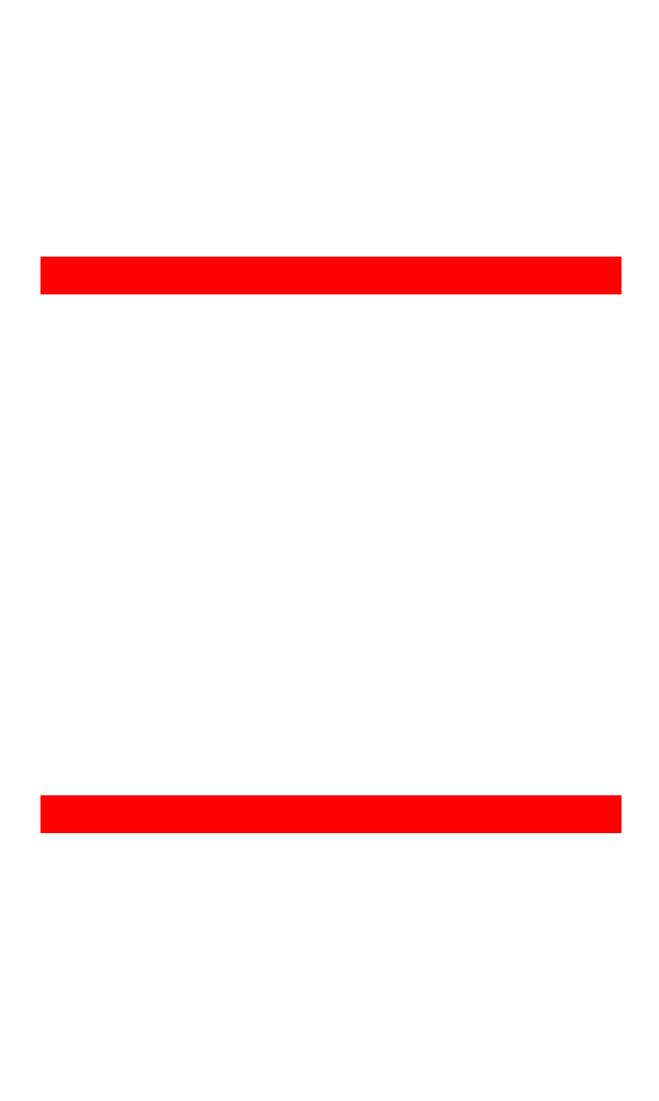


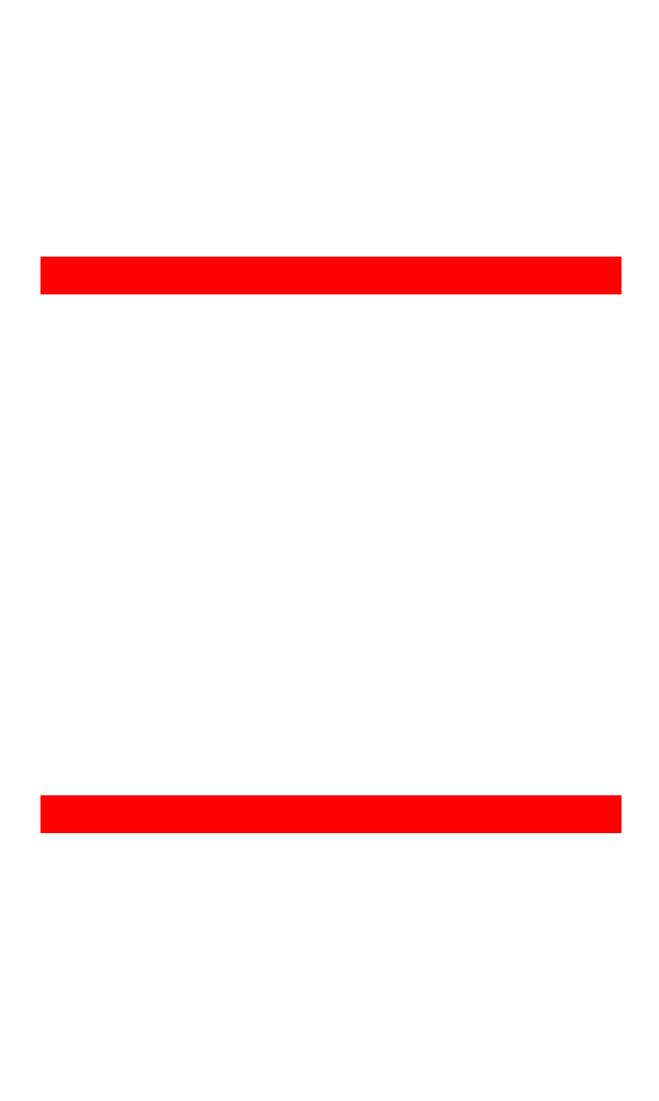
immec



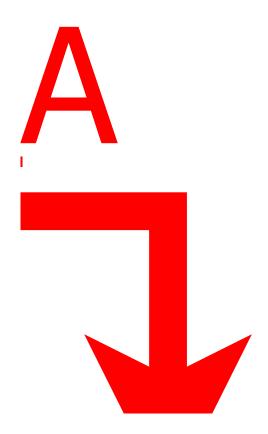
liate

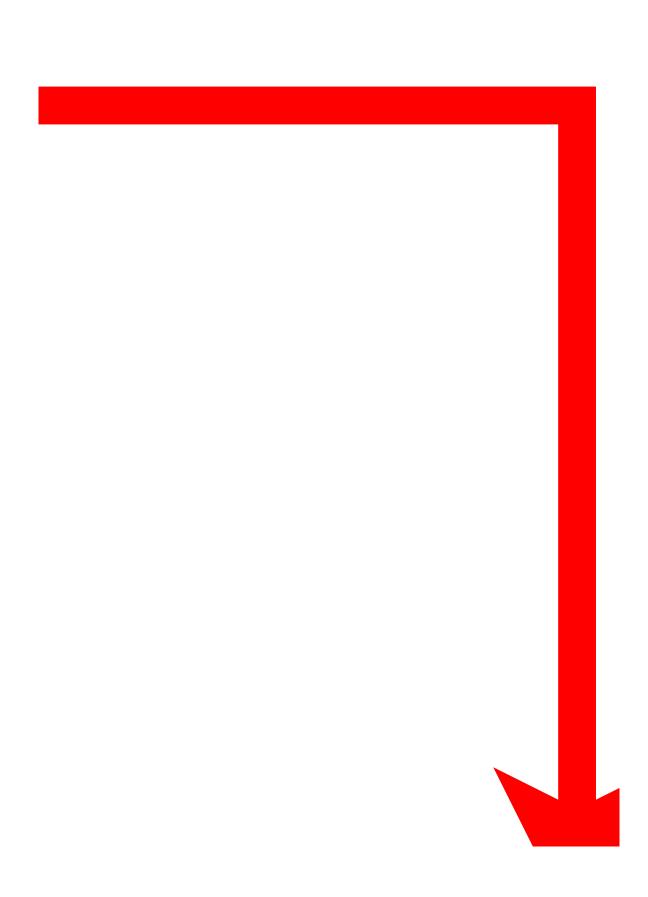






RF





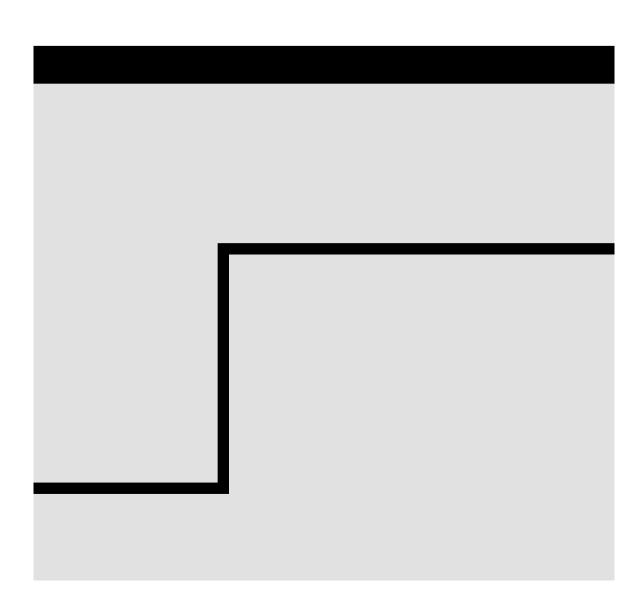


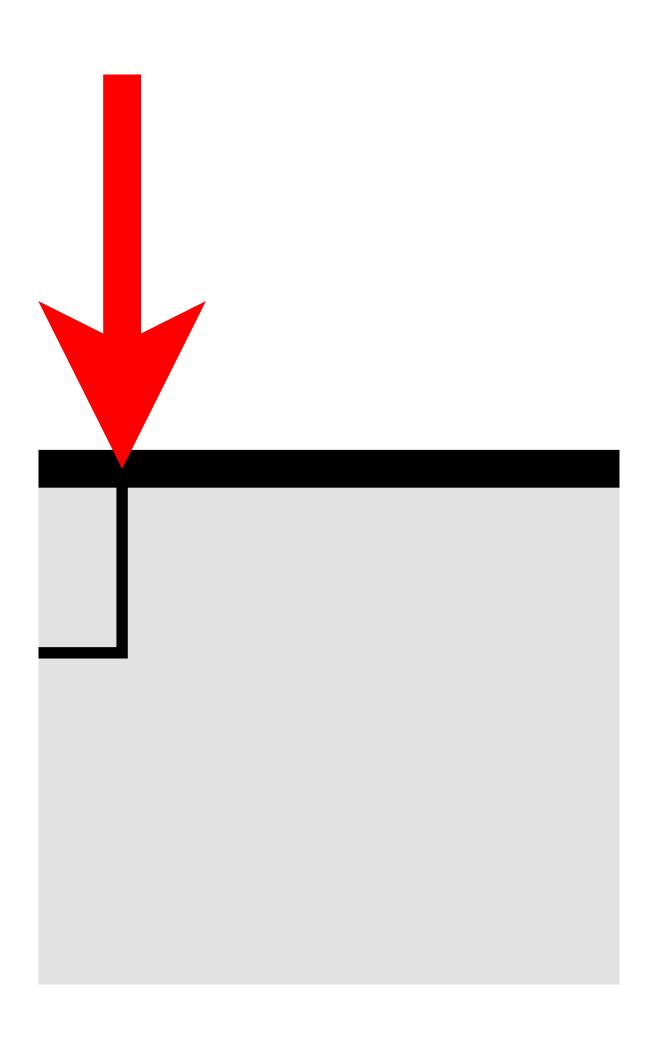


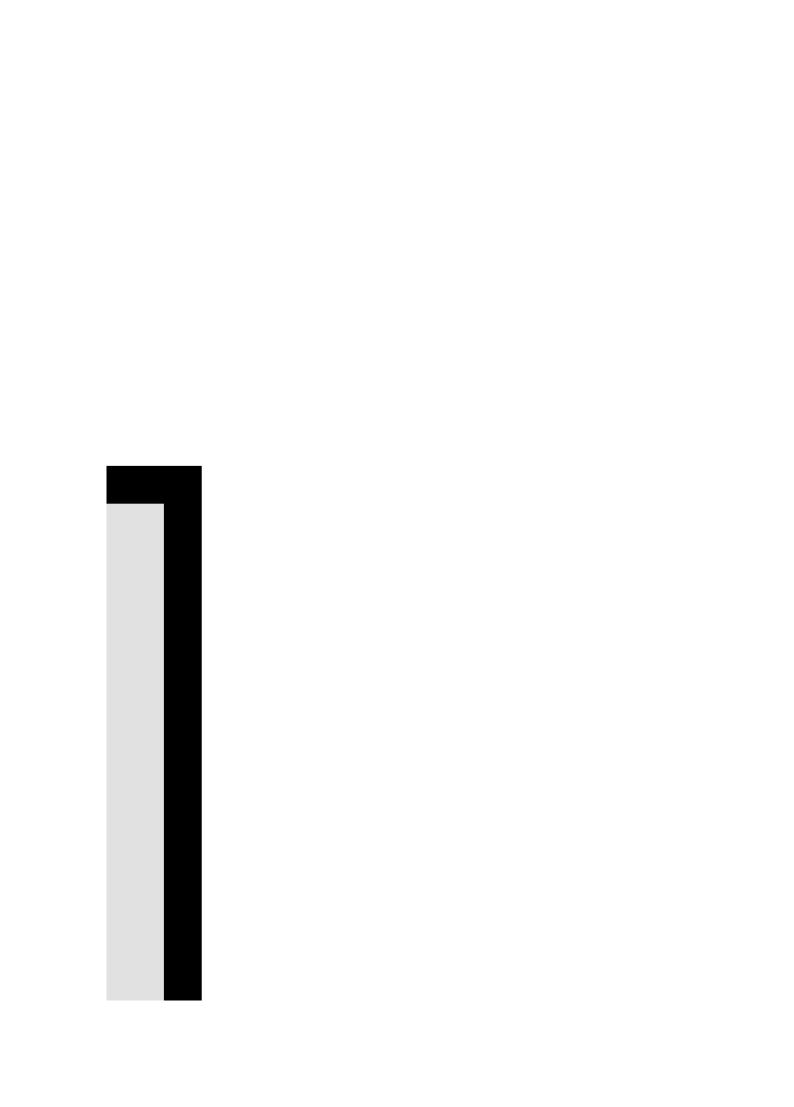


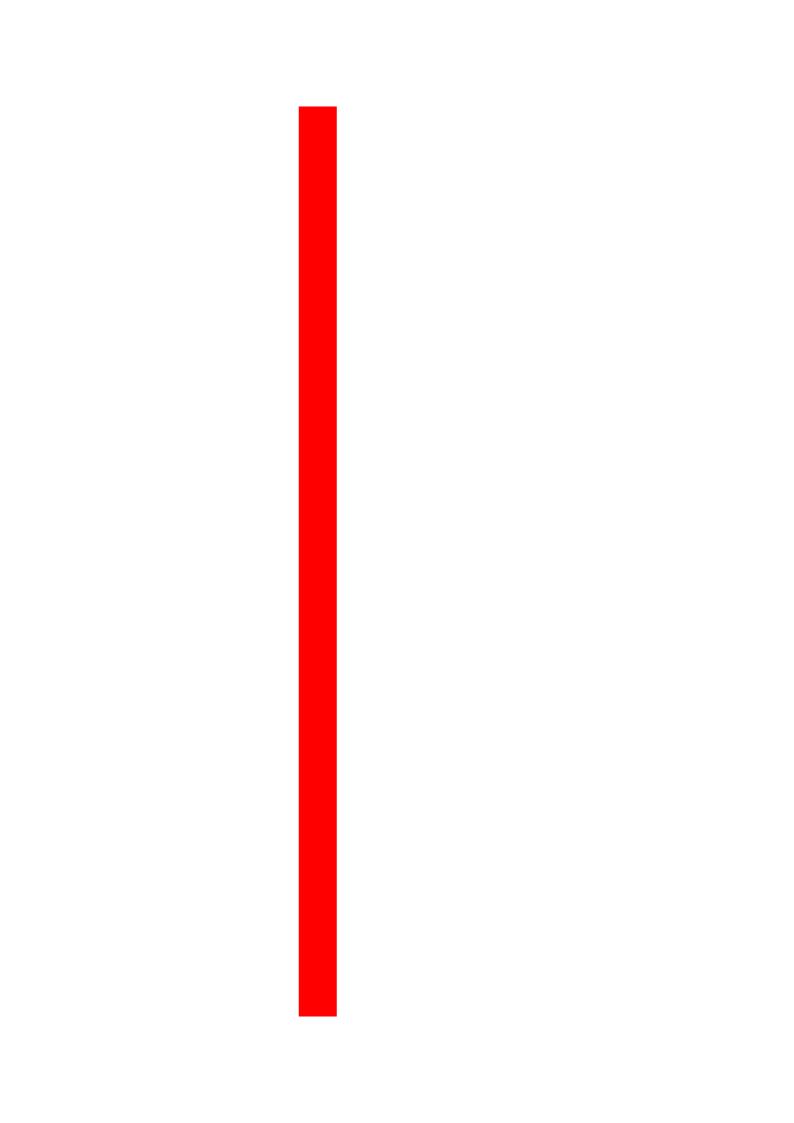
IFST

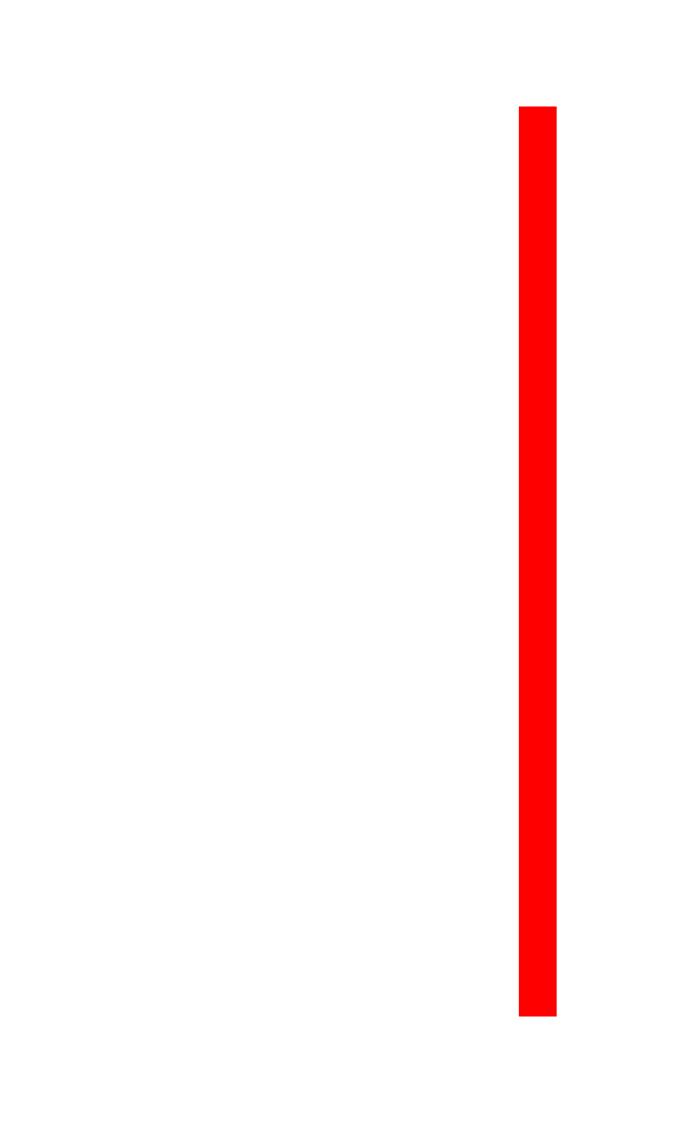
AGE

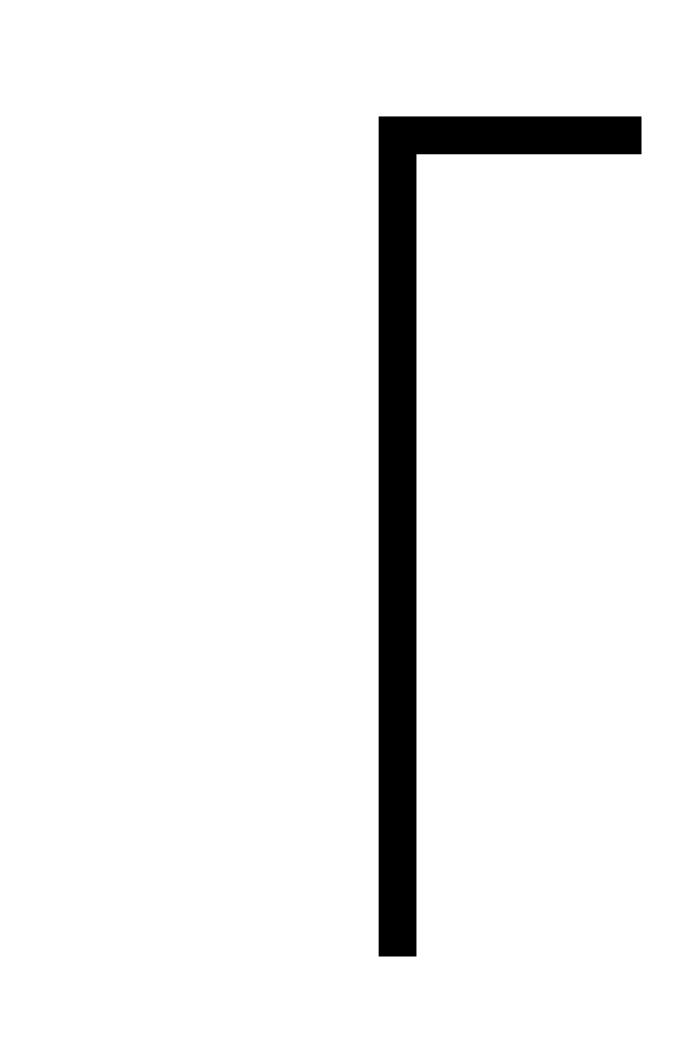




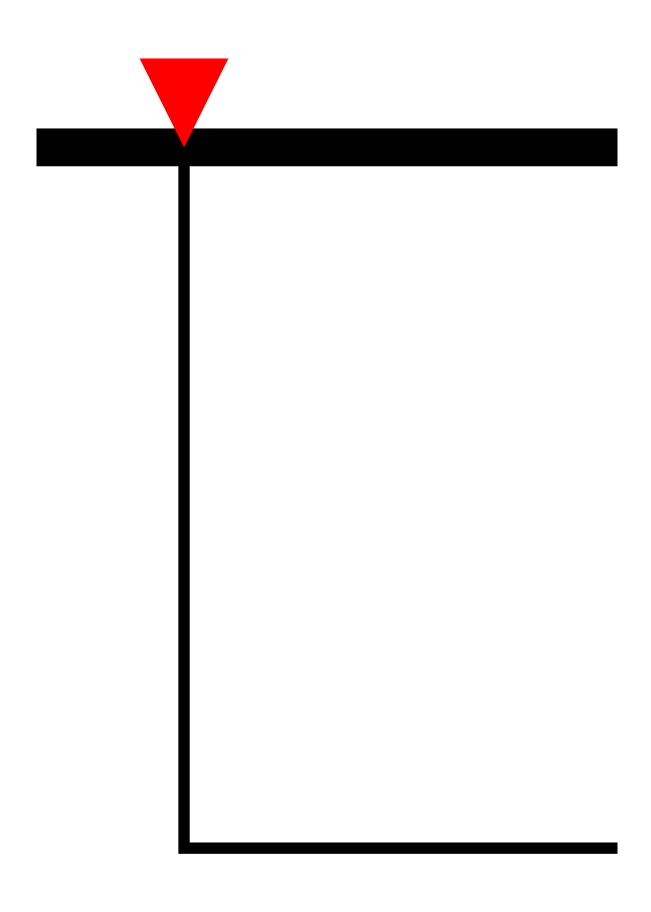




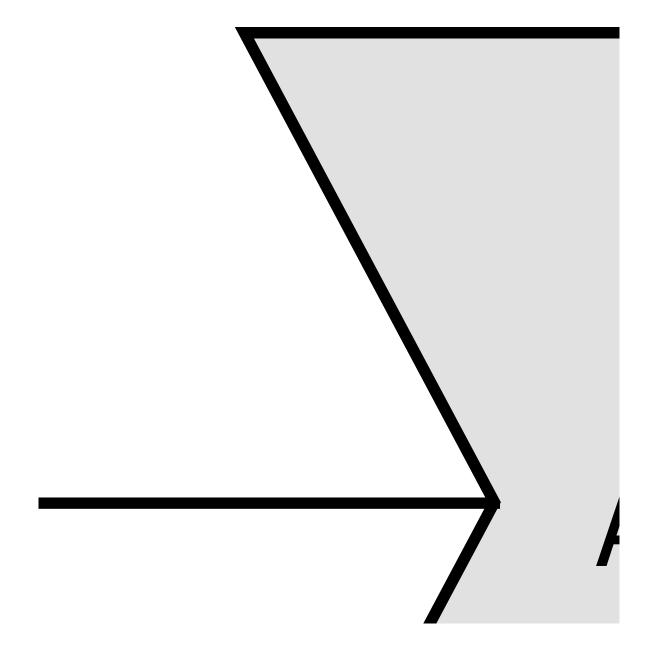


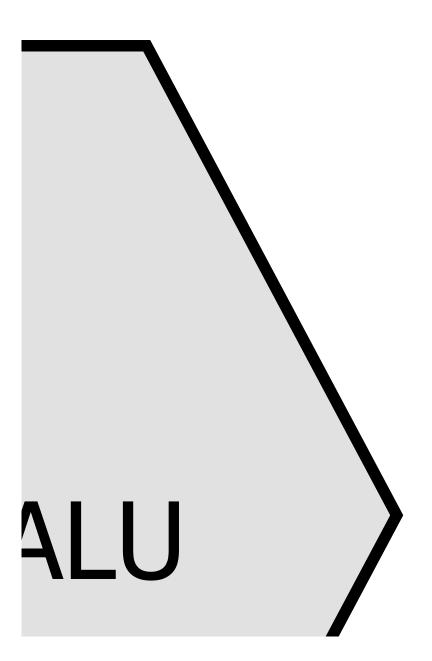


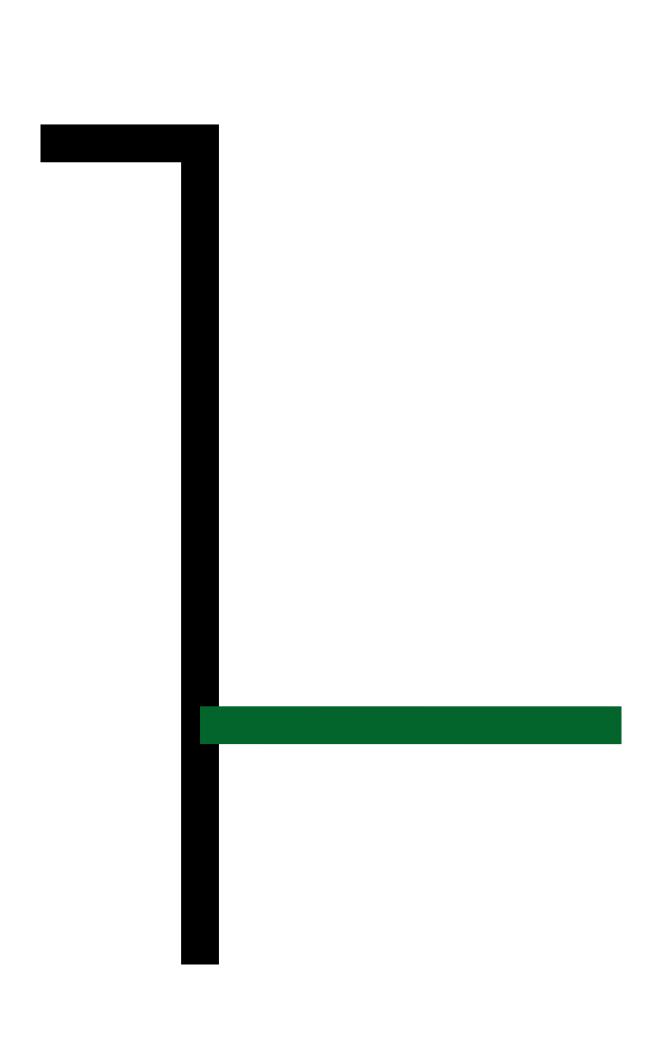






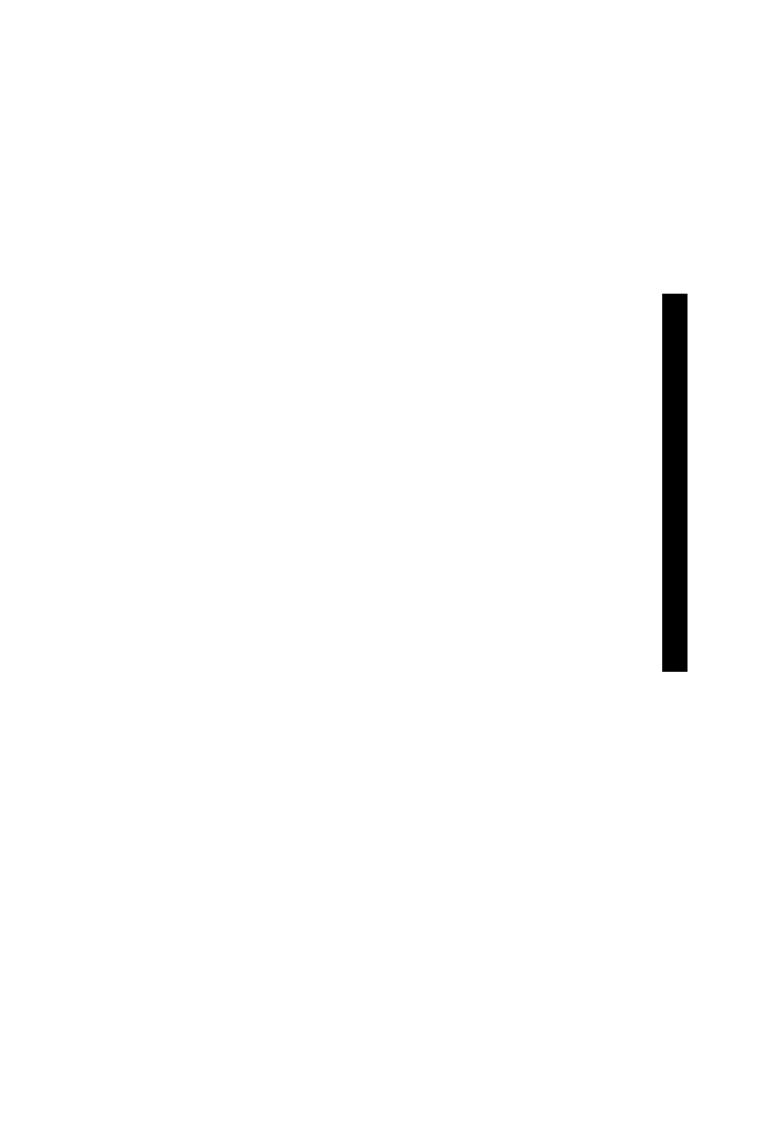




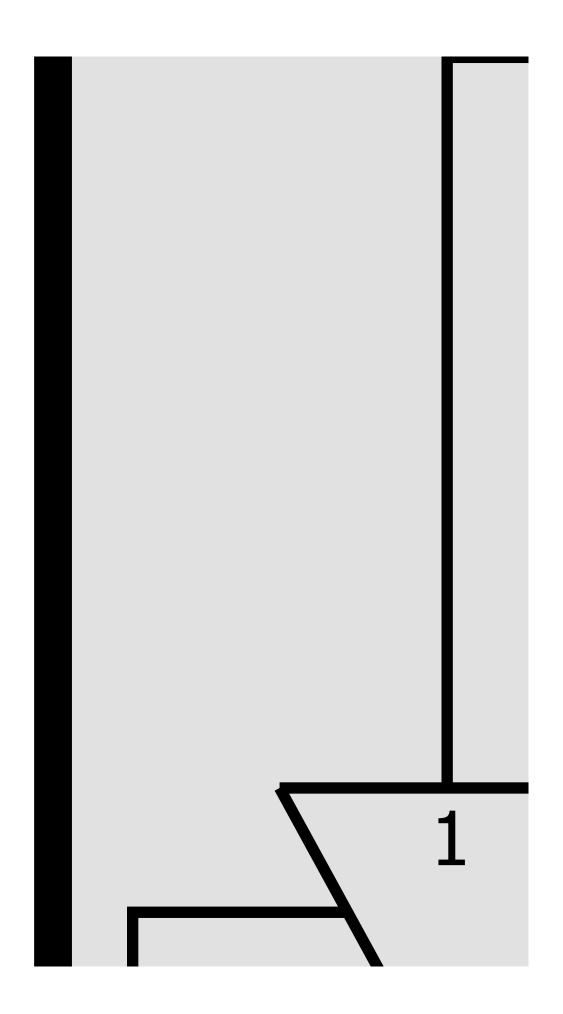


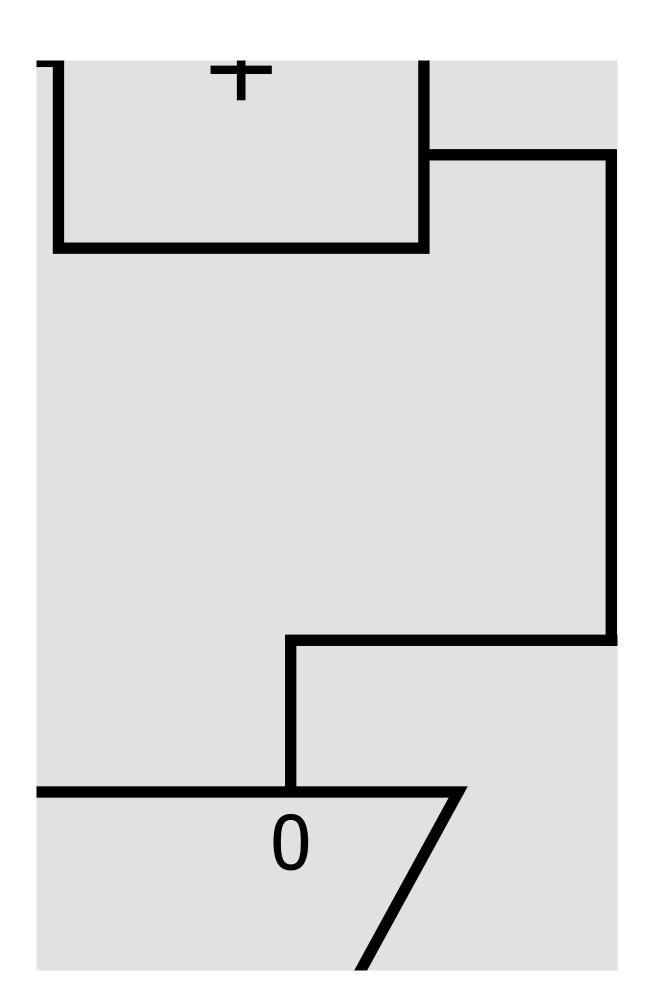
a

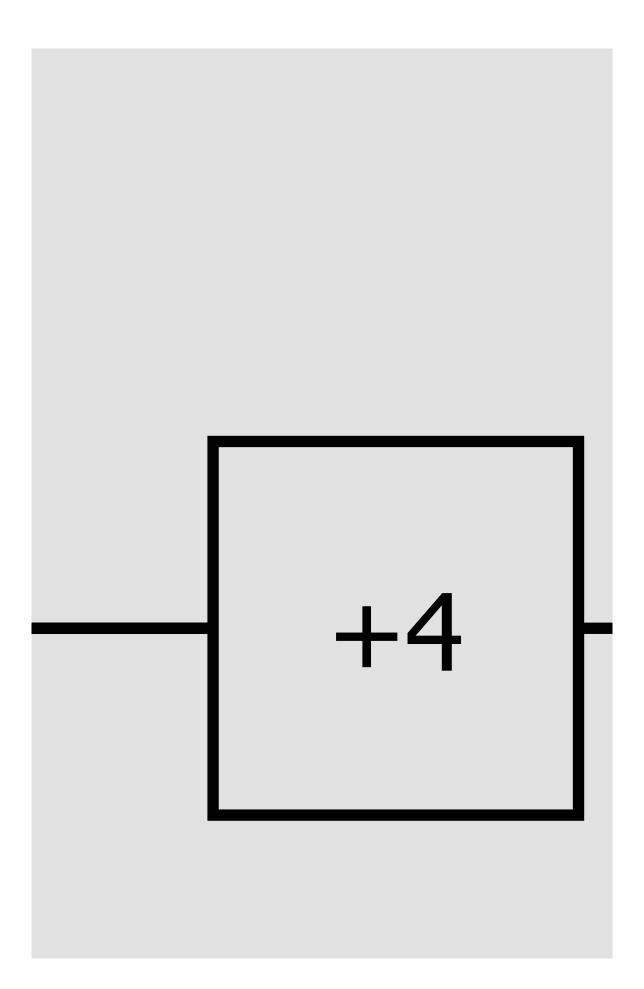
u_zero

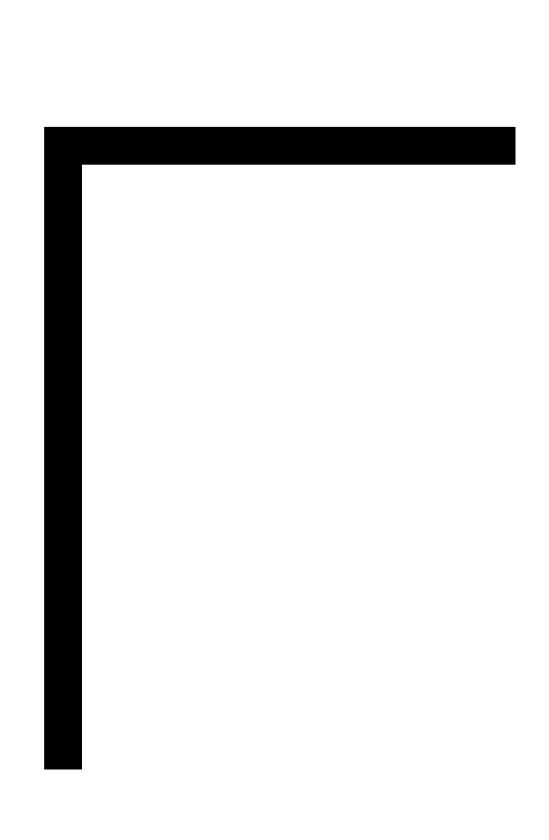


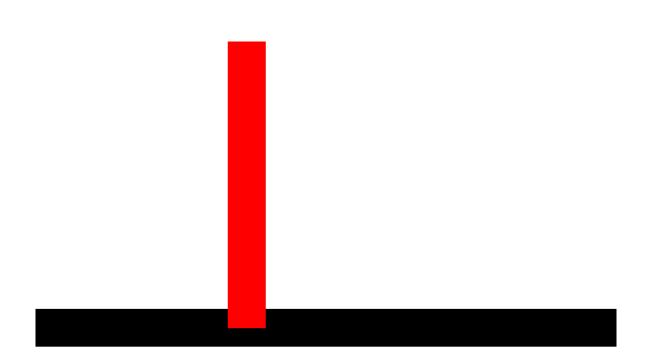


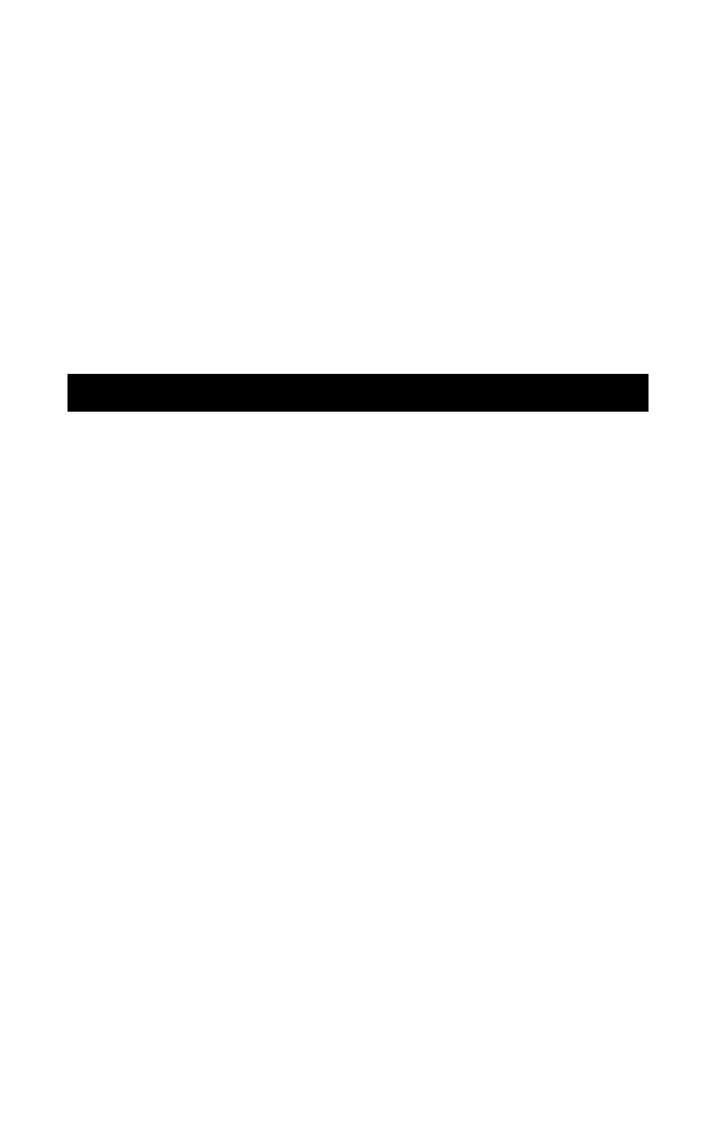


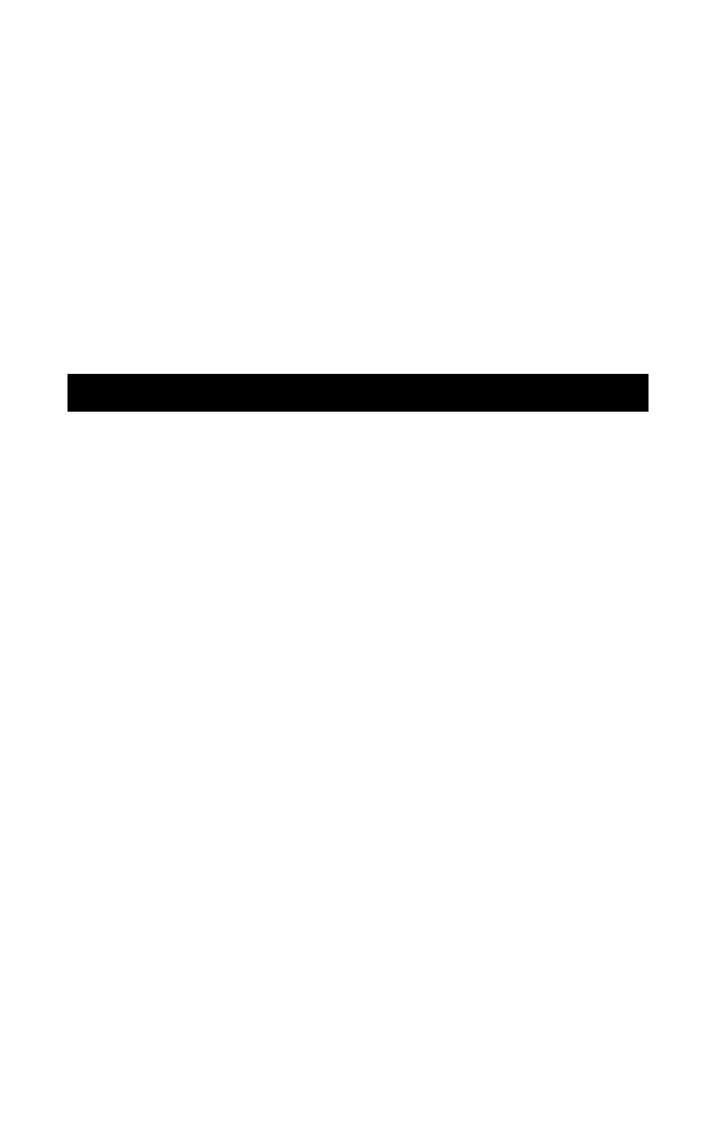




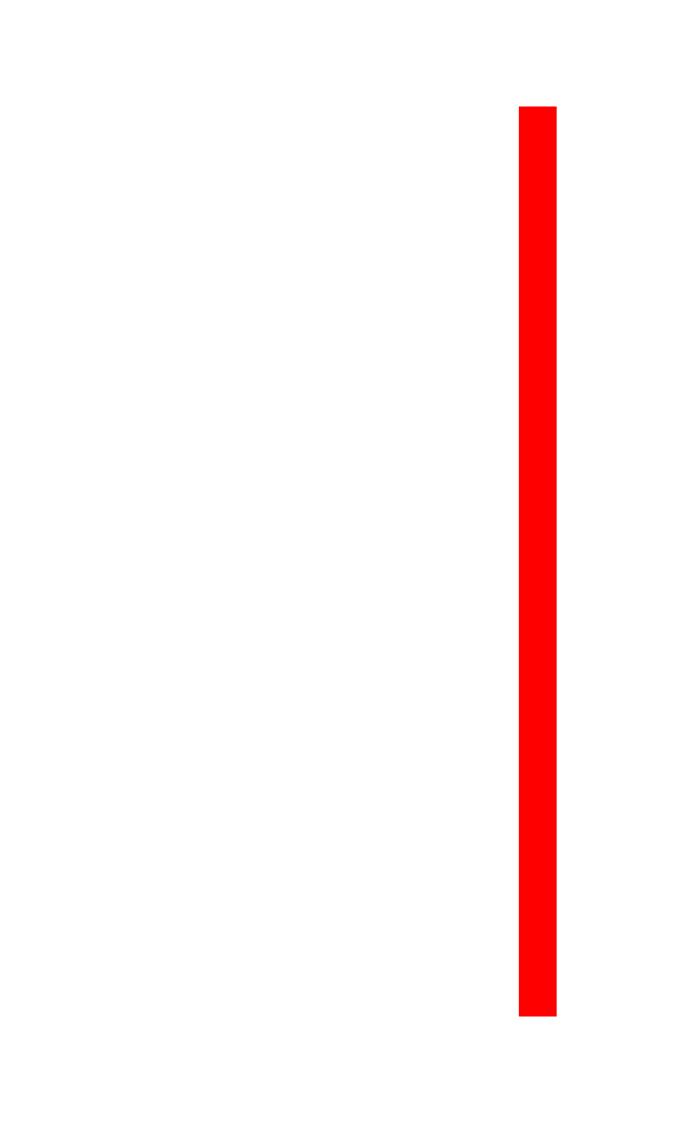




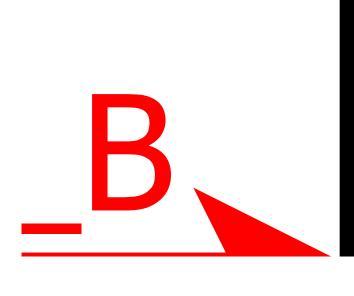


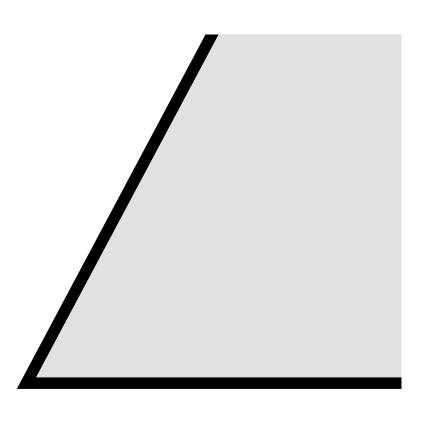


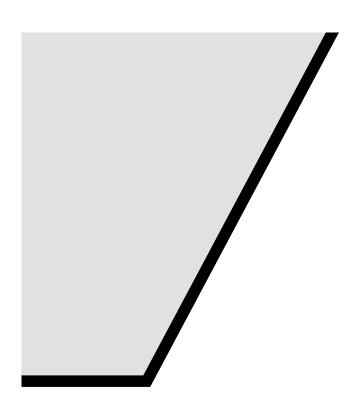


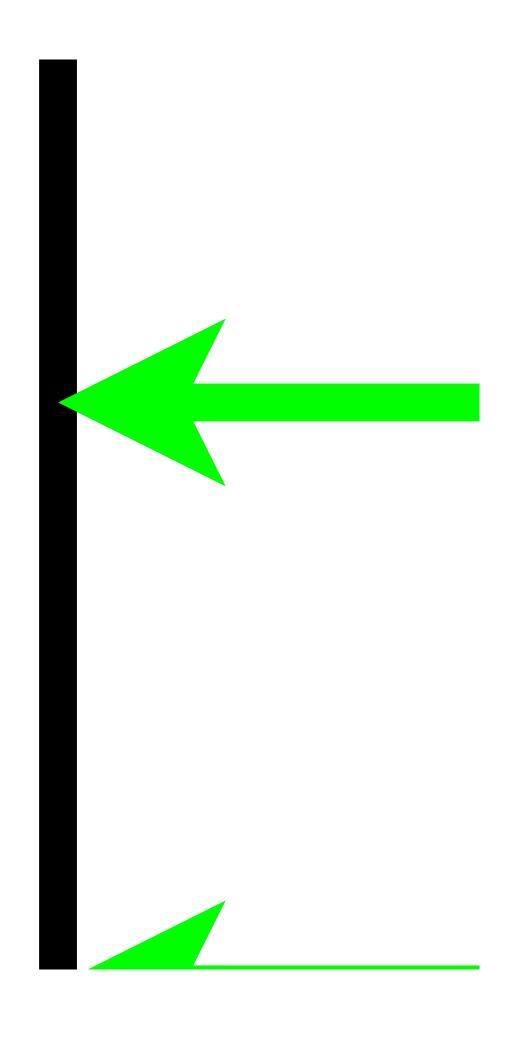


RF



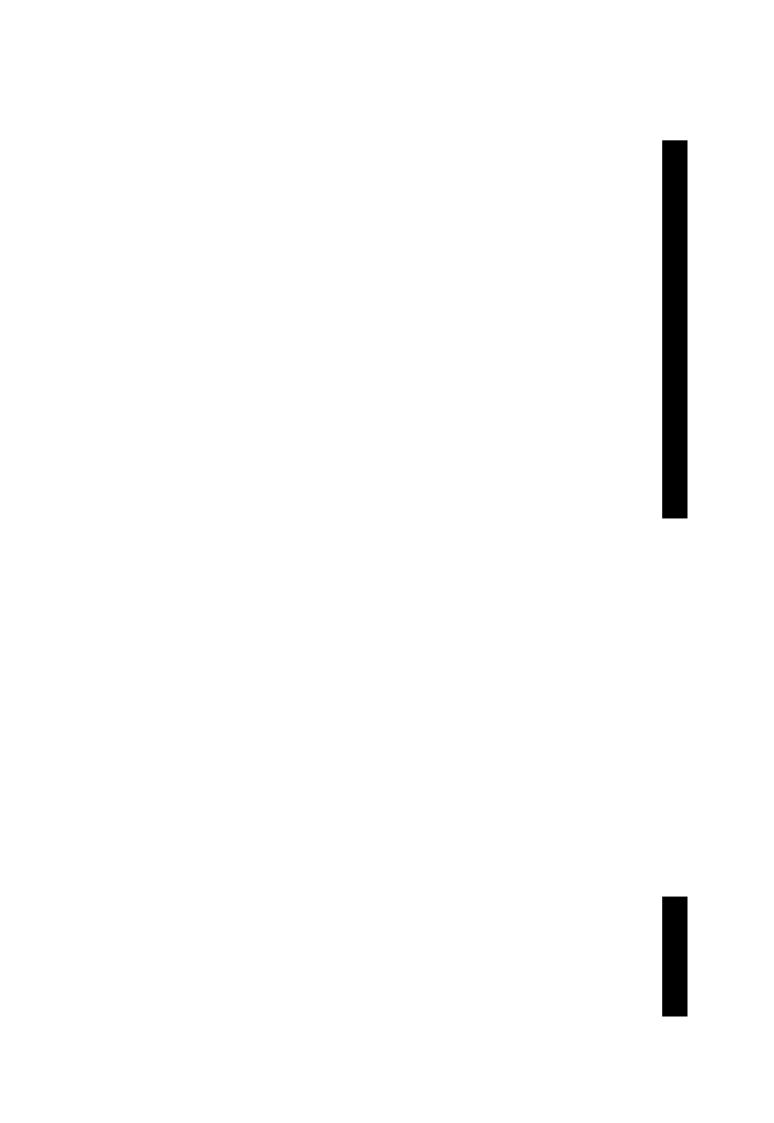




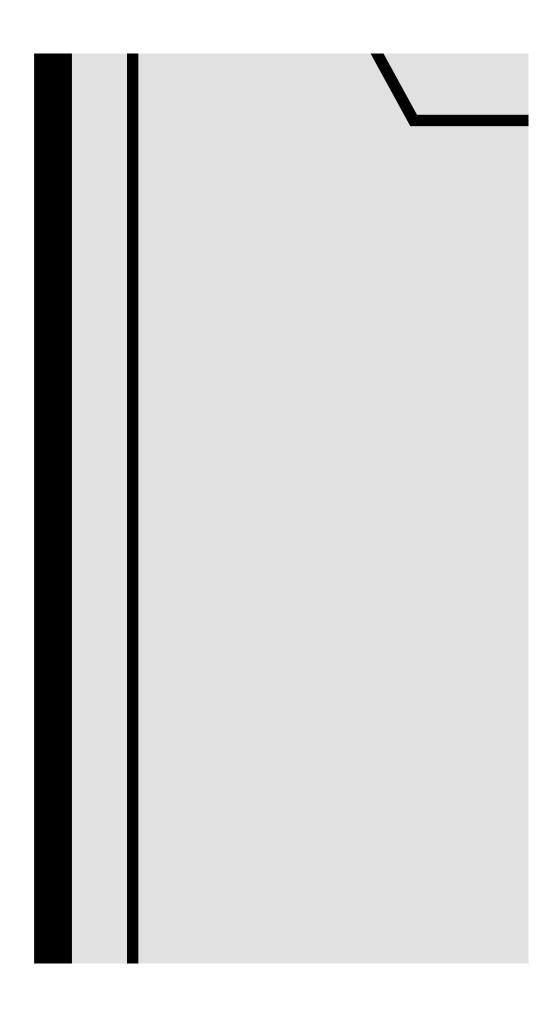


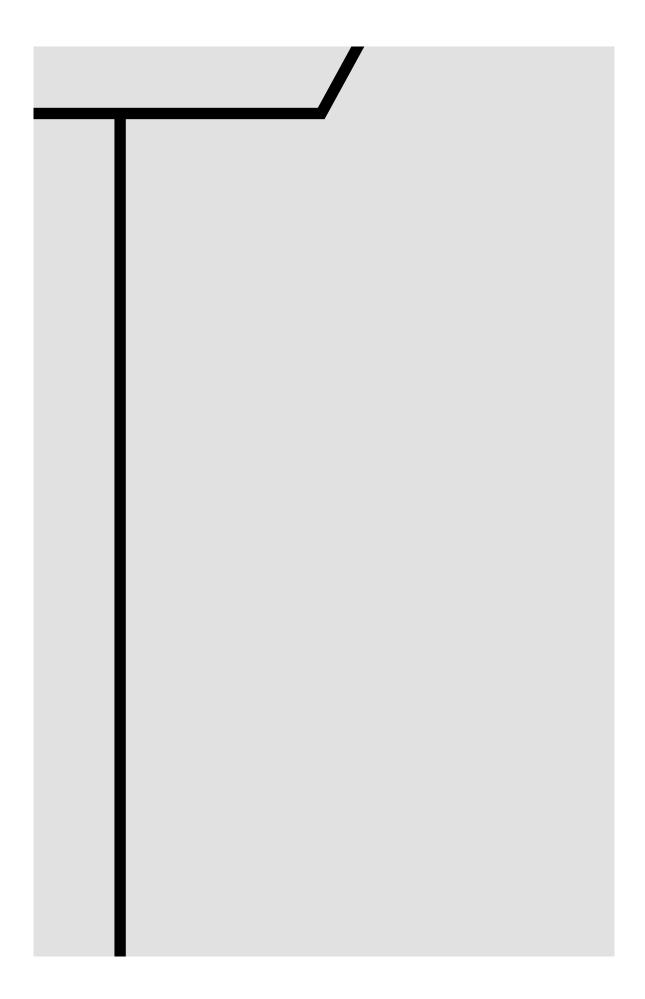
u_bin_s

III fiinc









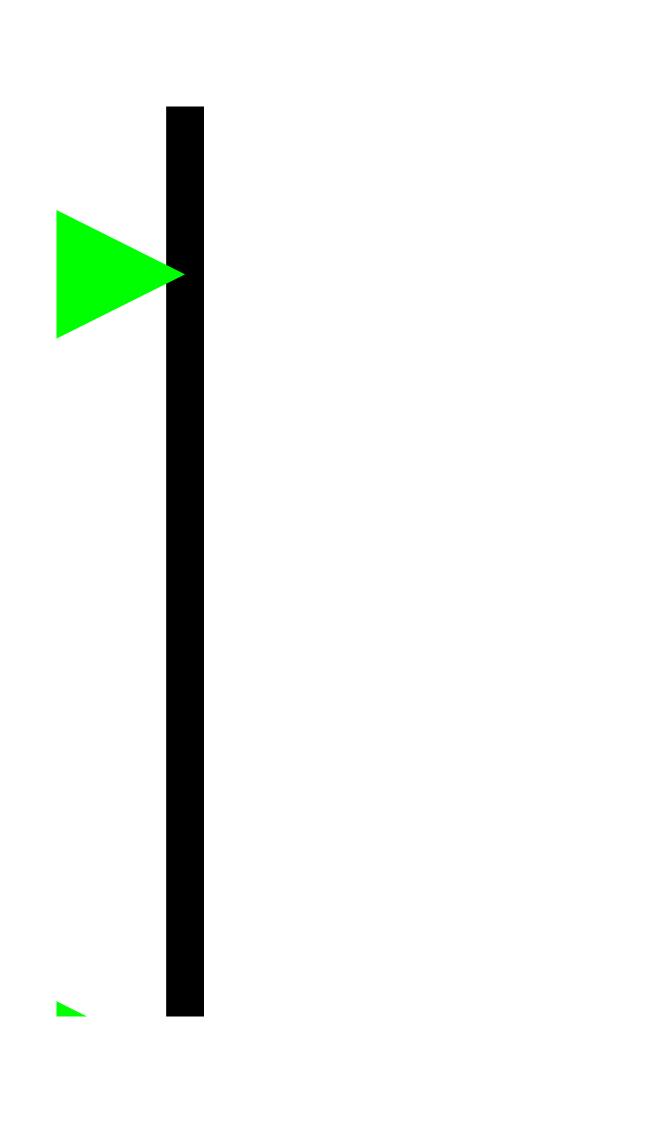
rf_

_wren



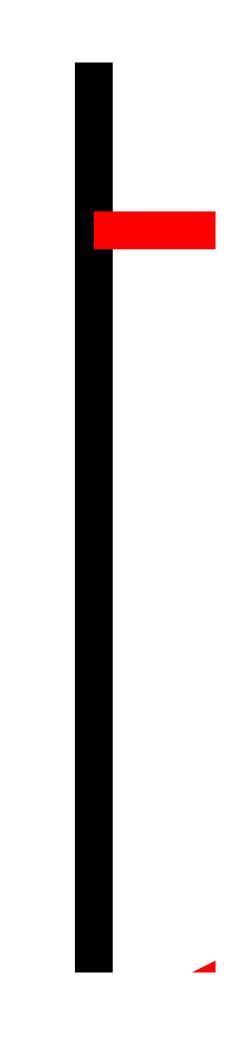








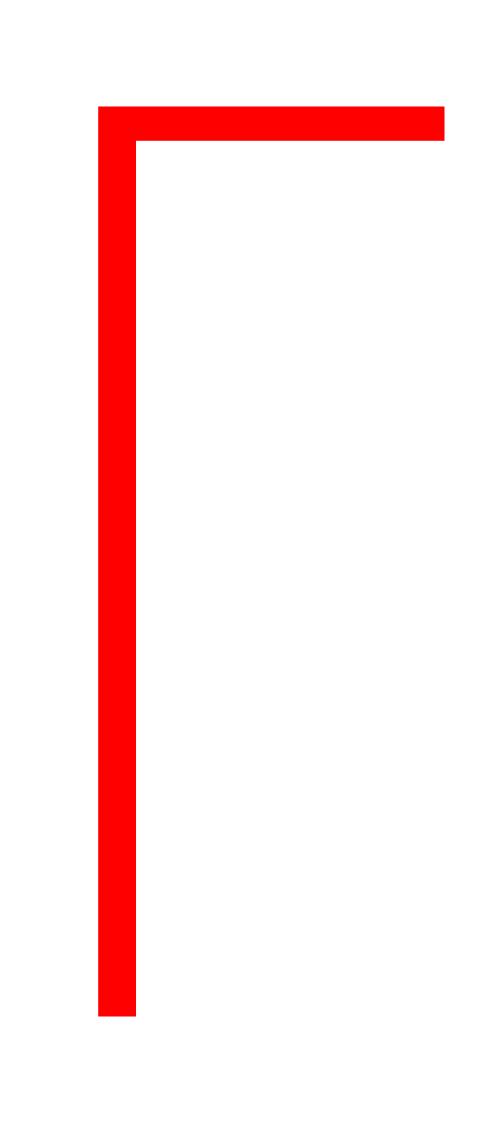


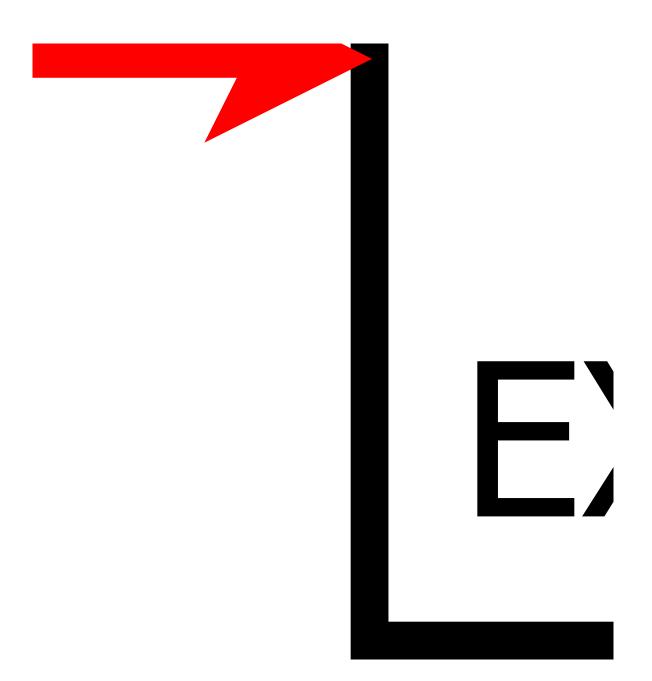


RF_A

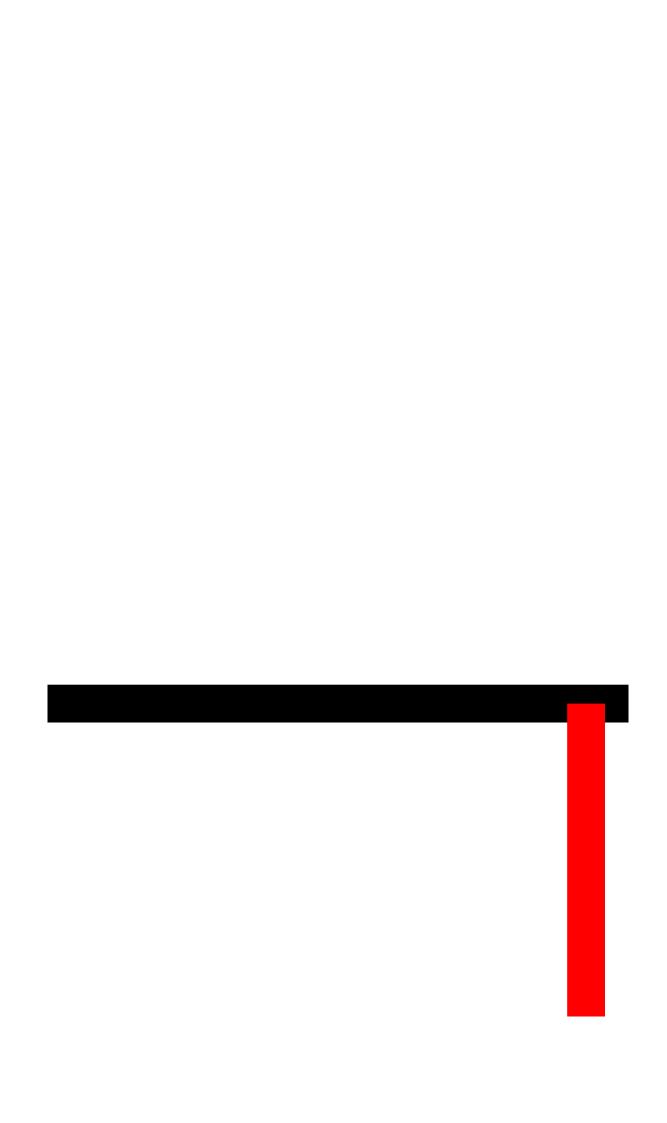
alu o

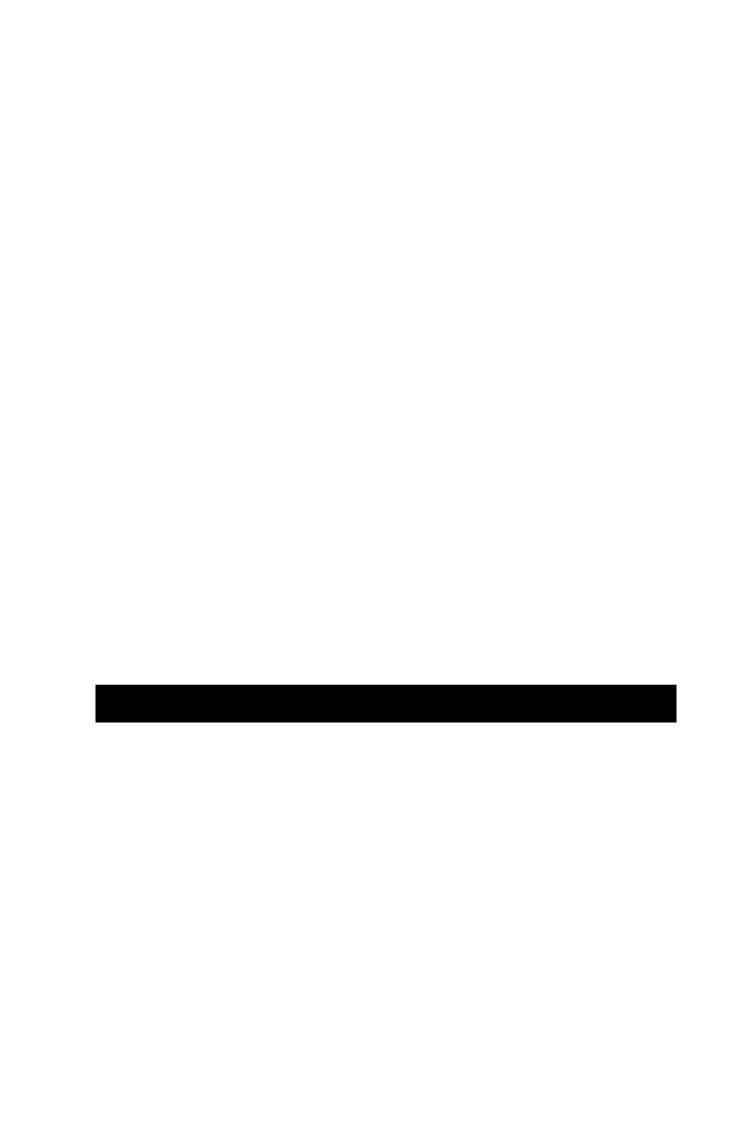
ut

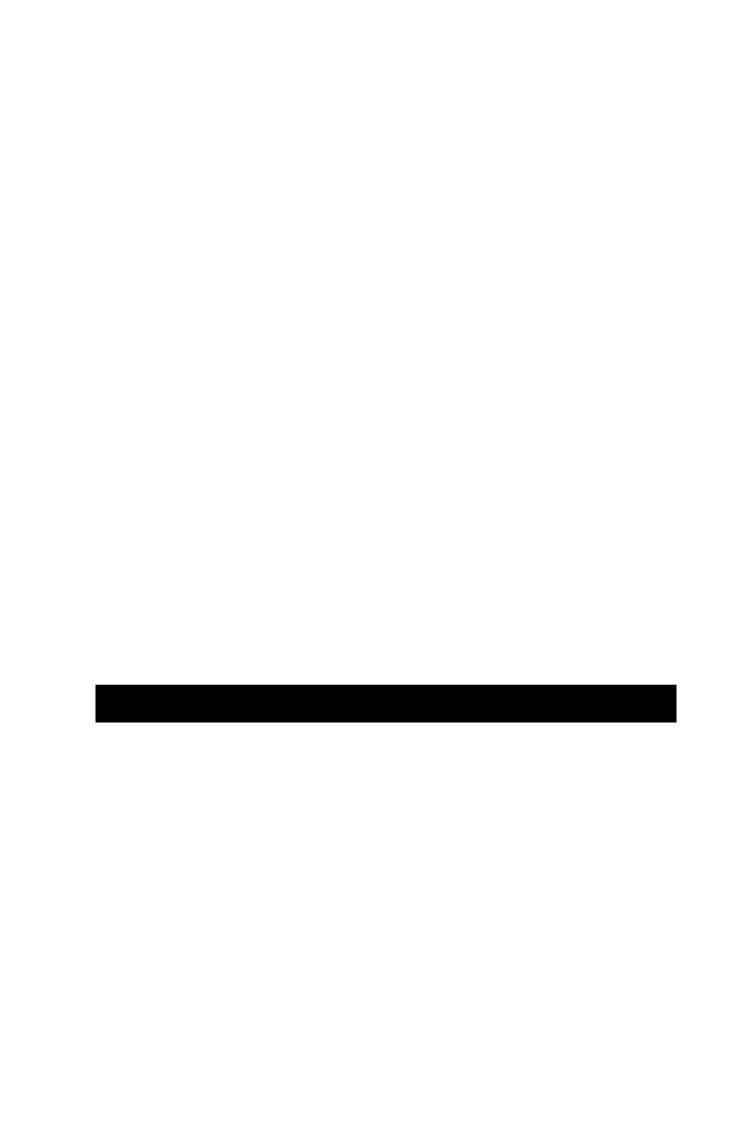


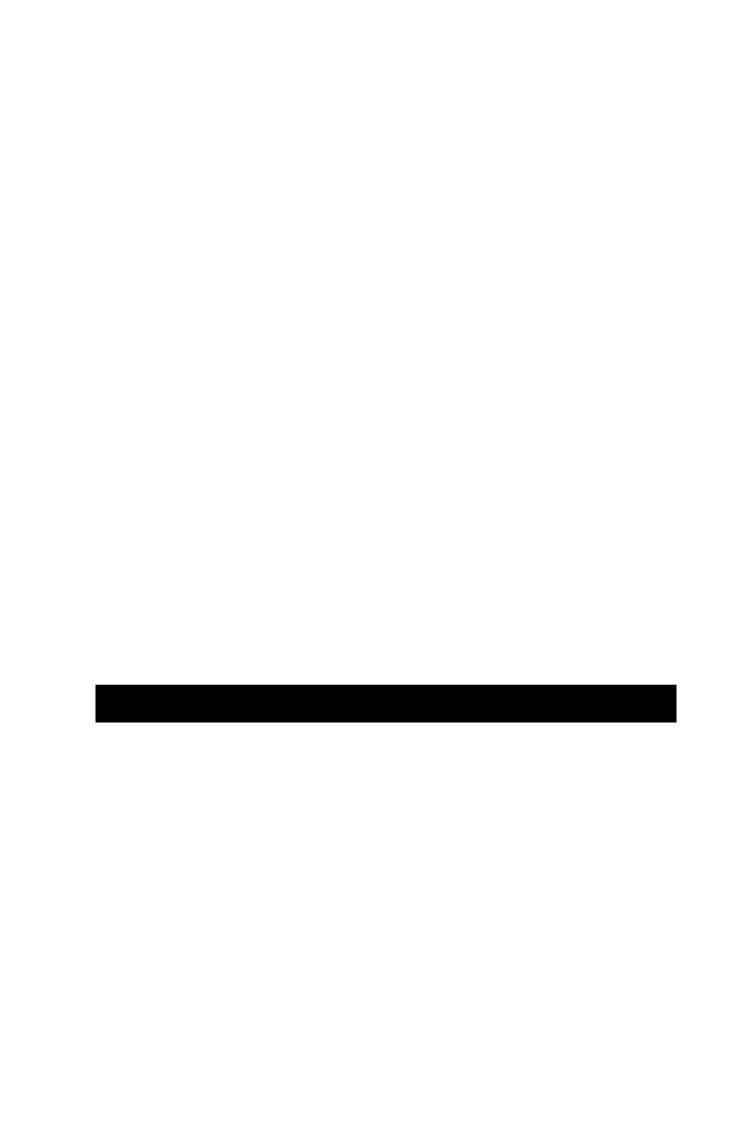


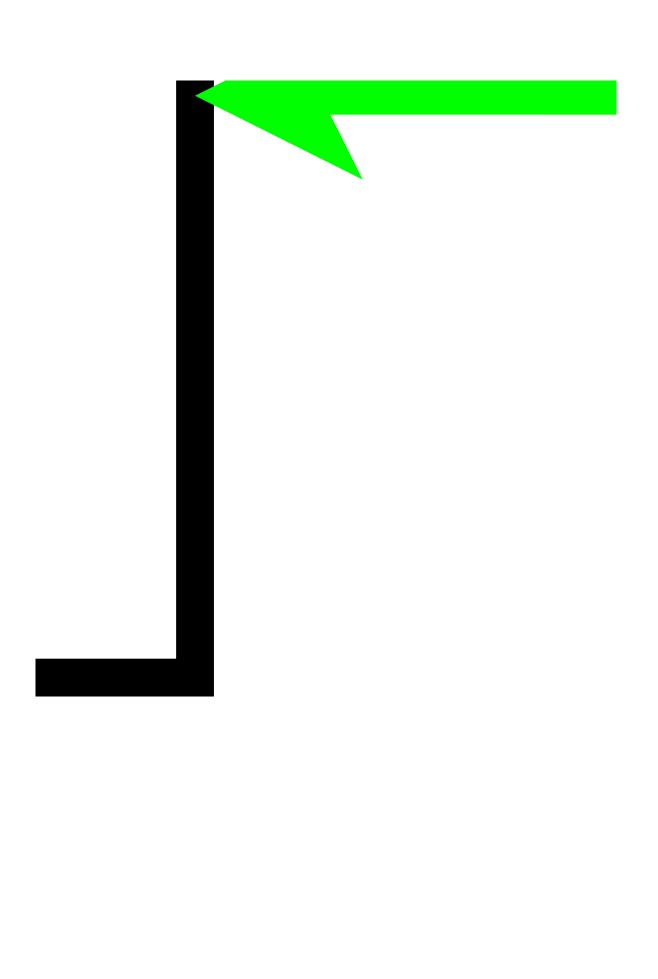
XSTAG



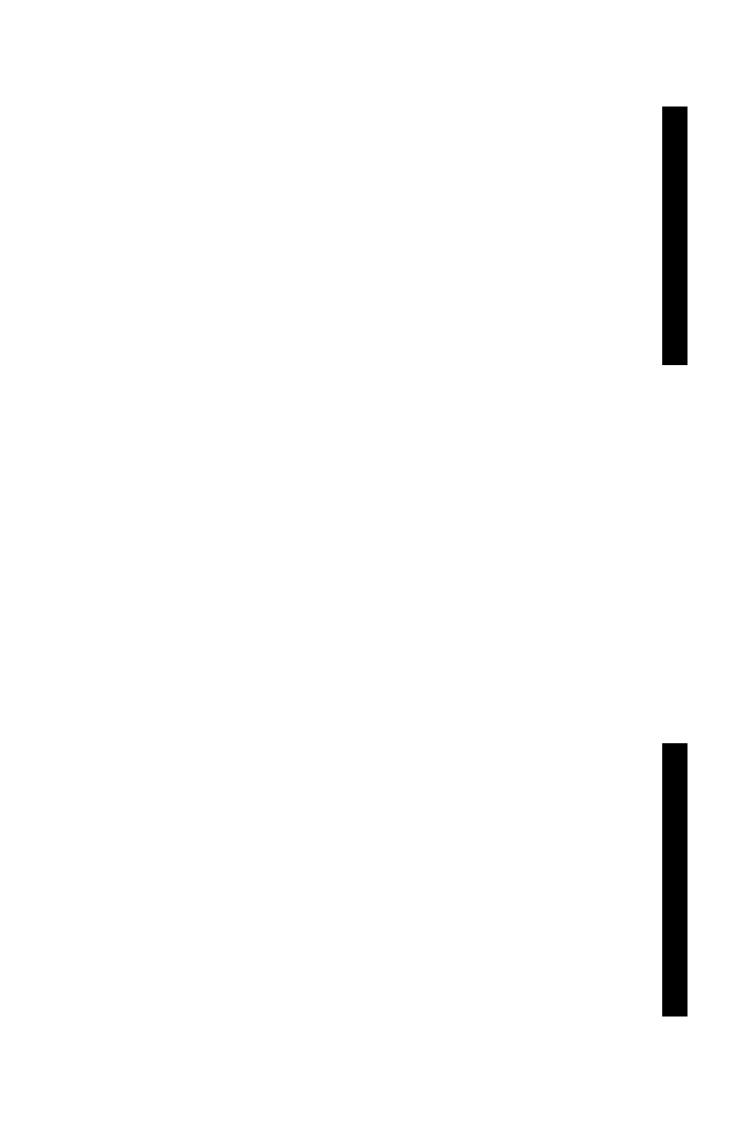






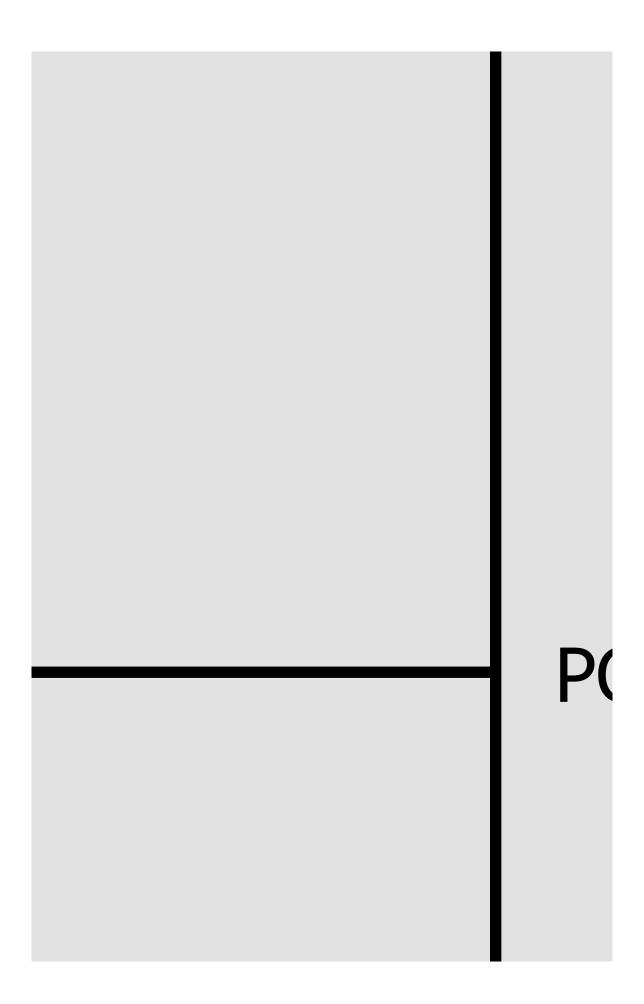


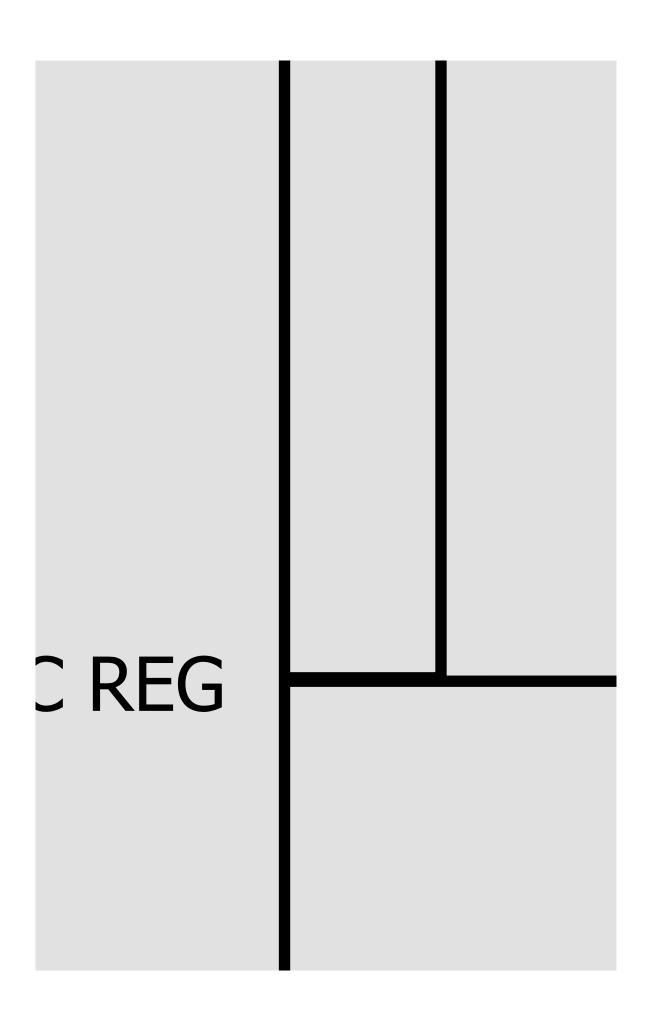




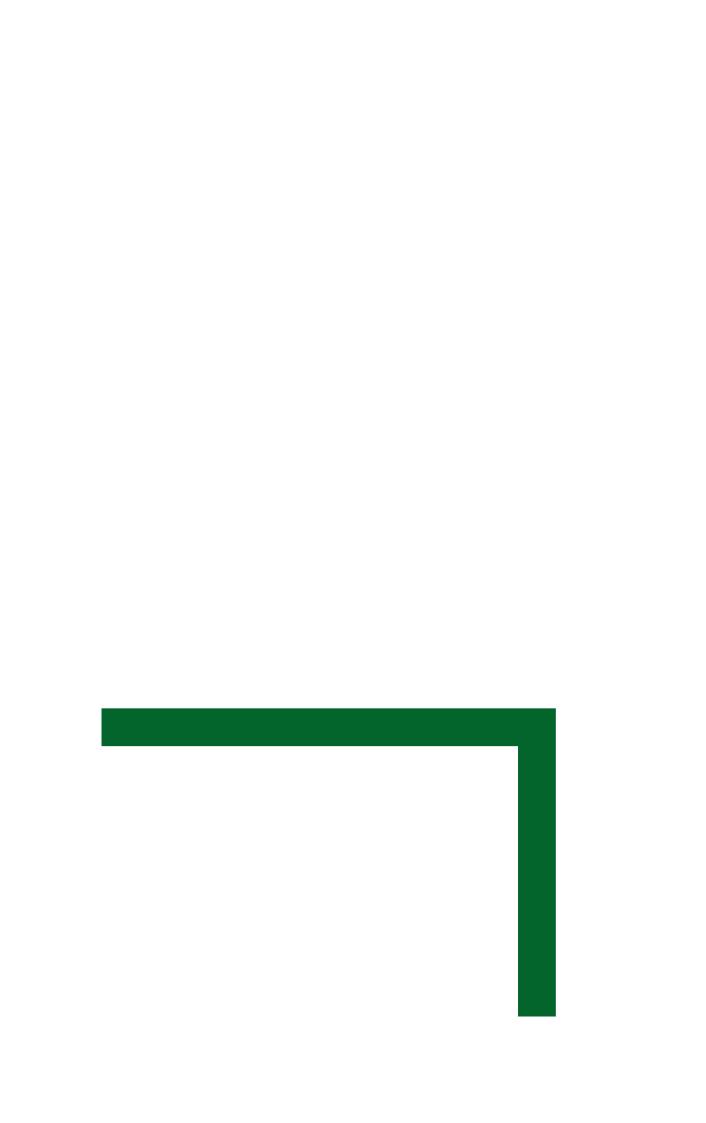








PC

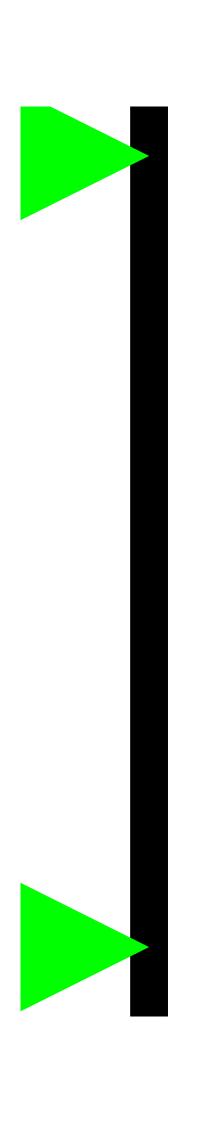


imr

b_se_

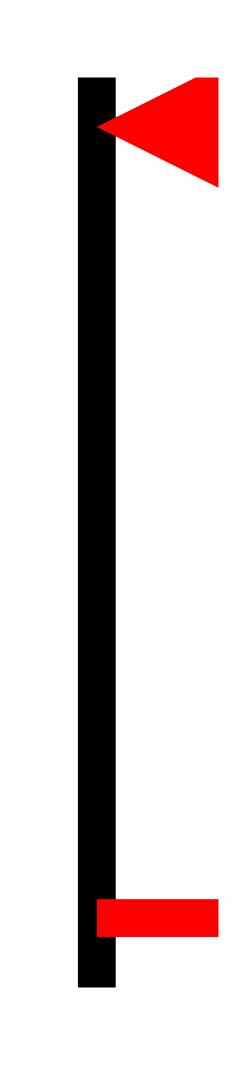
next









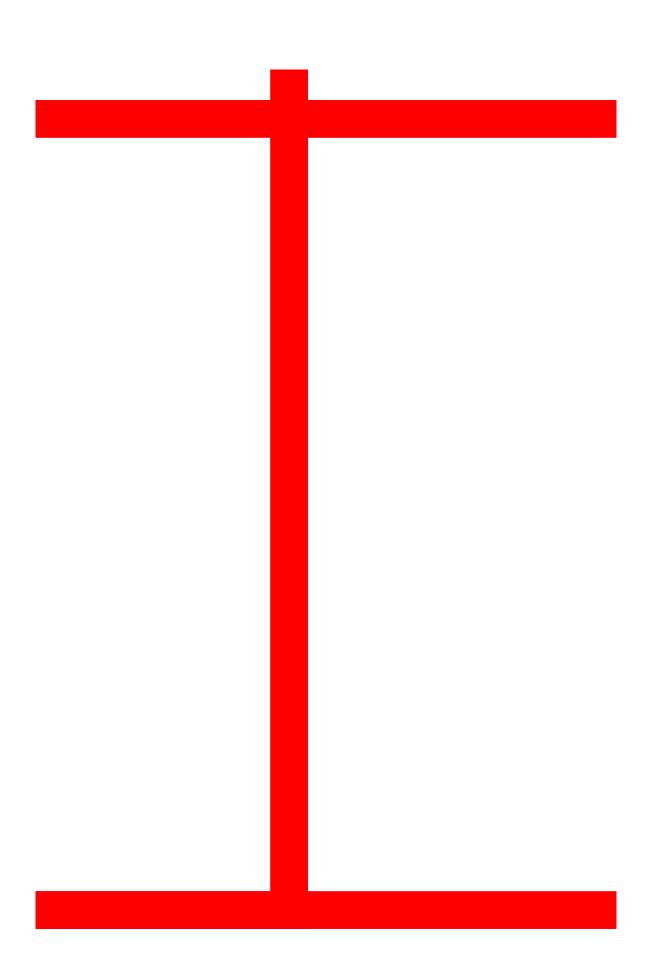


RF_B



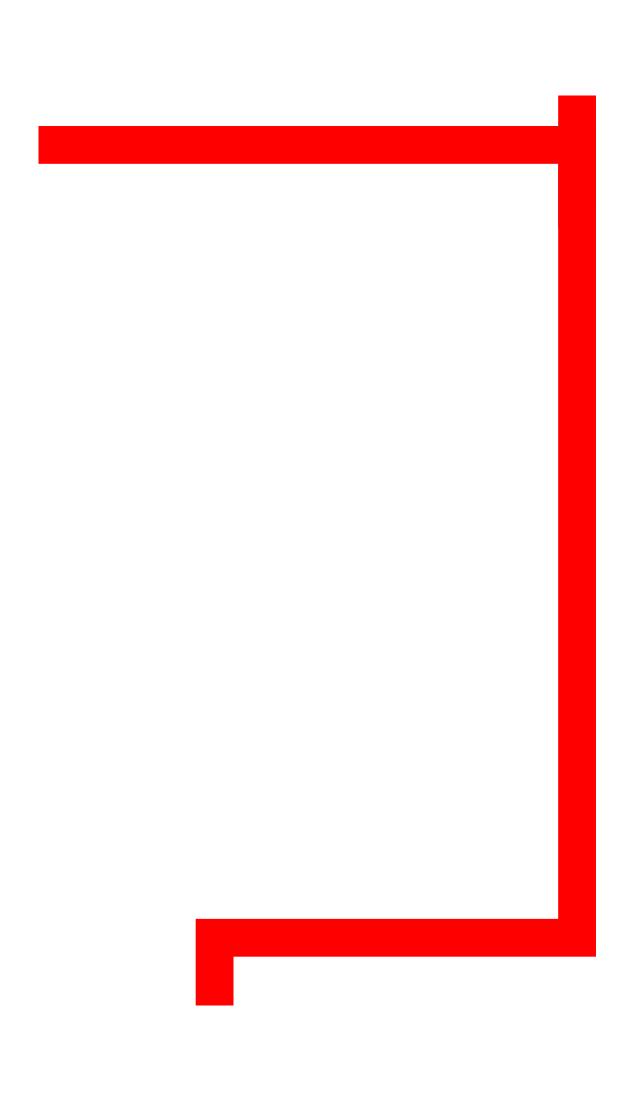


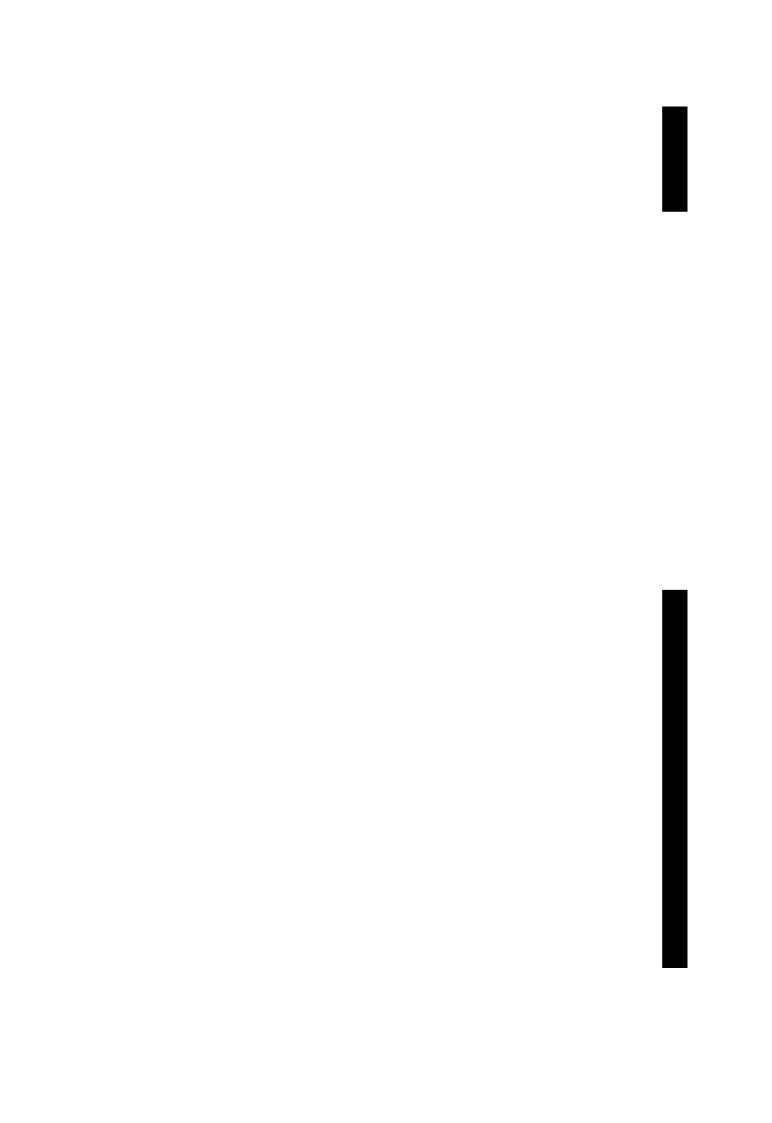


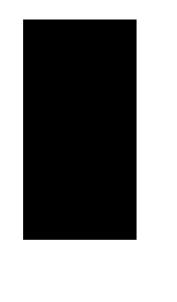


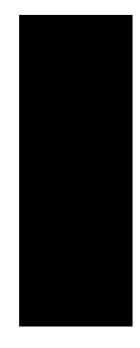
MEM_Datai

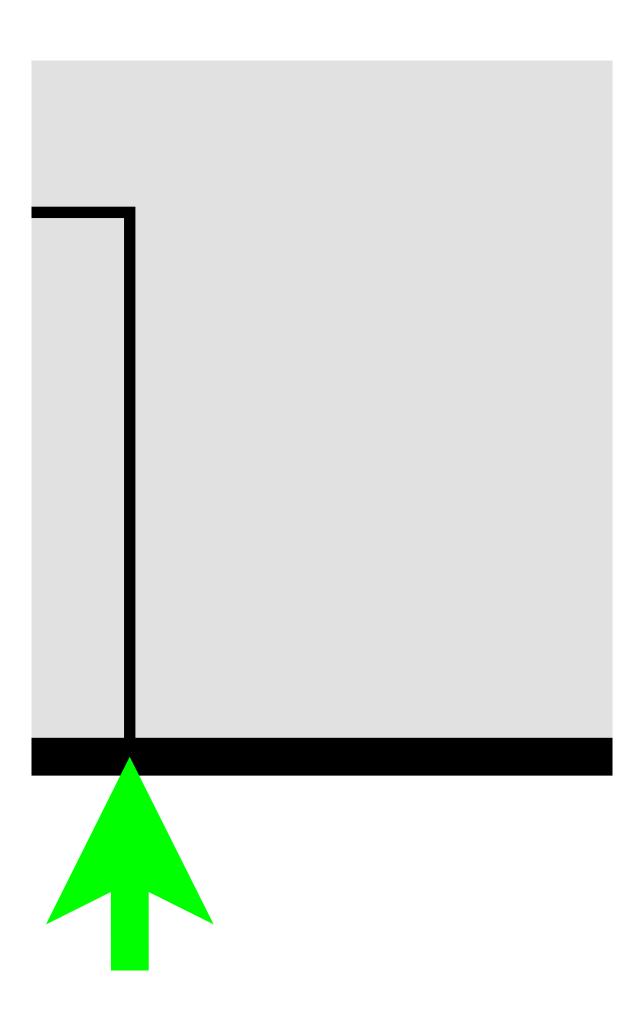
n (RF_B)

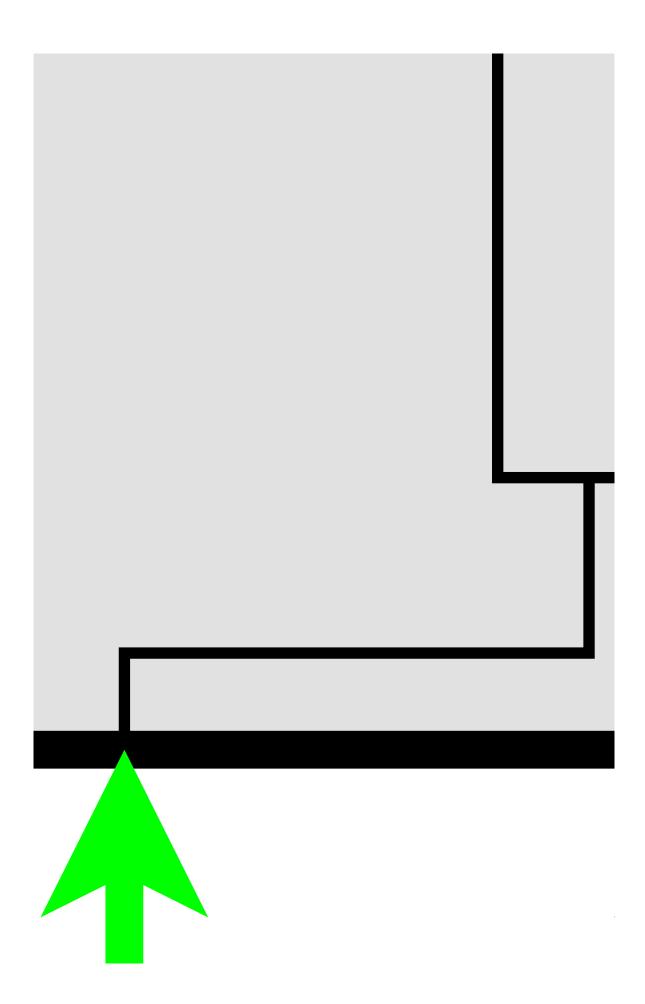


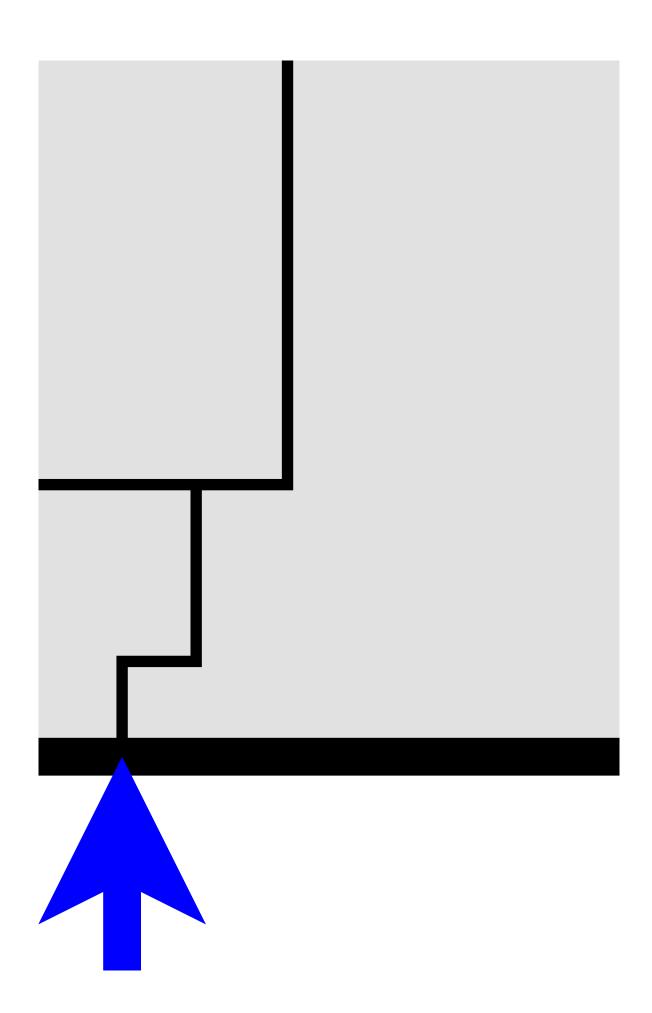


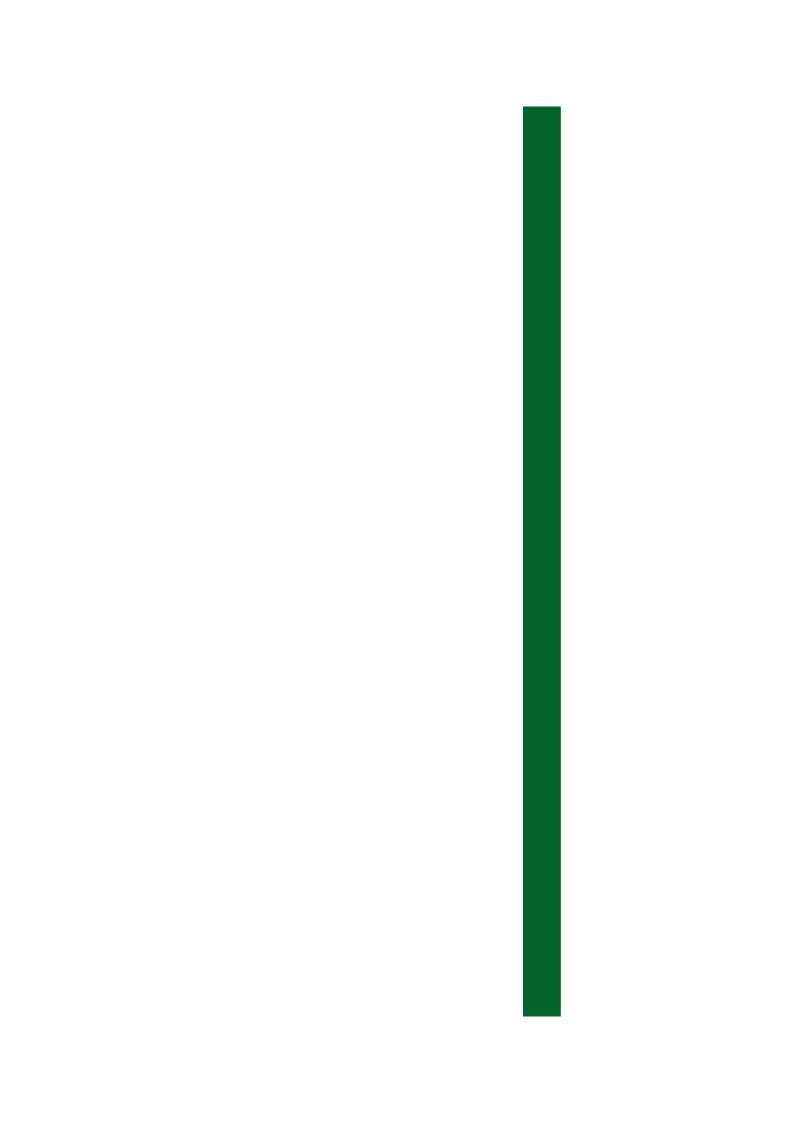








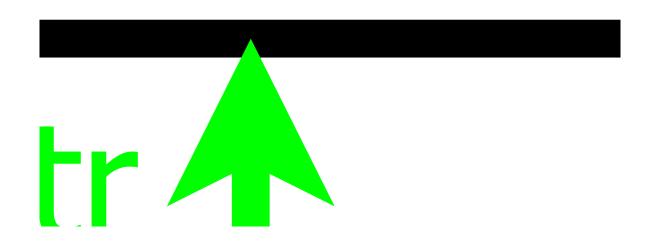




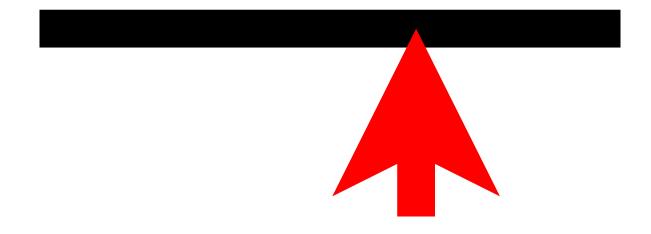




ins'

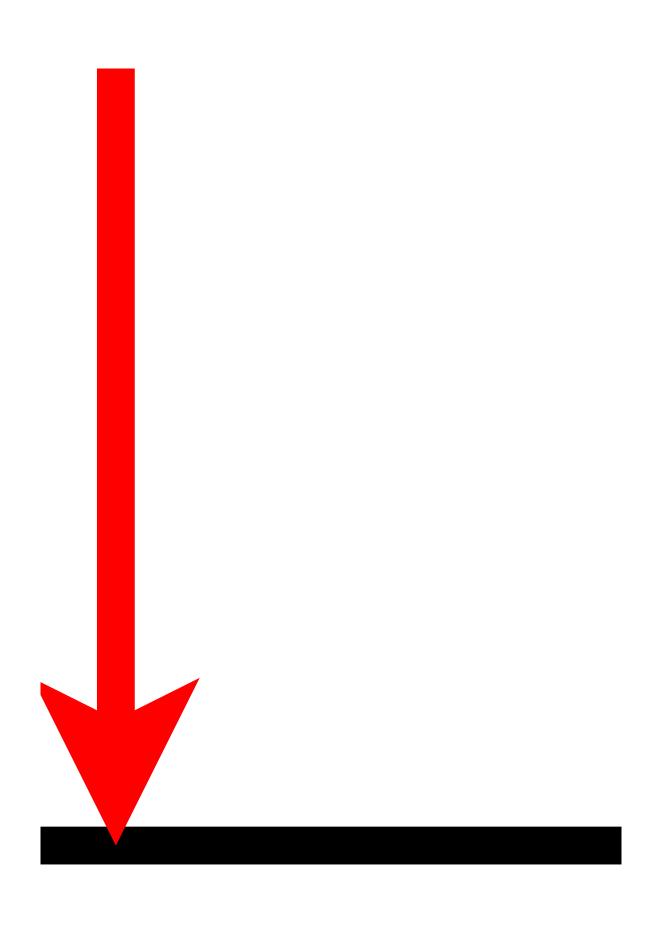






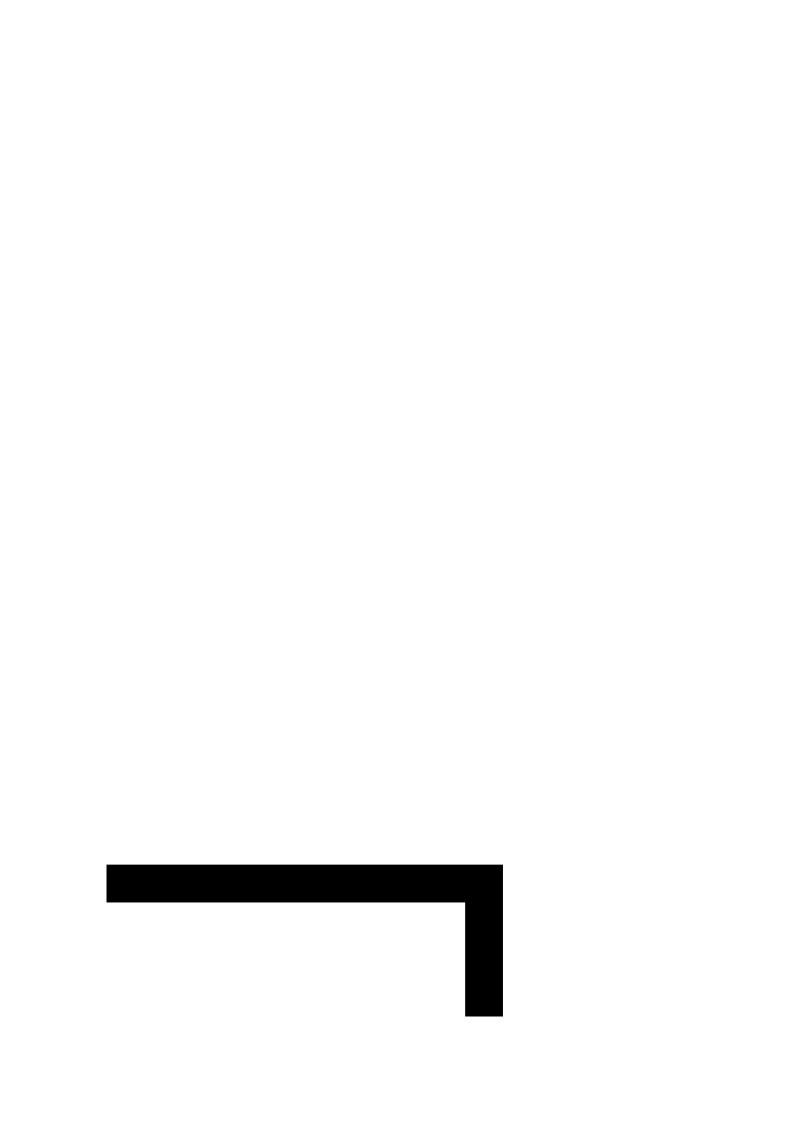






alu_me

:m_addr



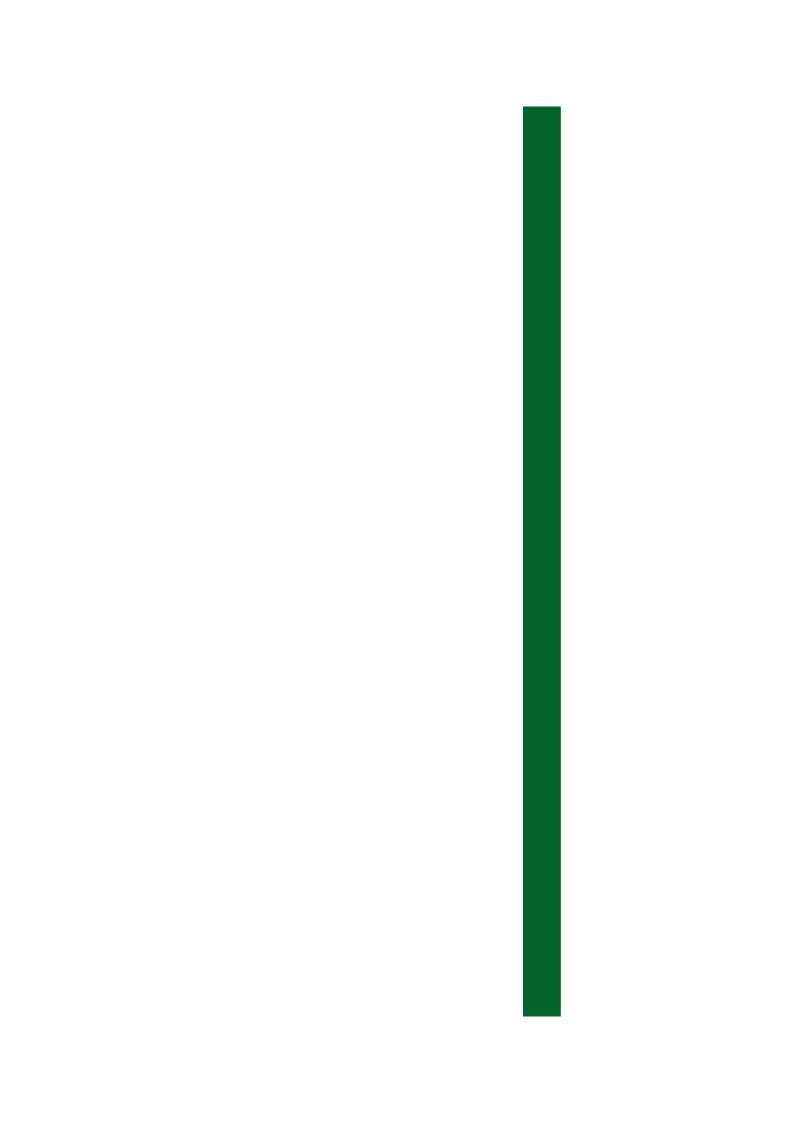




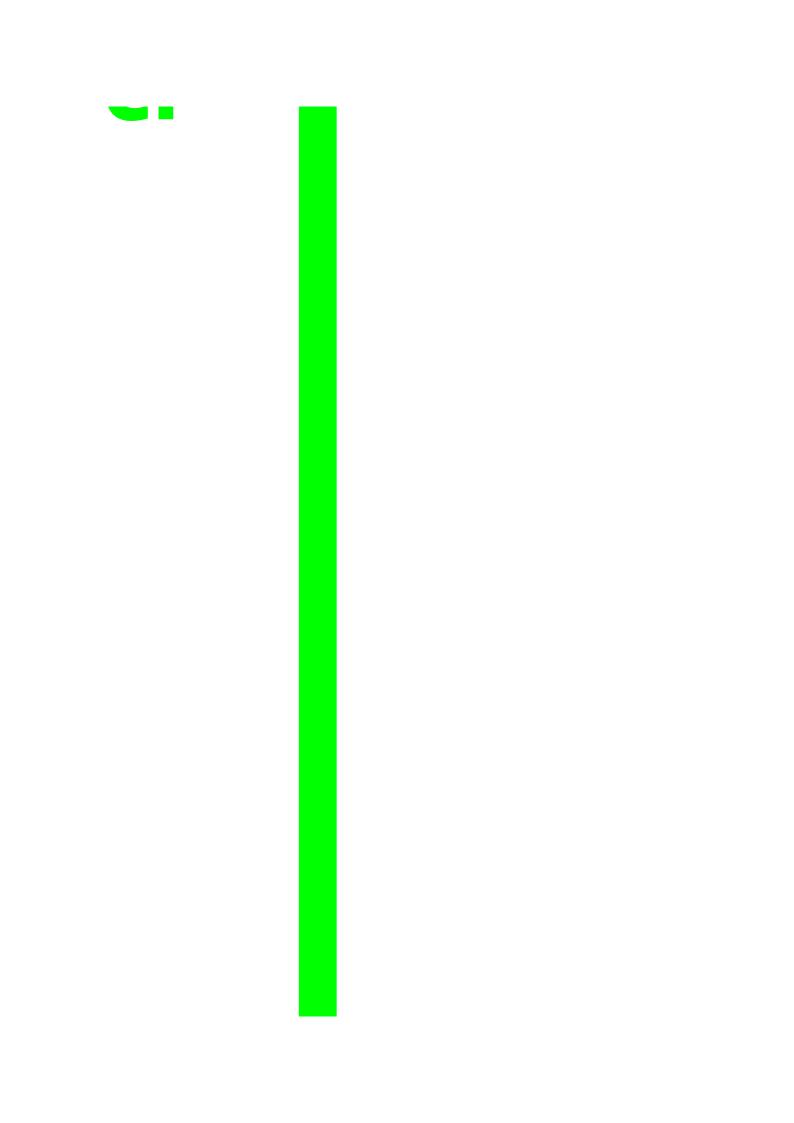
C Se DC

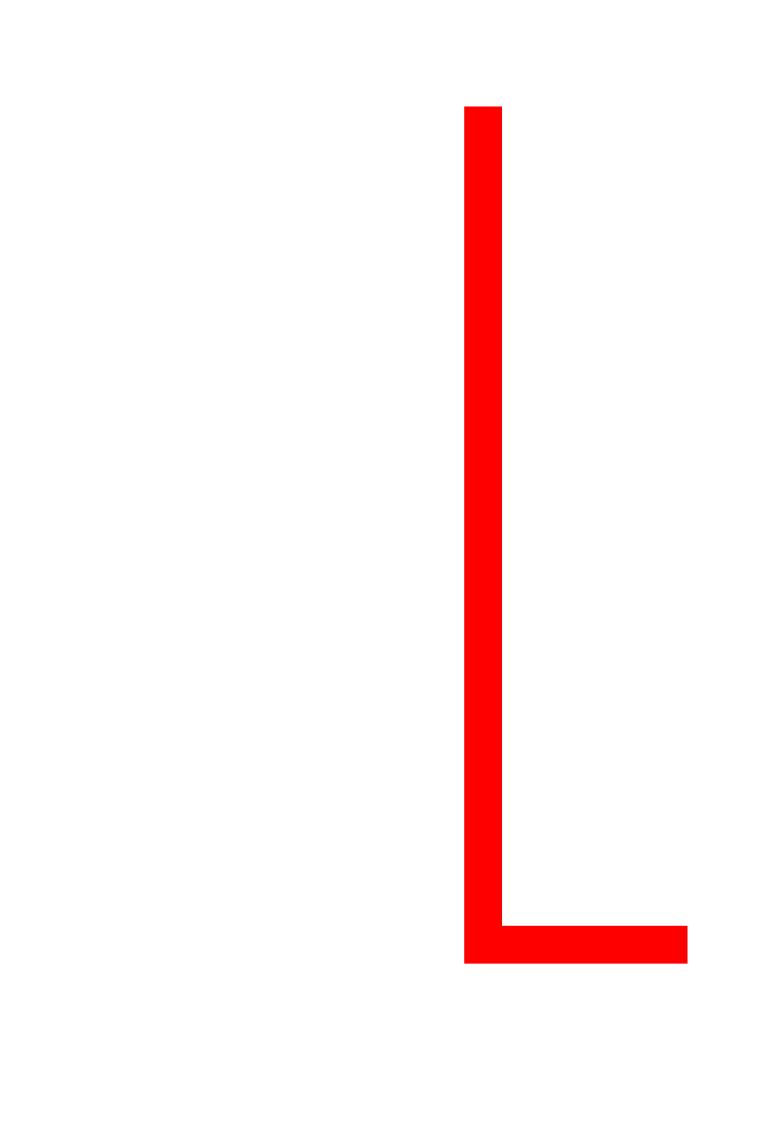
lden __cloc

k/reset















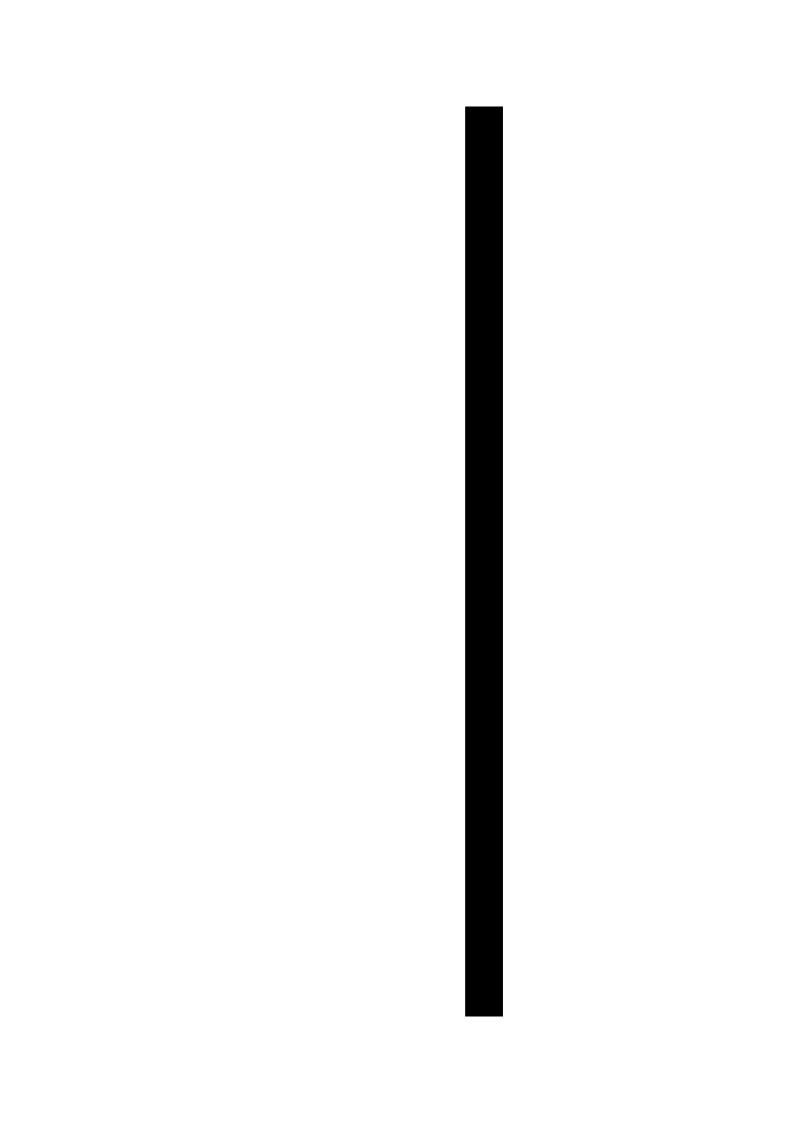
m_out

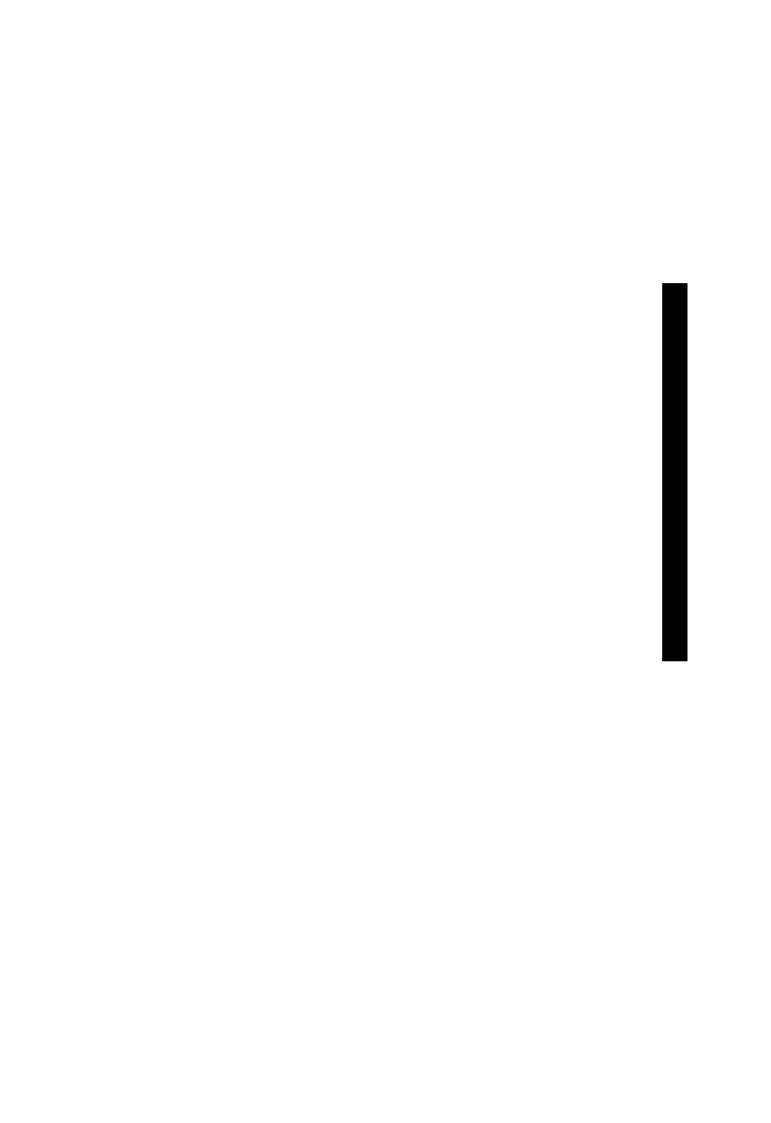




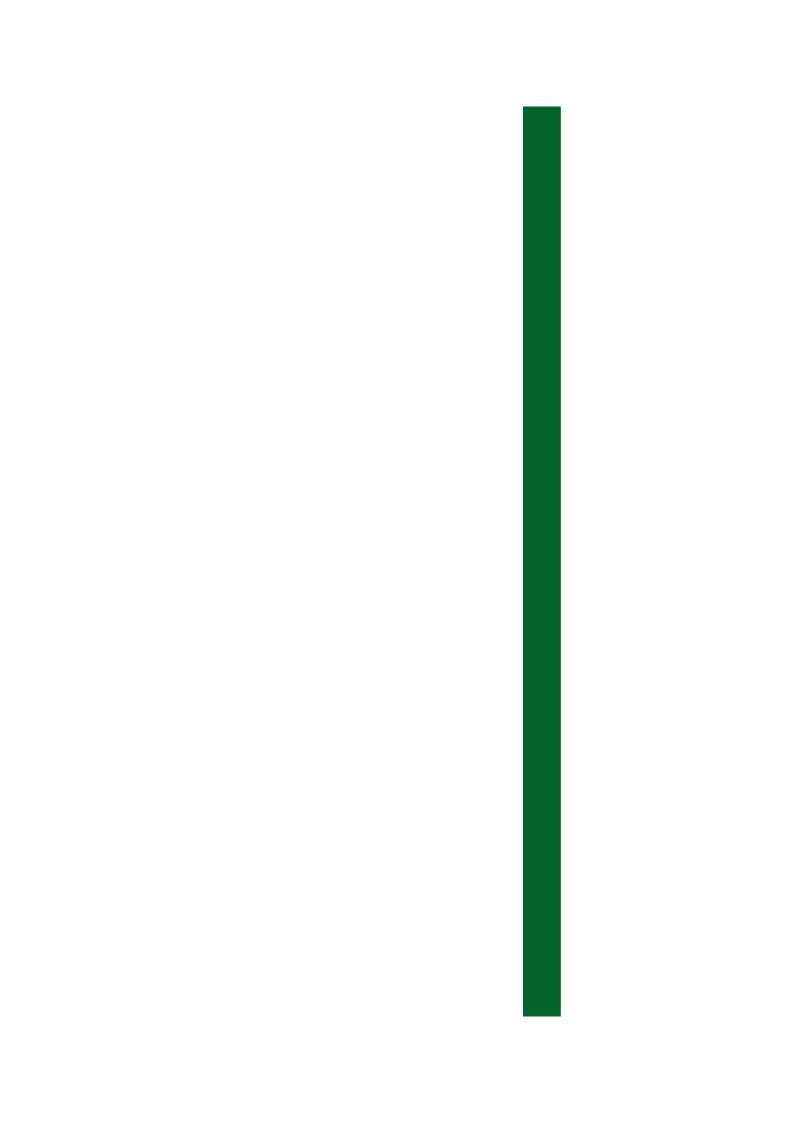
1STAG

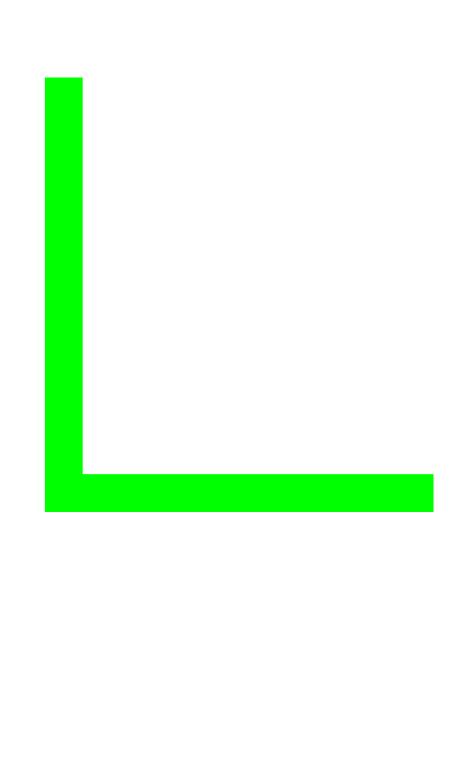


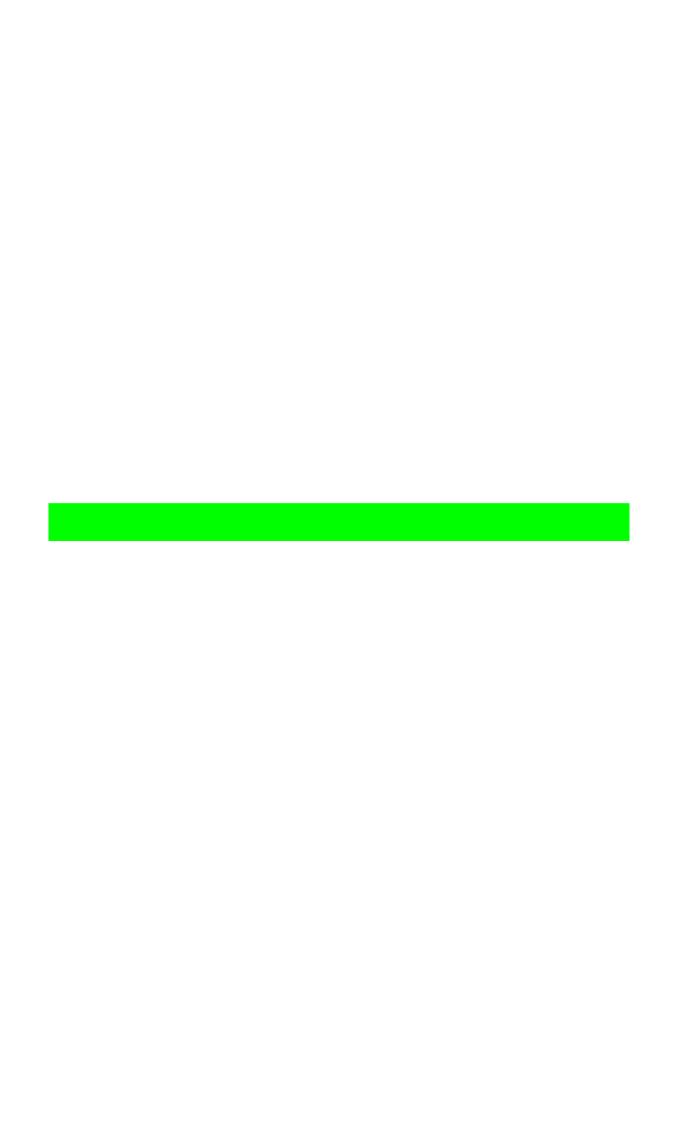


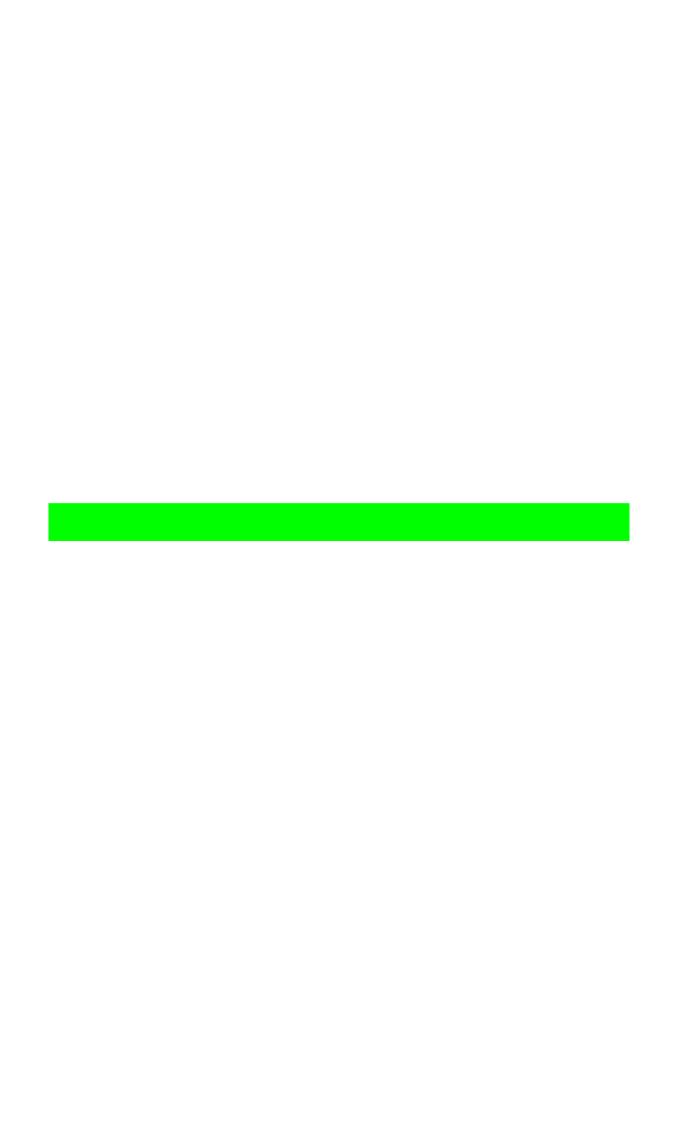


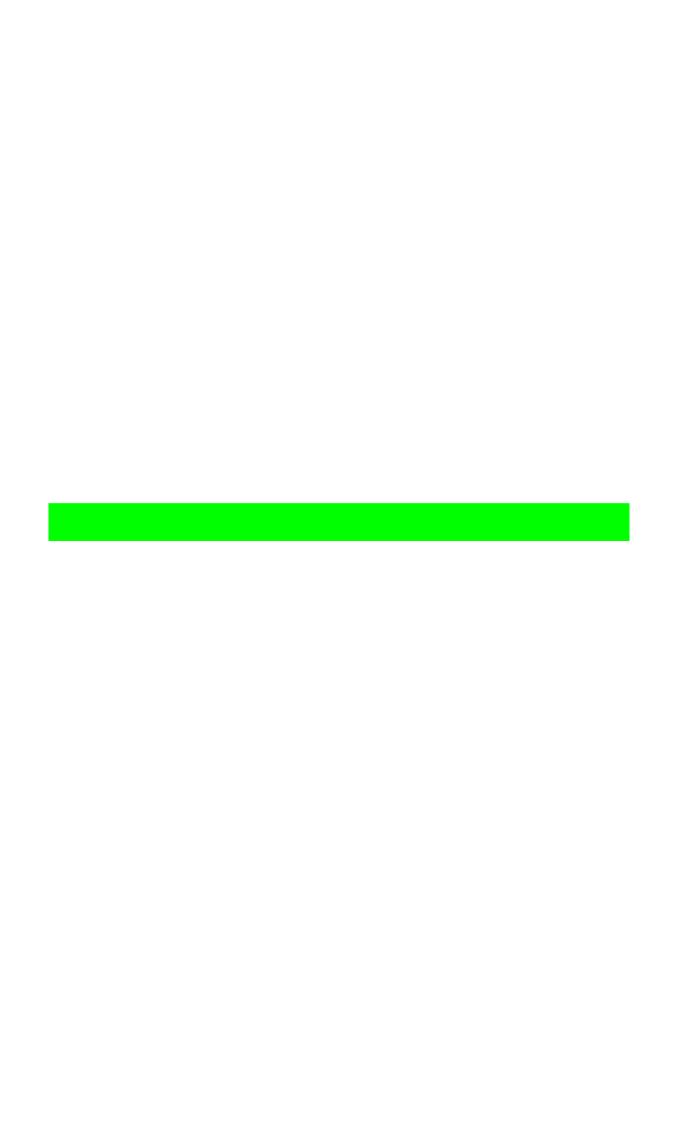


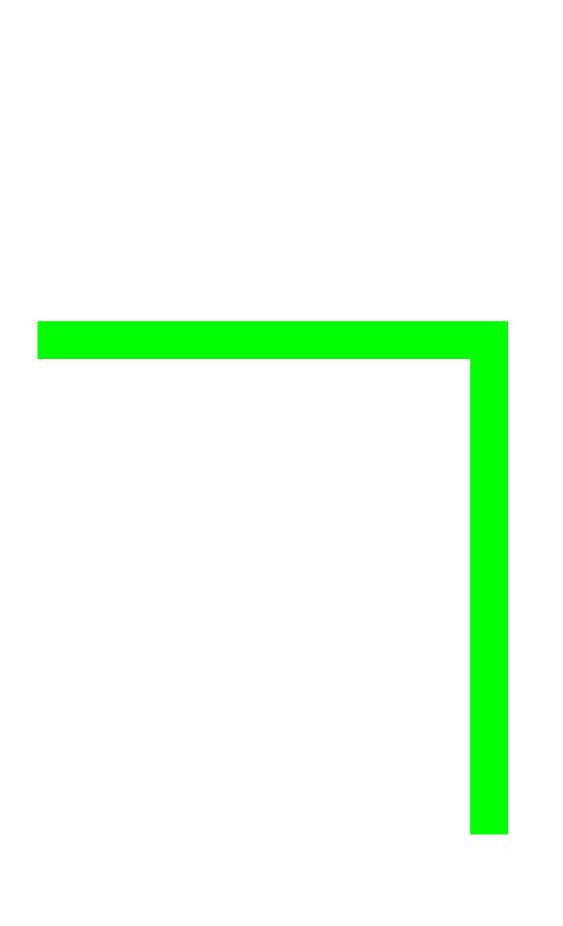


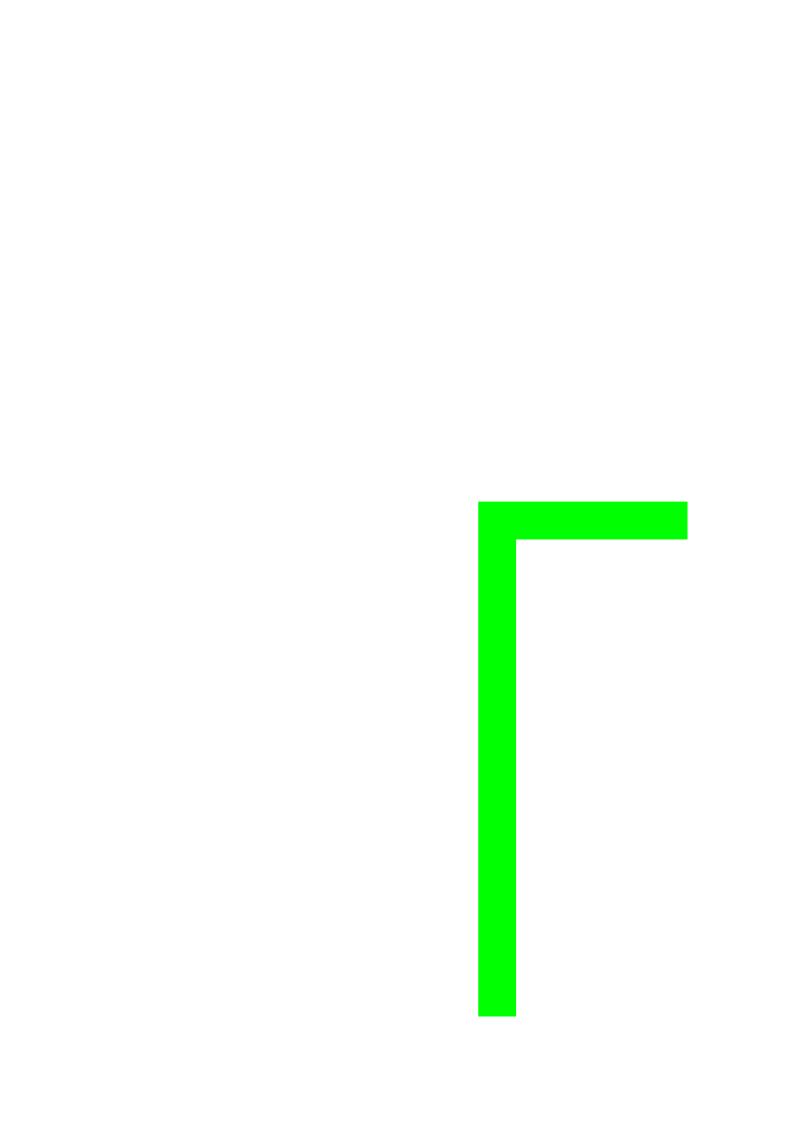


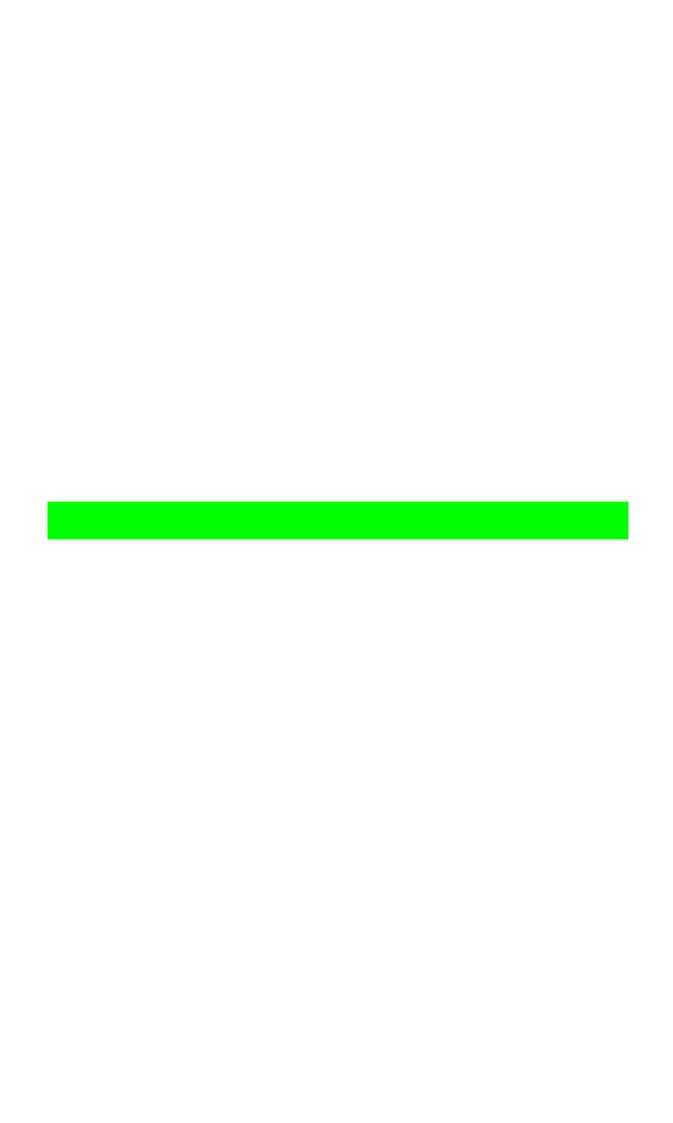


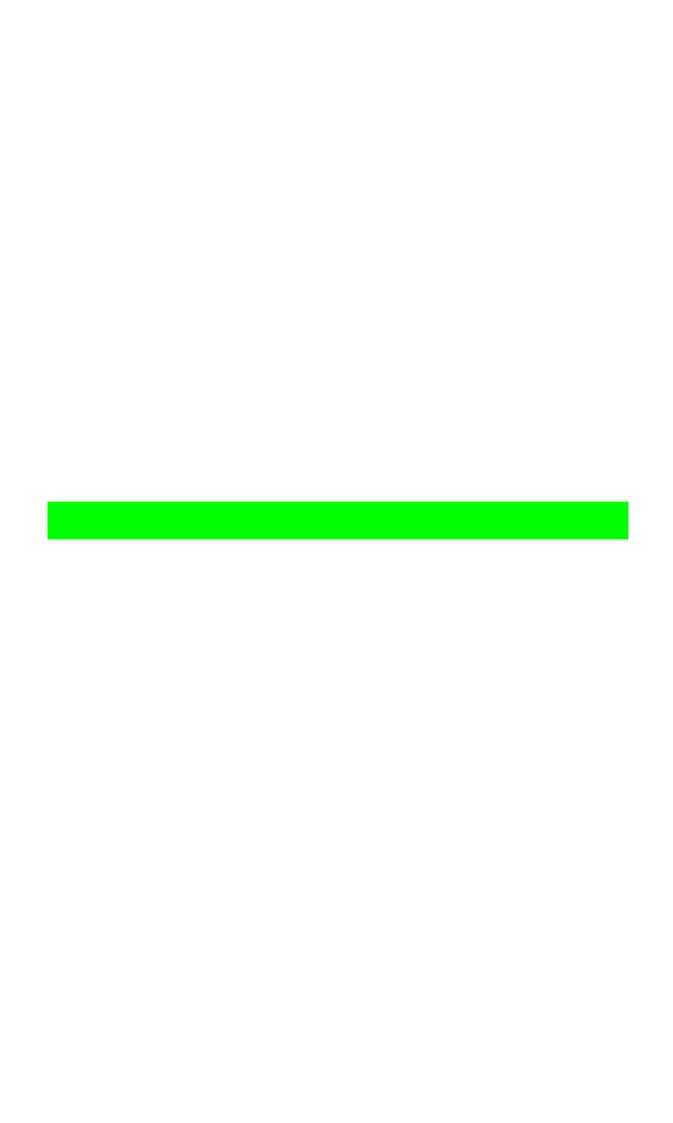












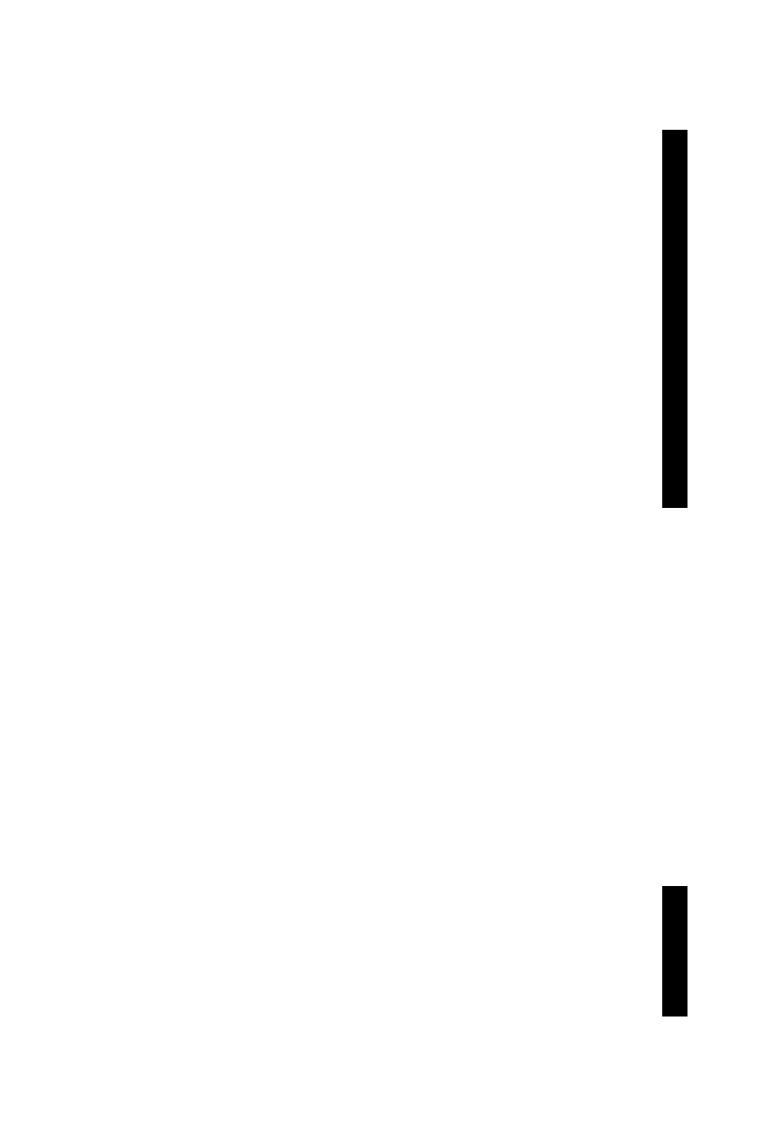
MM_rd

data

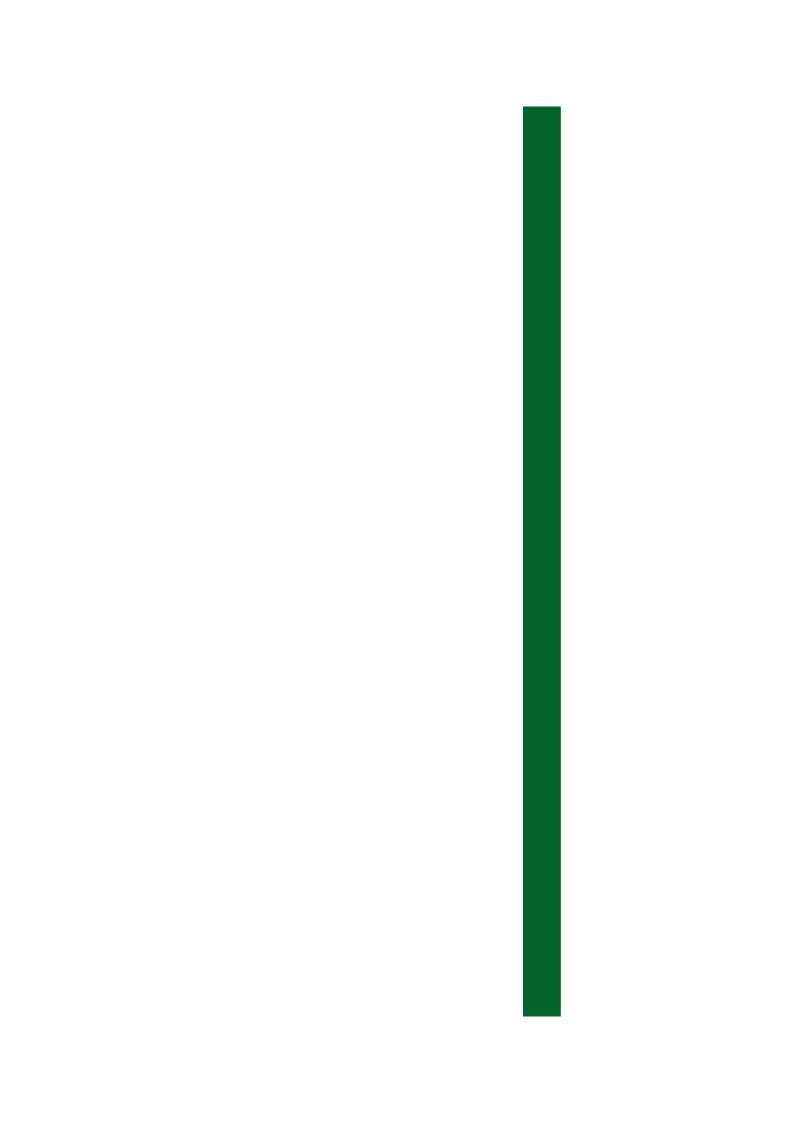


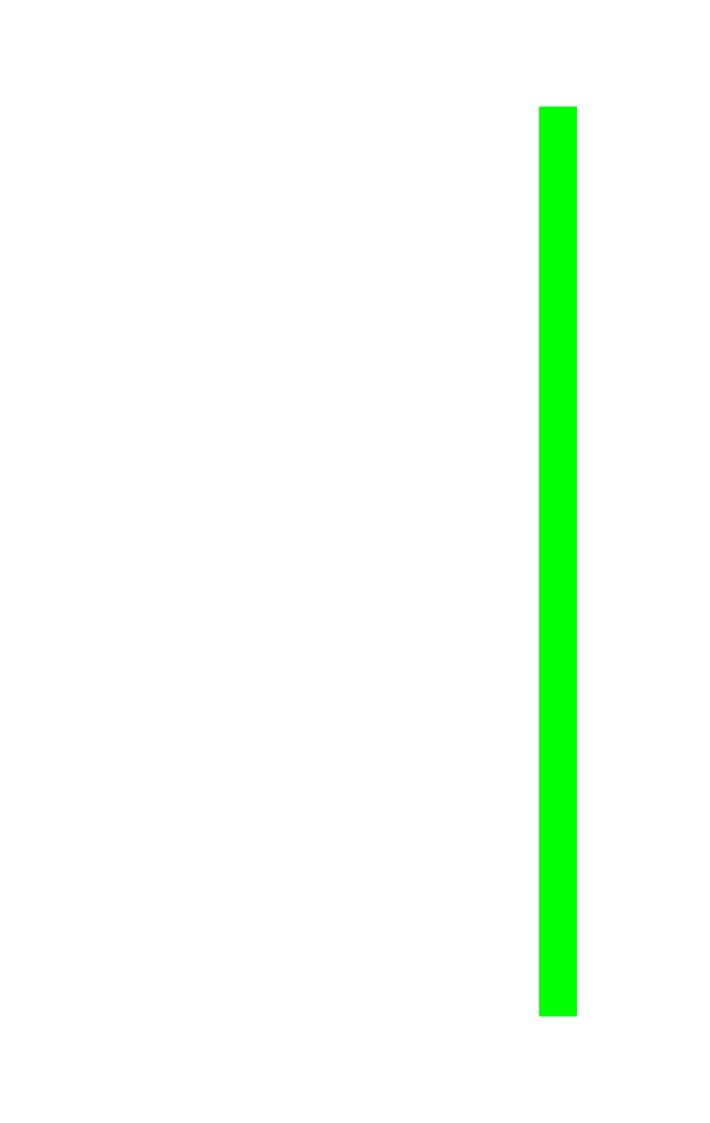


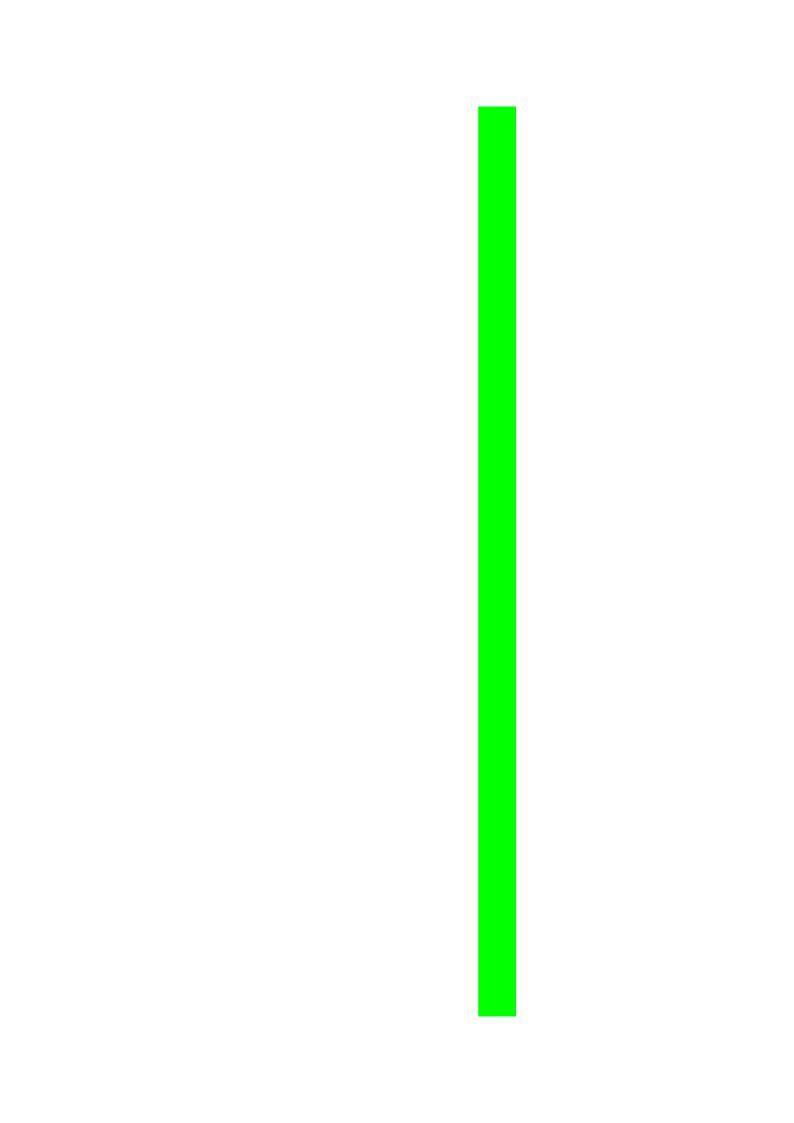




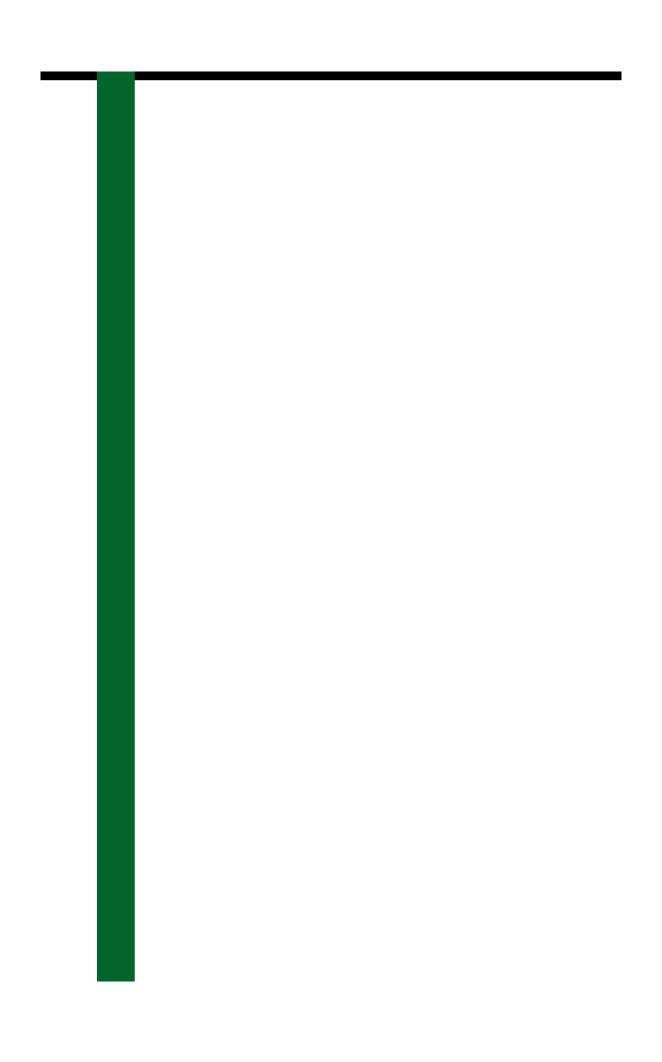


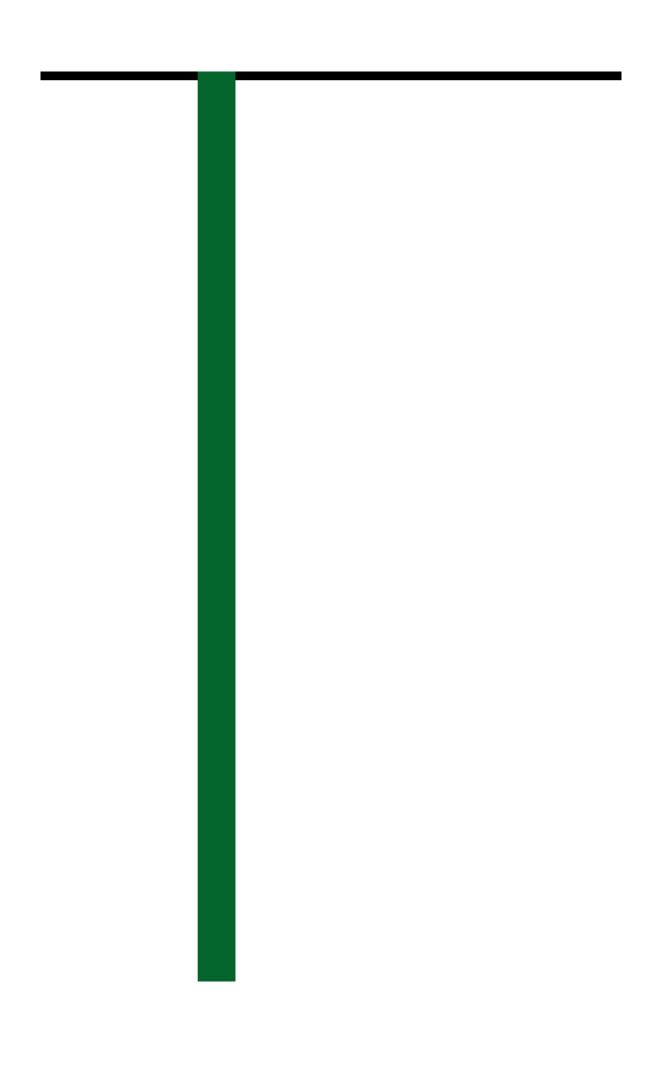


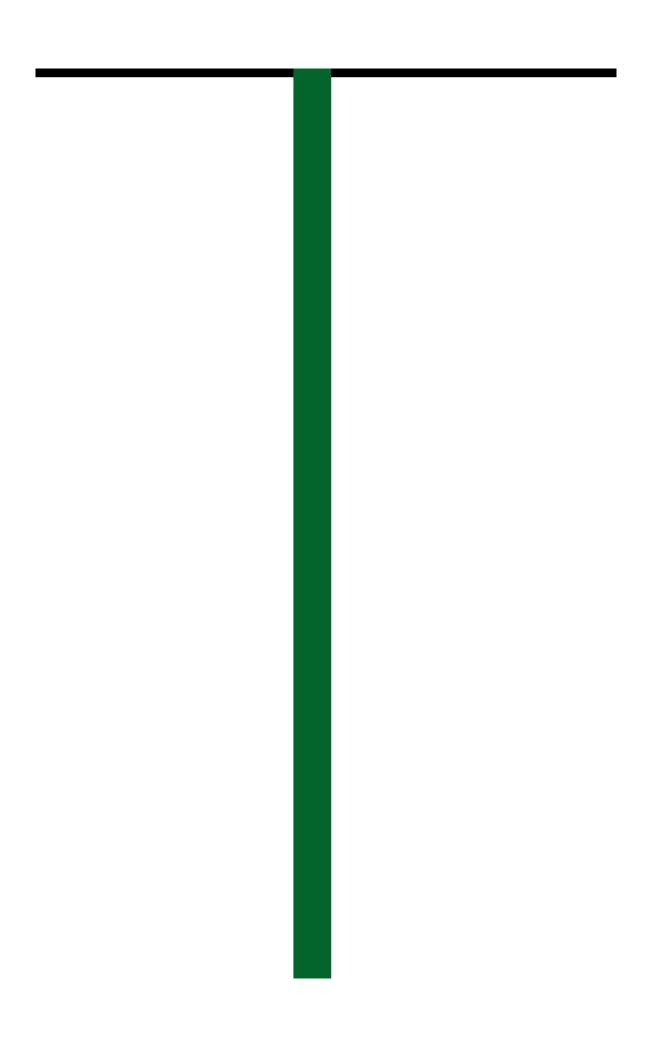


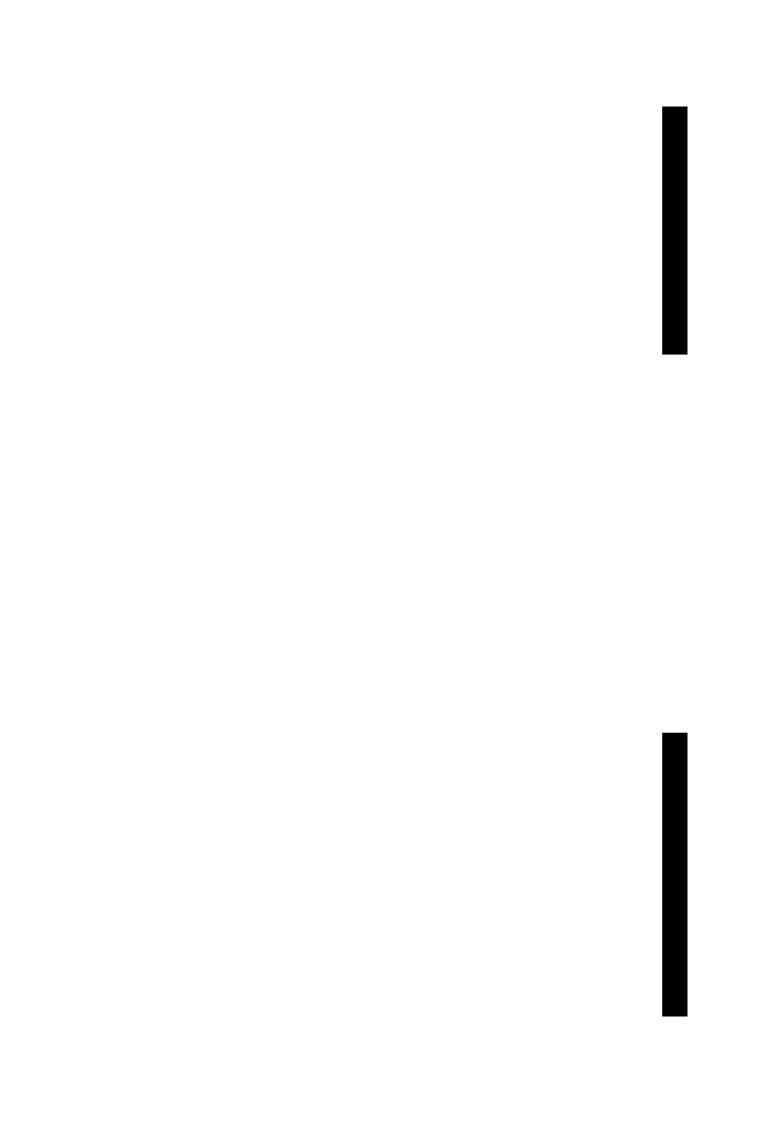
















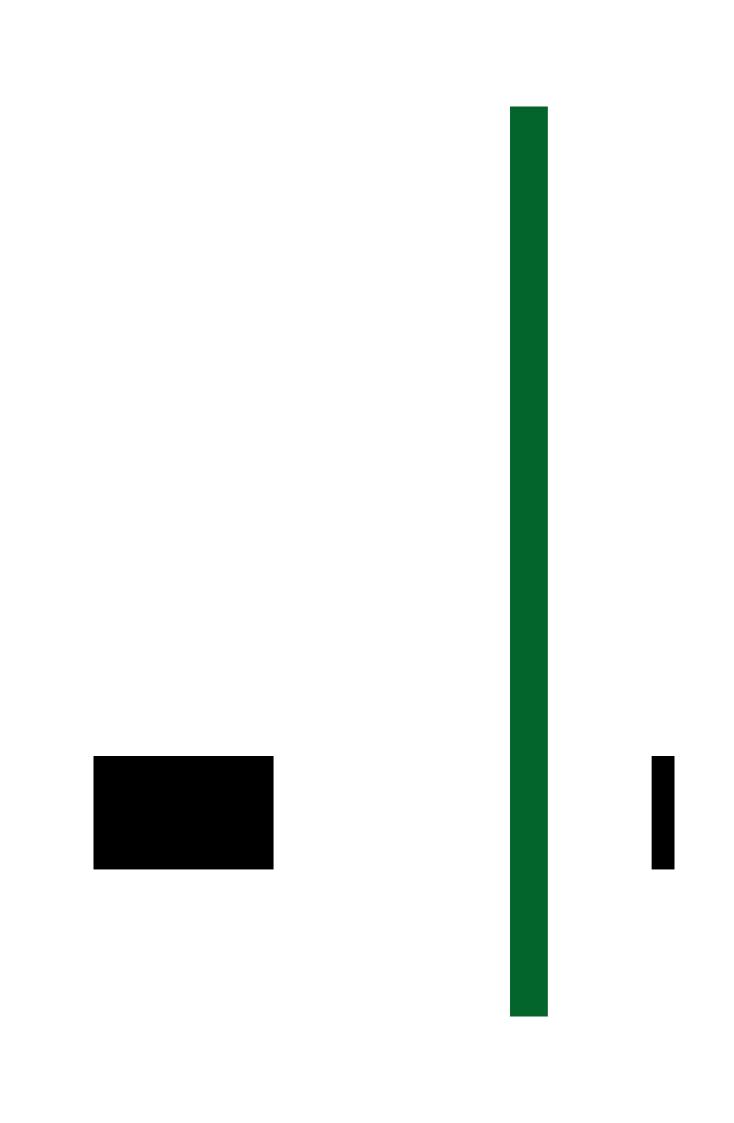








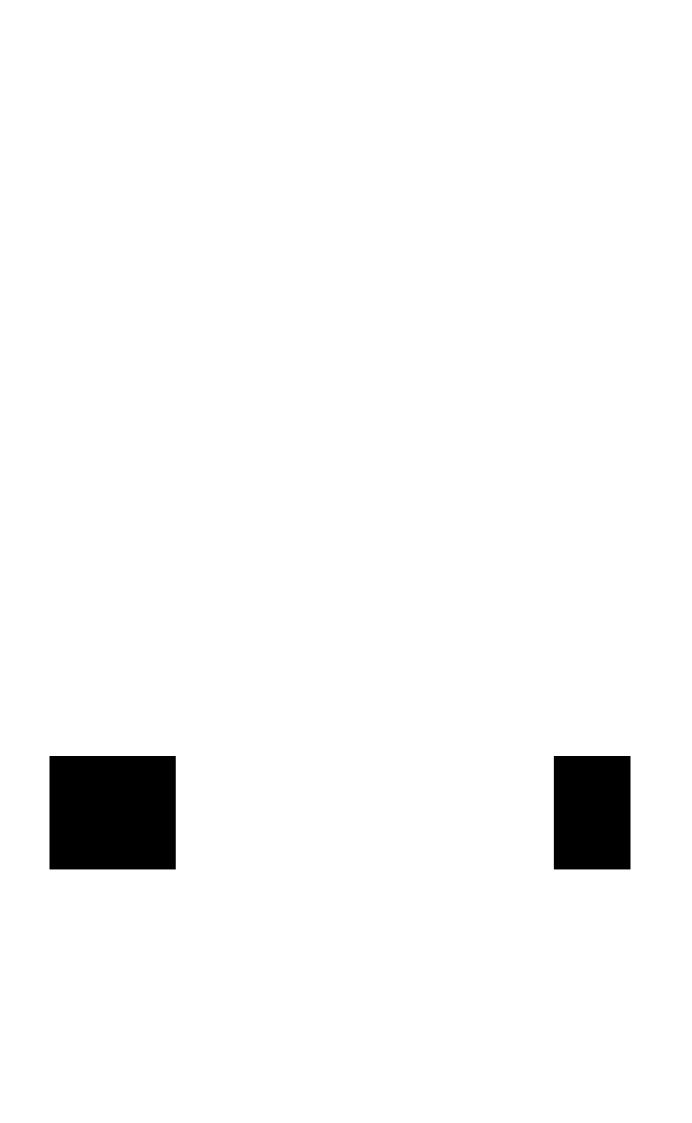








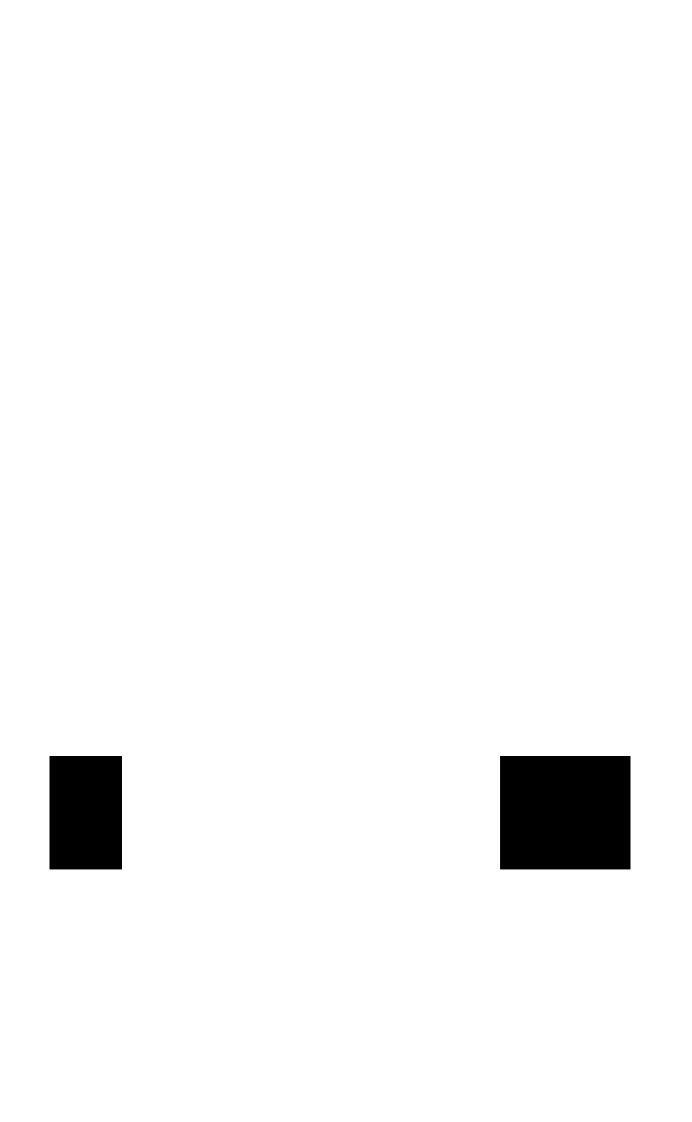


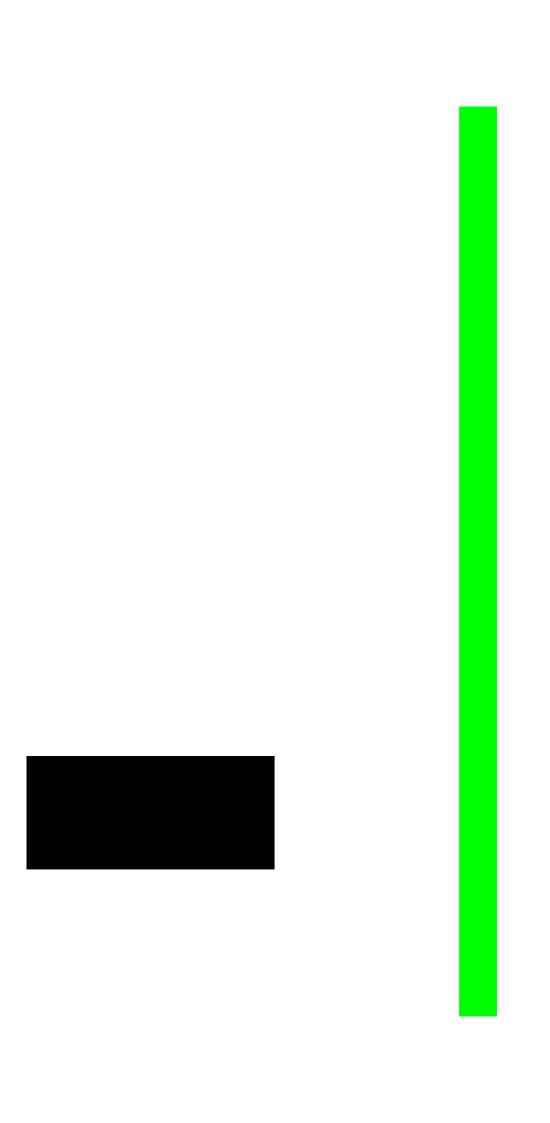


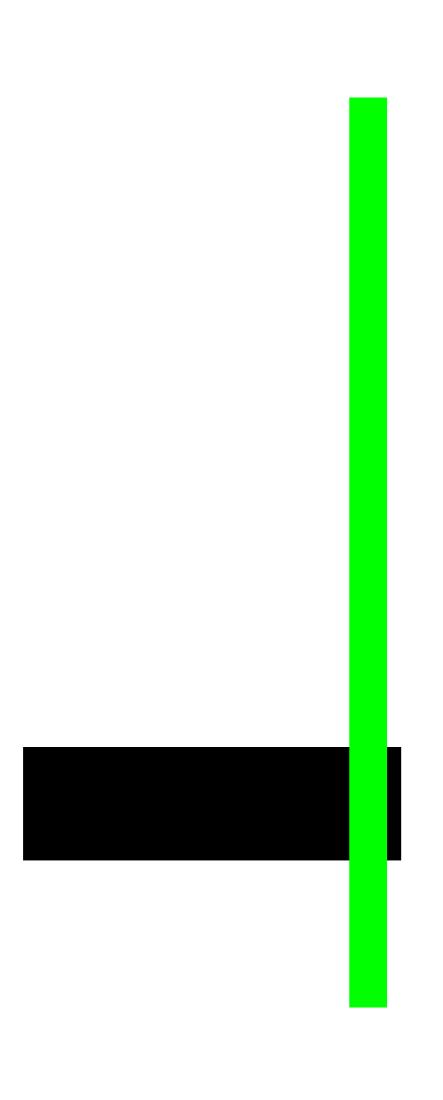




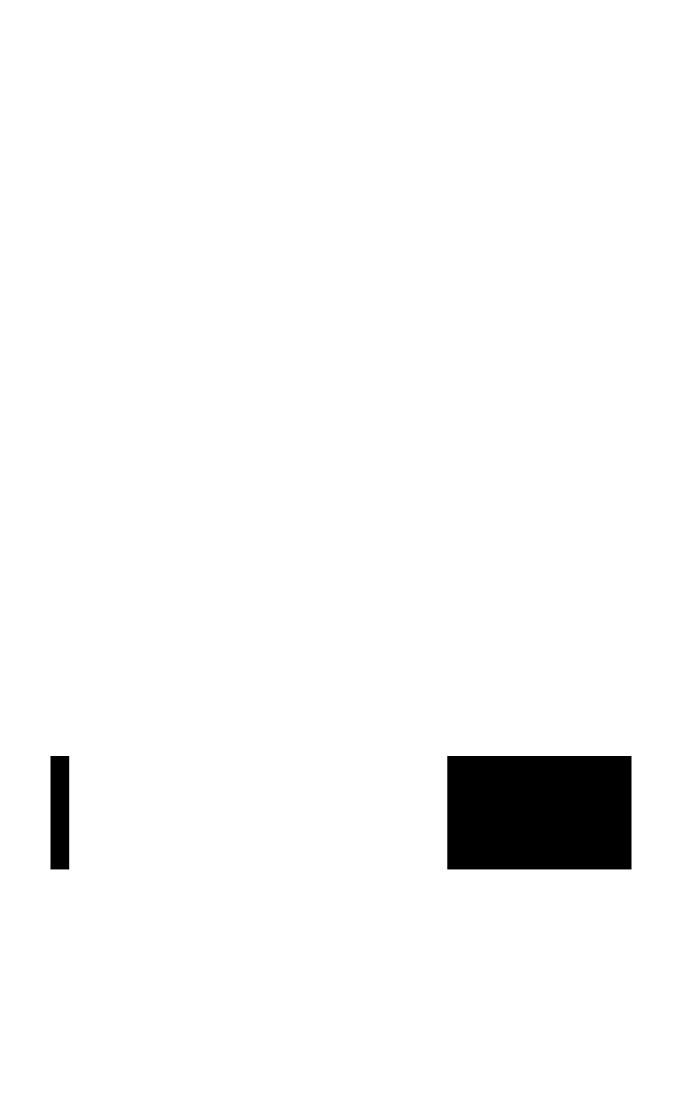








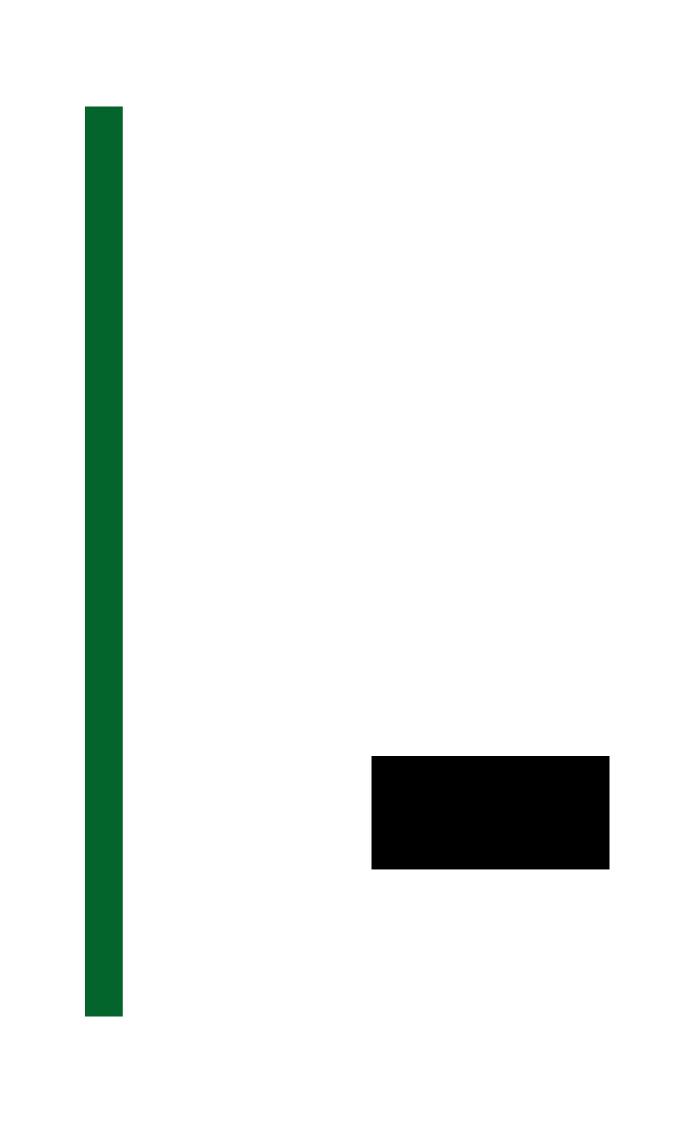


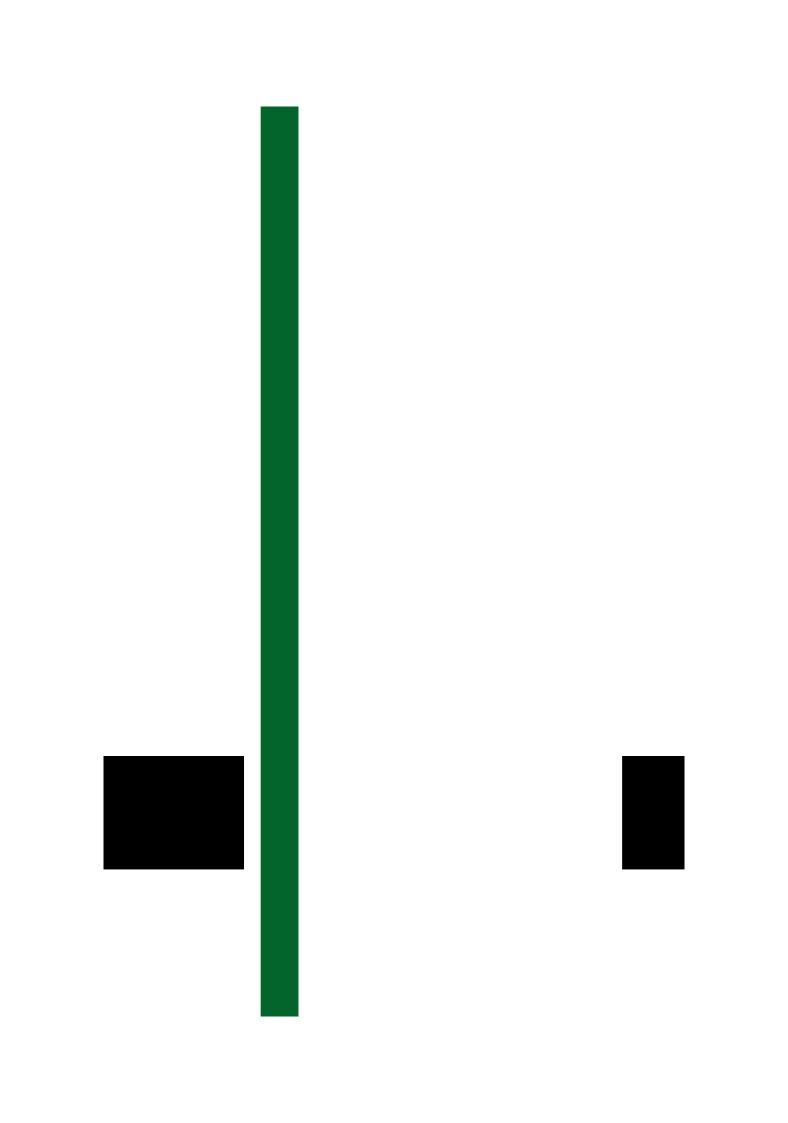


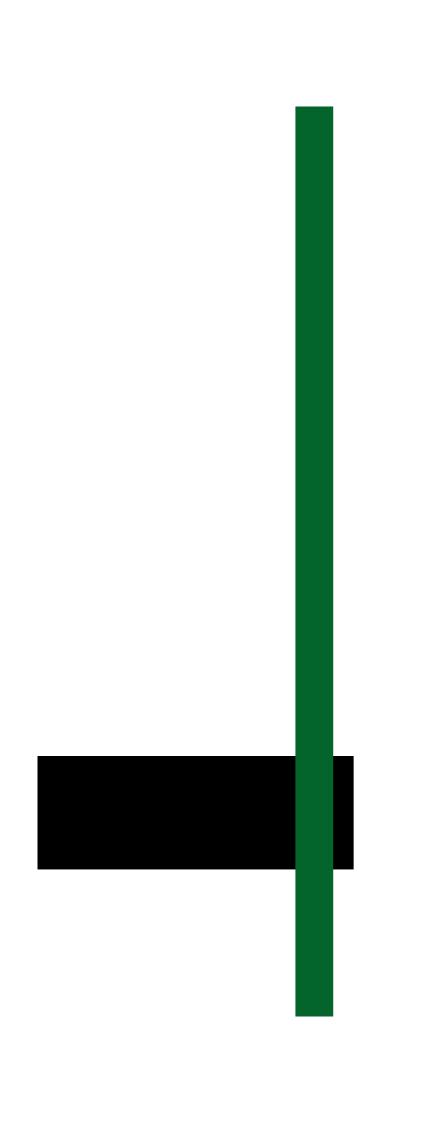






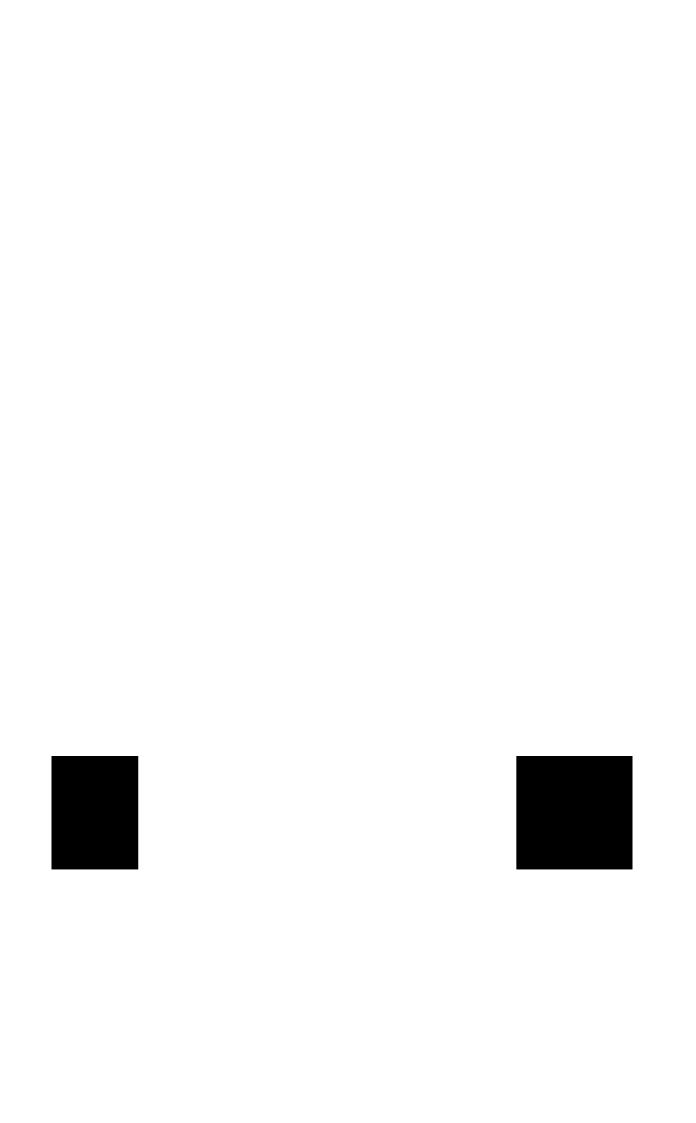




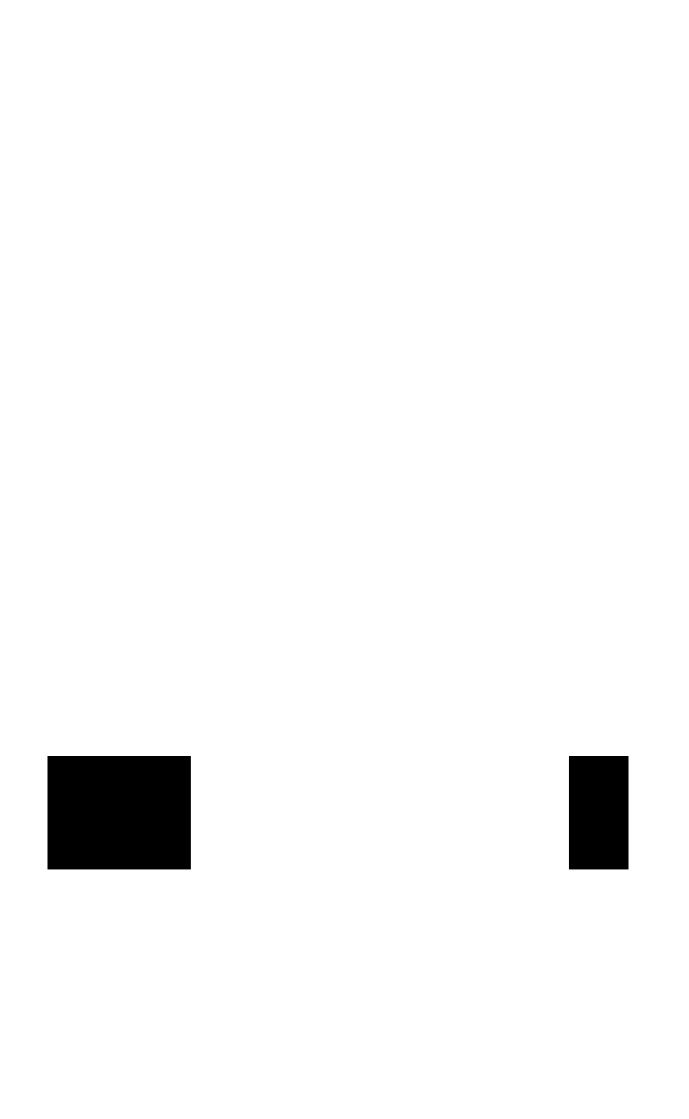




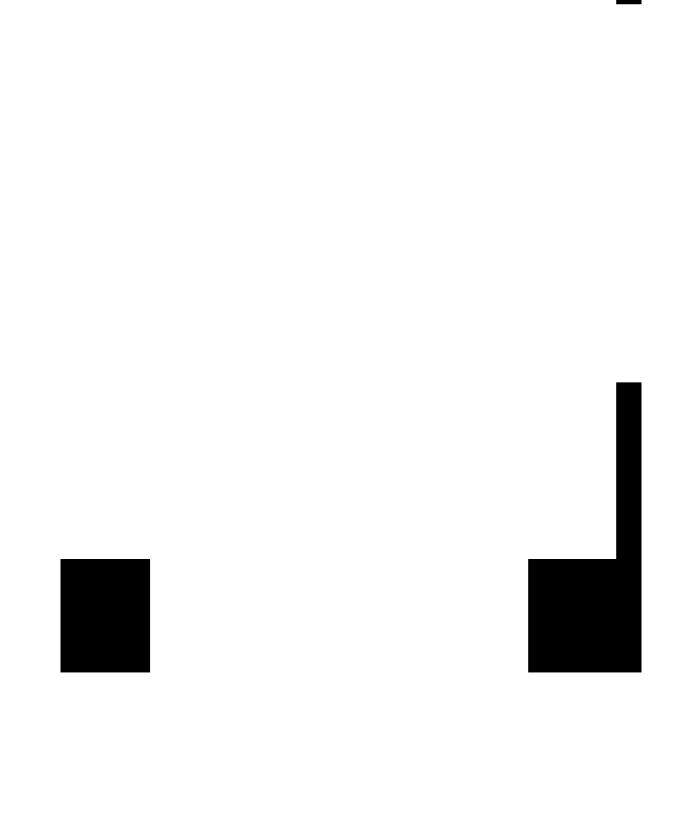


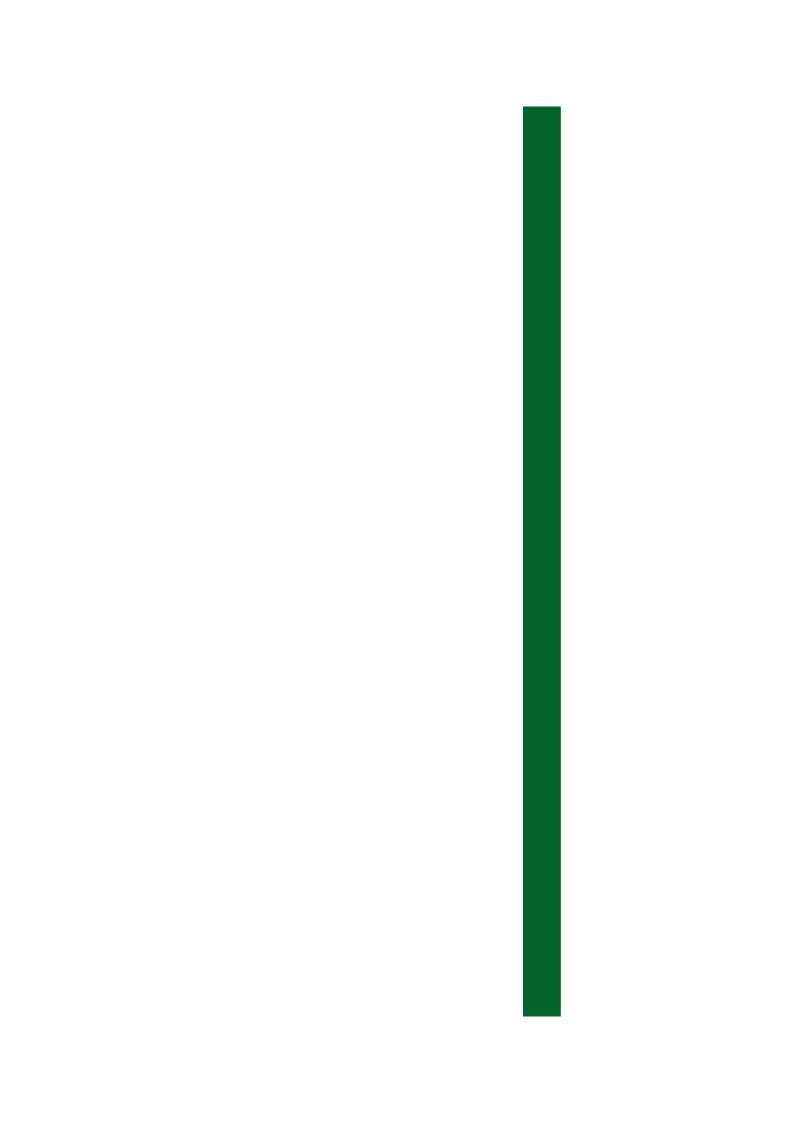


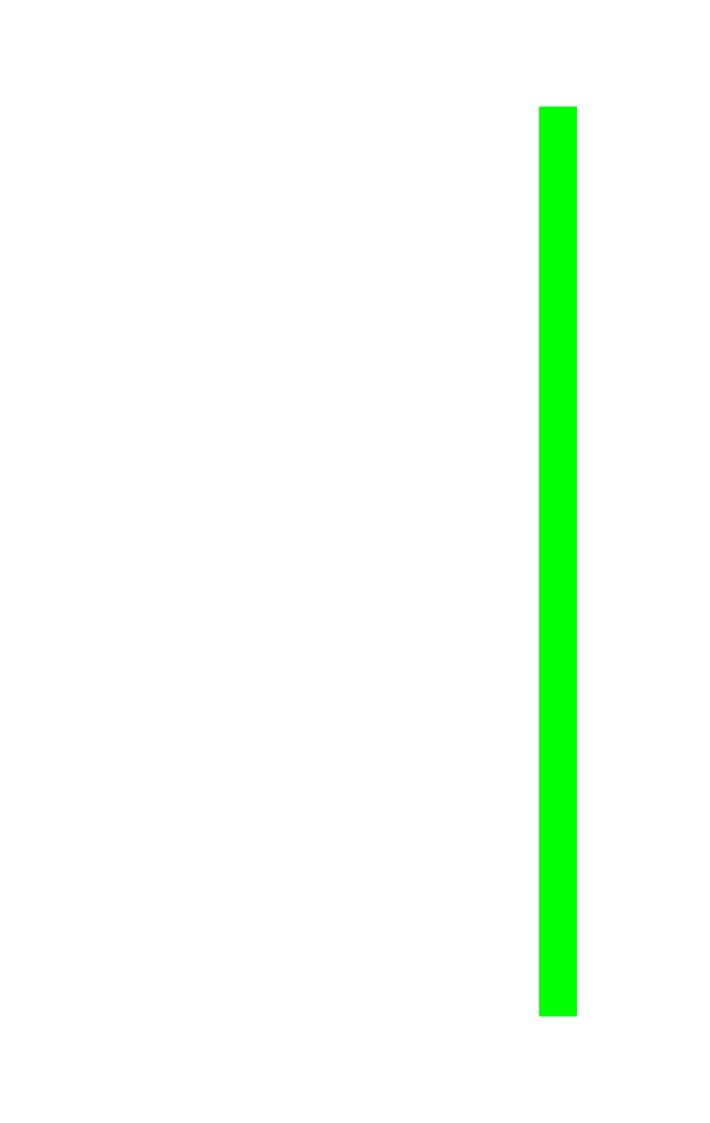


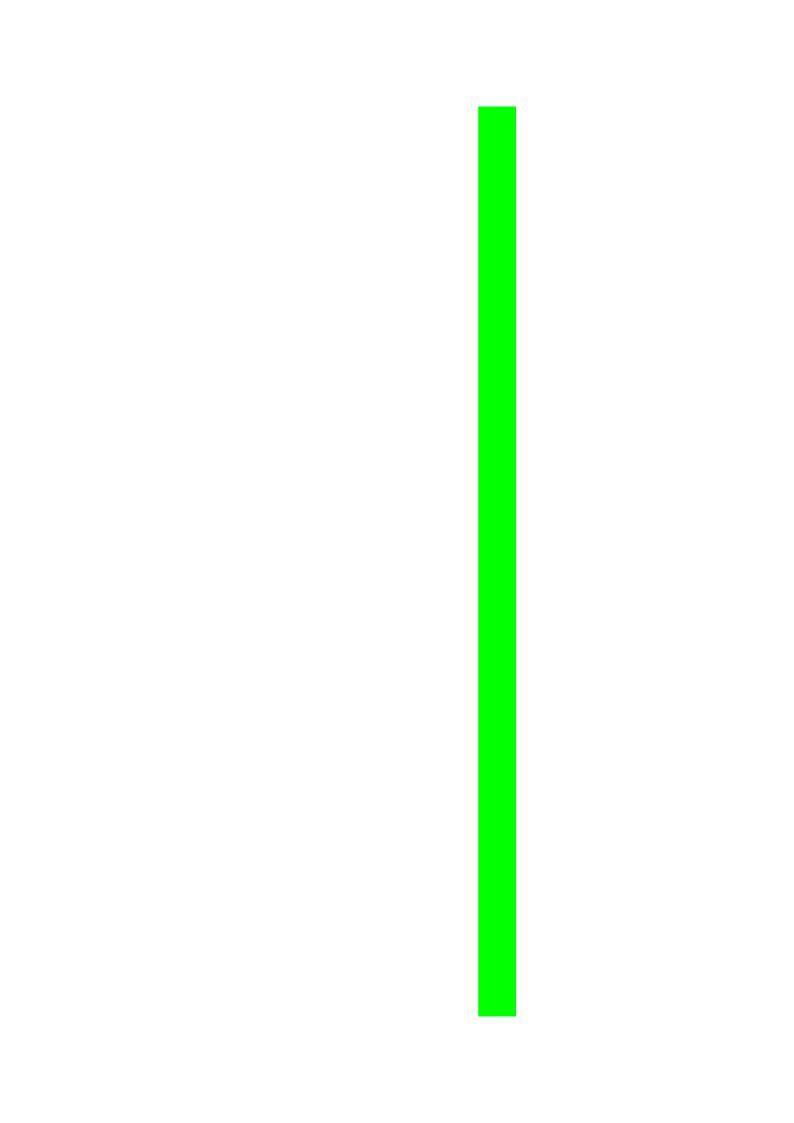


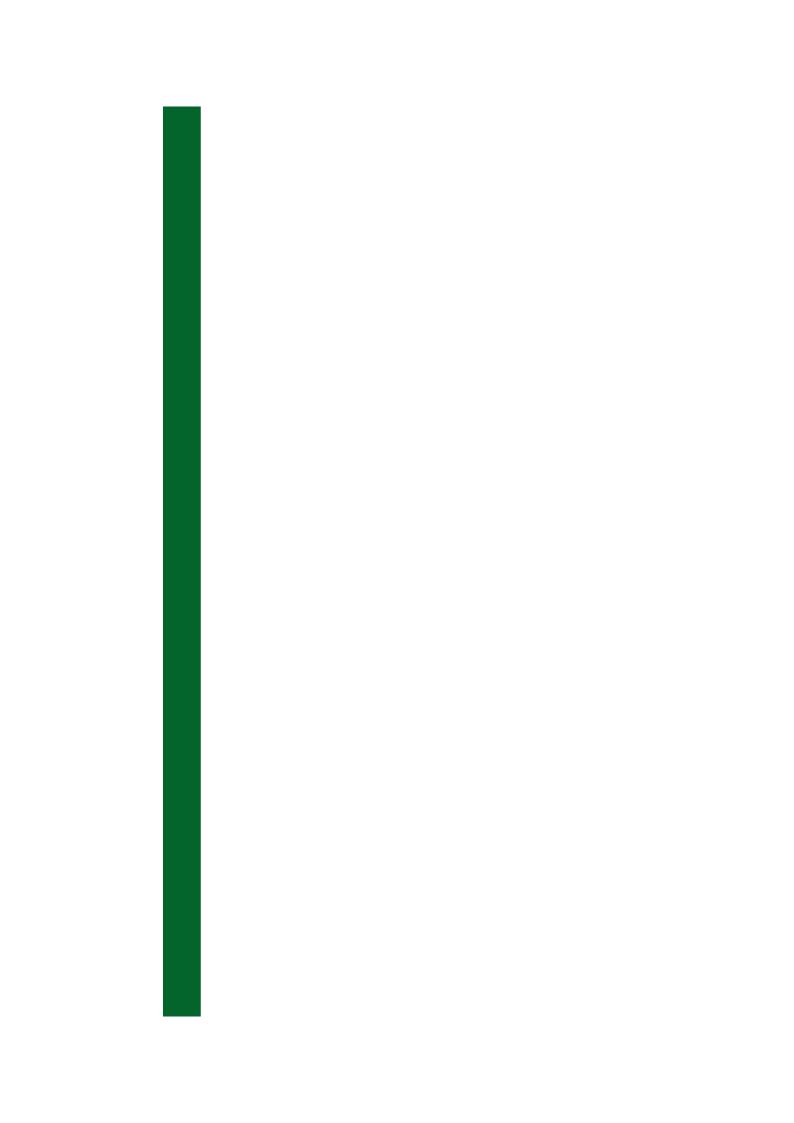


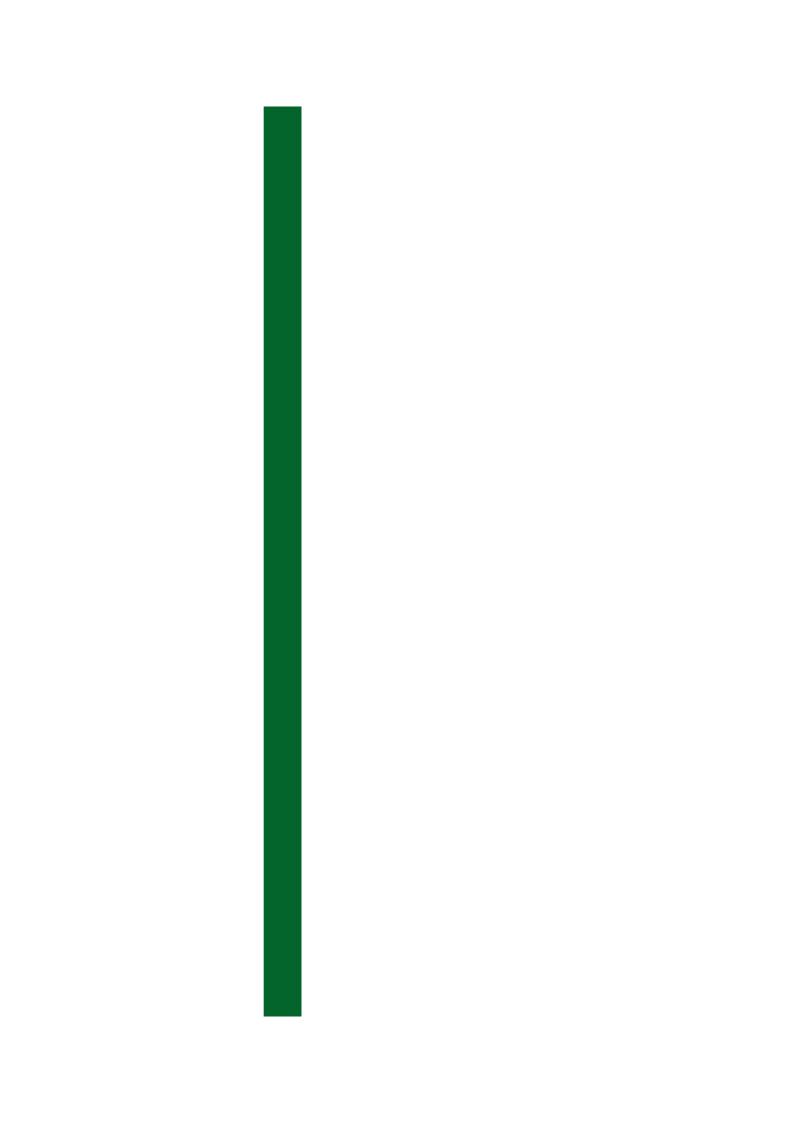


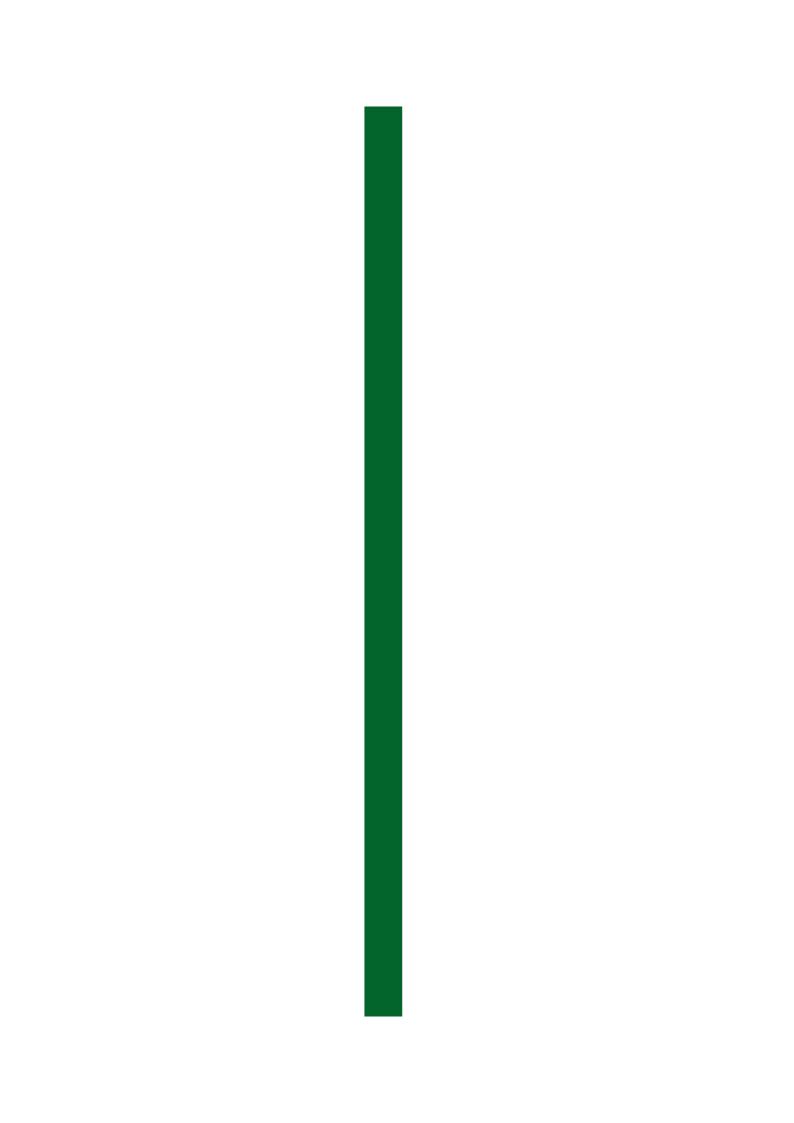


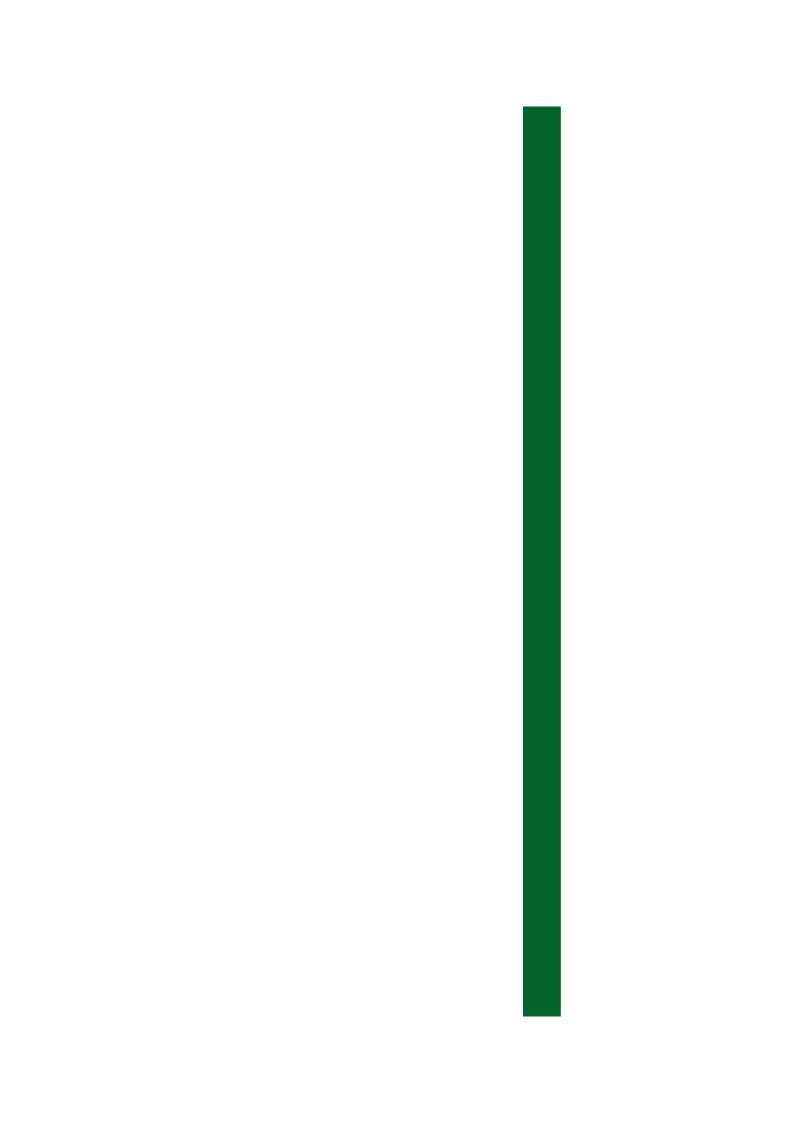






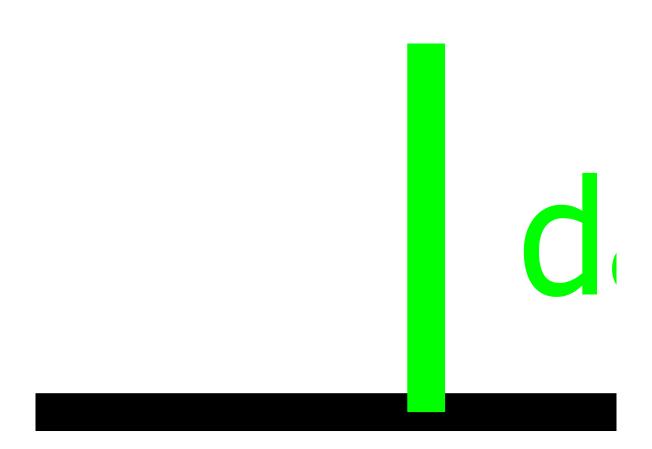




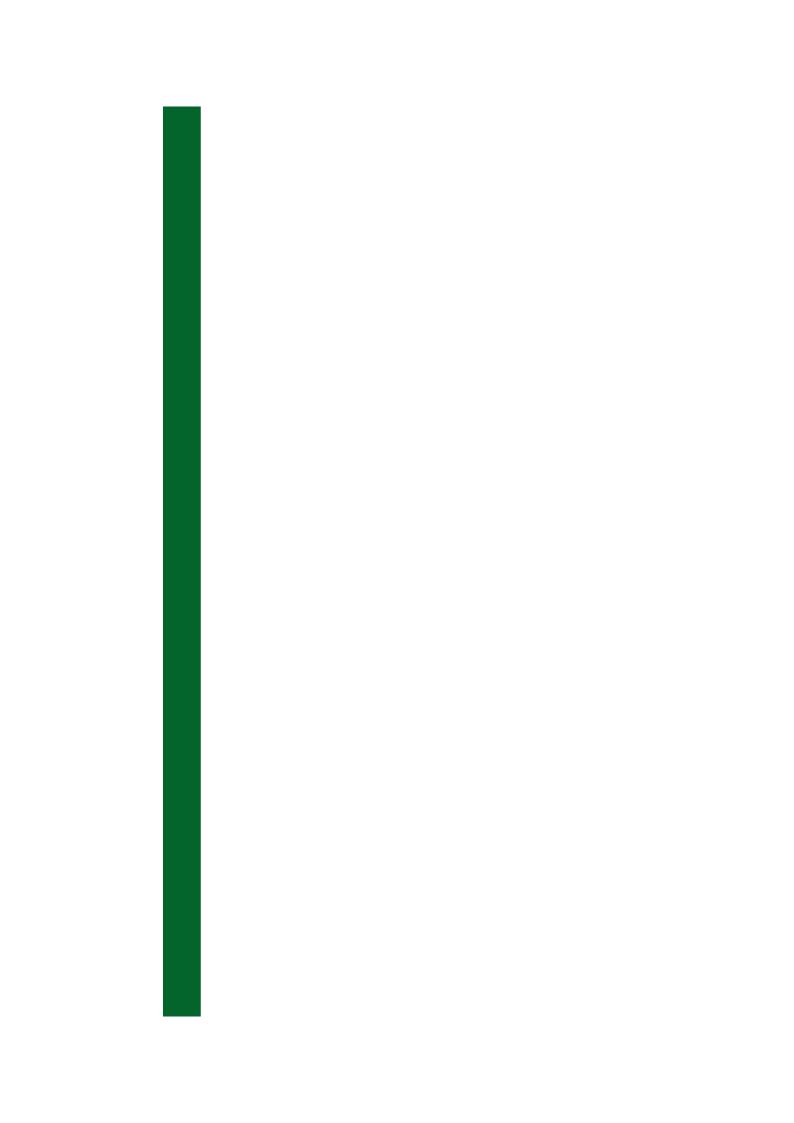


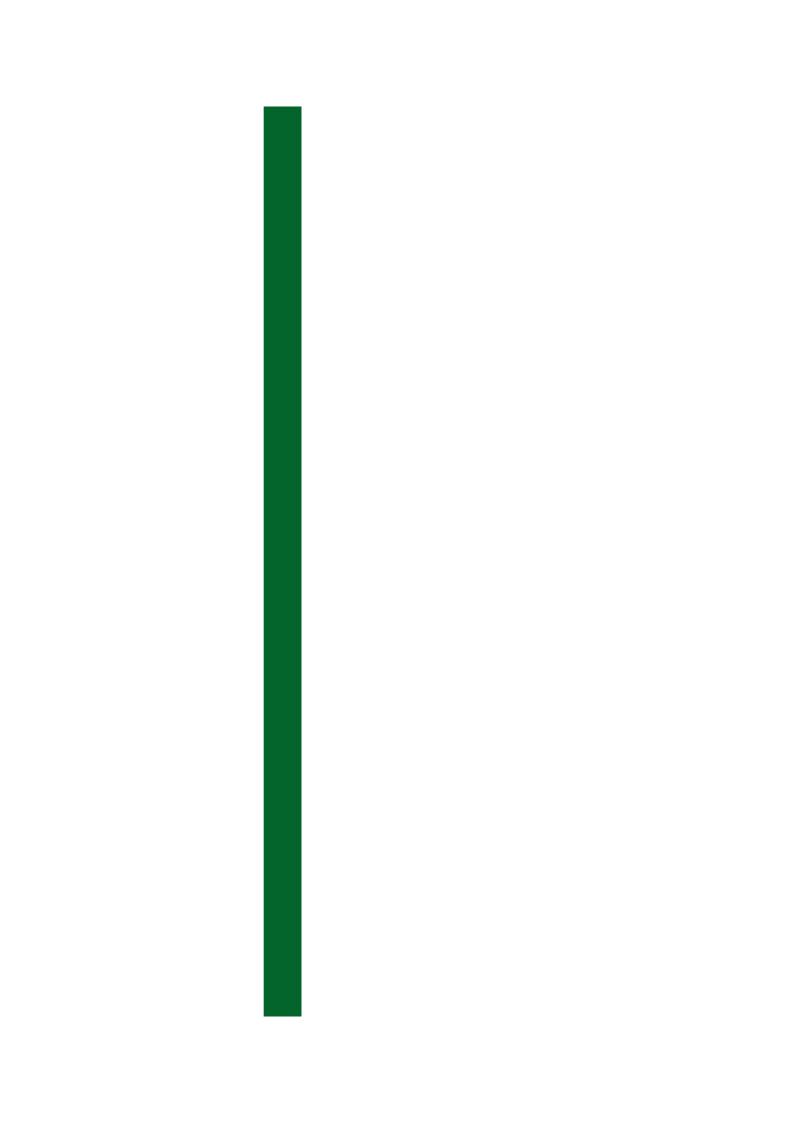
instr

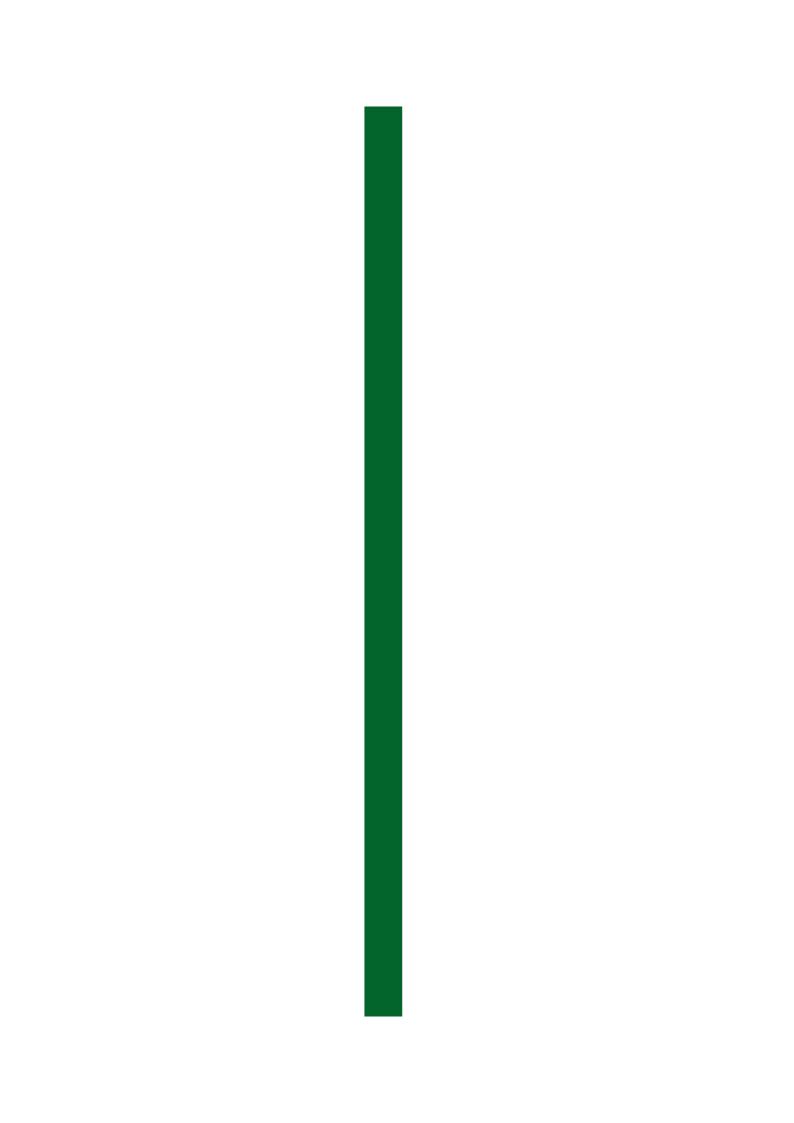
dout

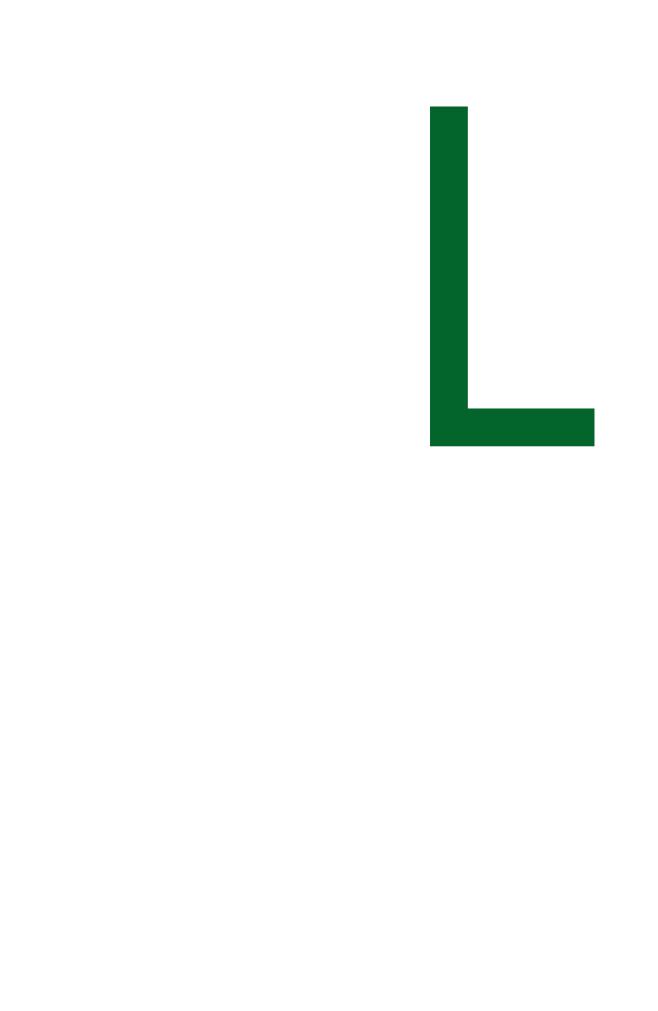


ata_dou















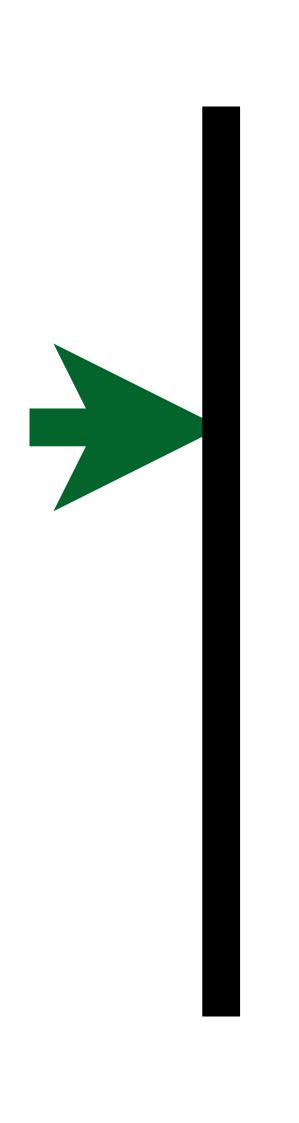


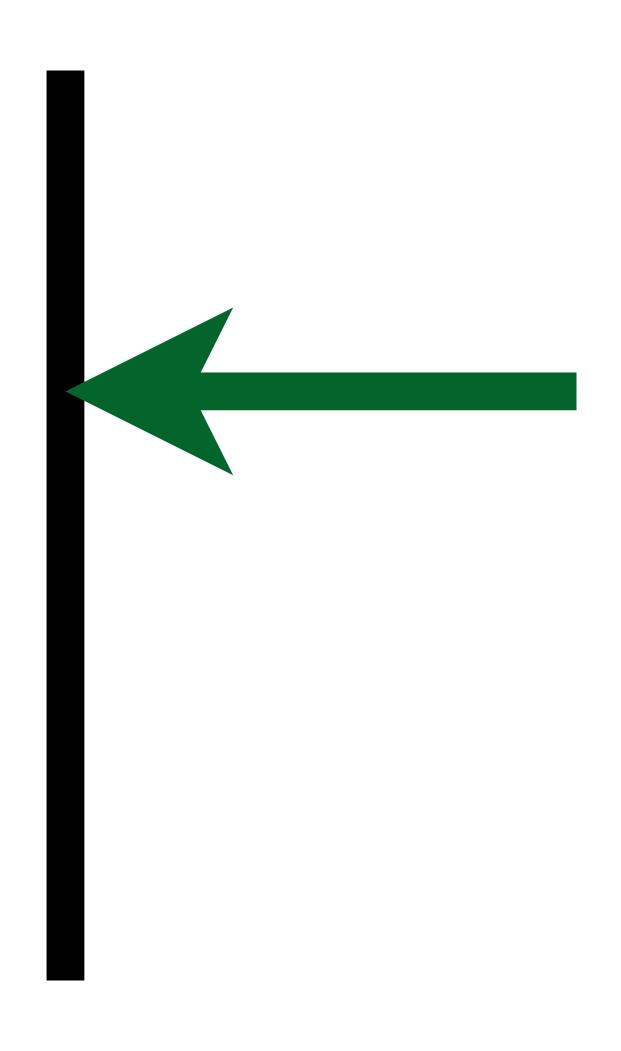




ins

tr_addr



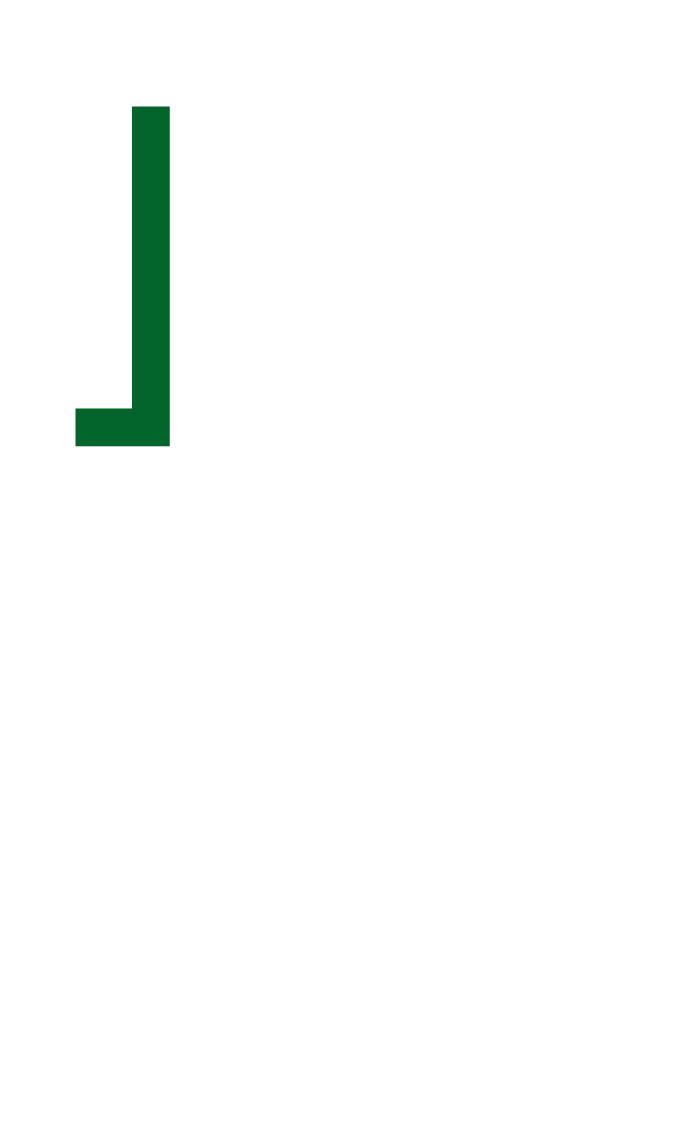


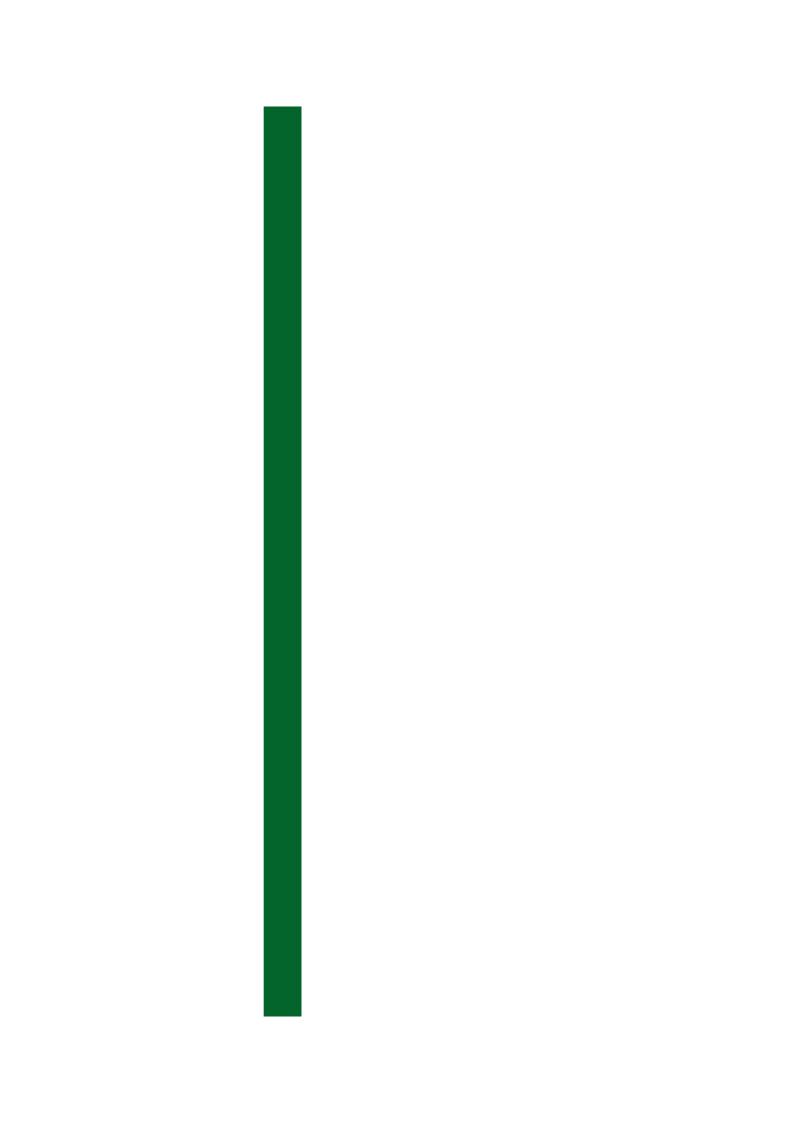


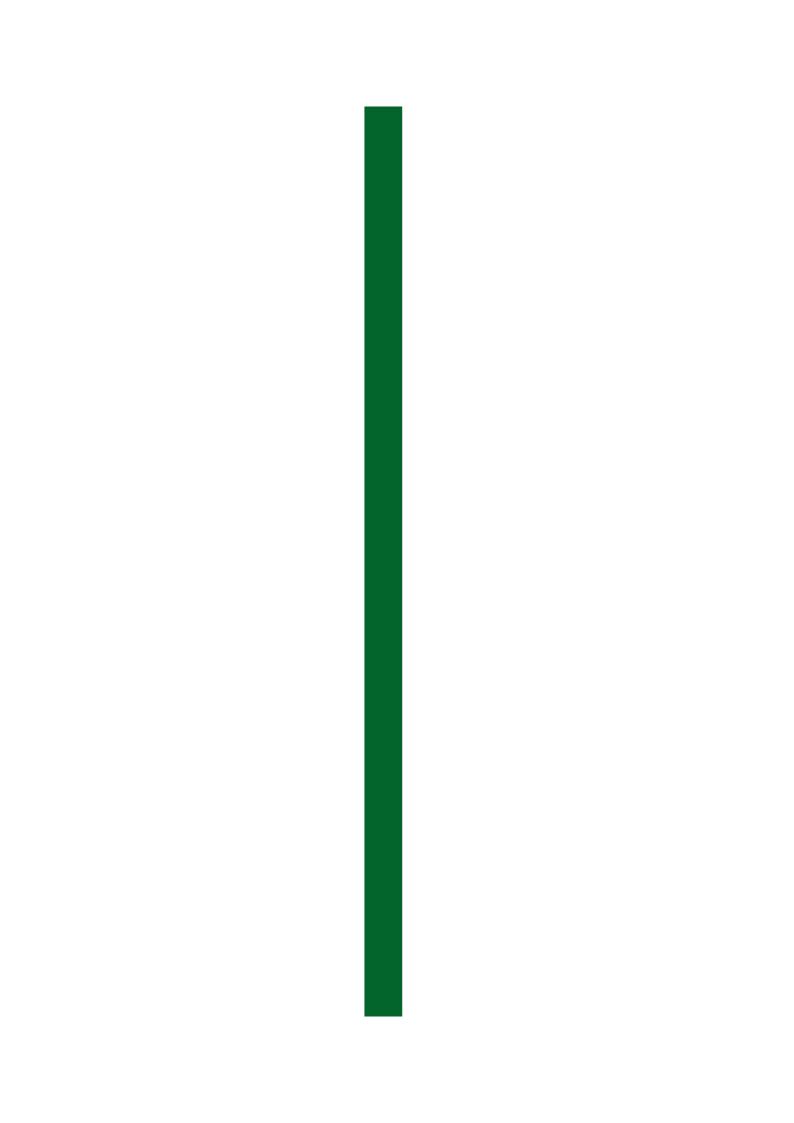


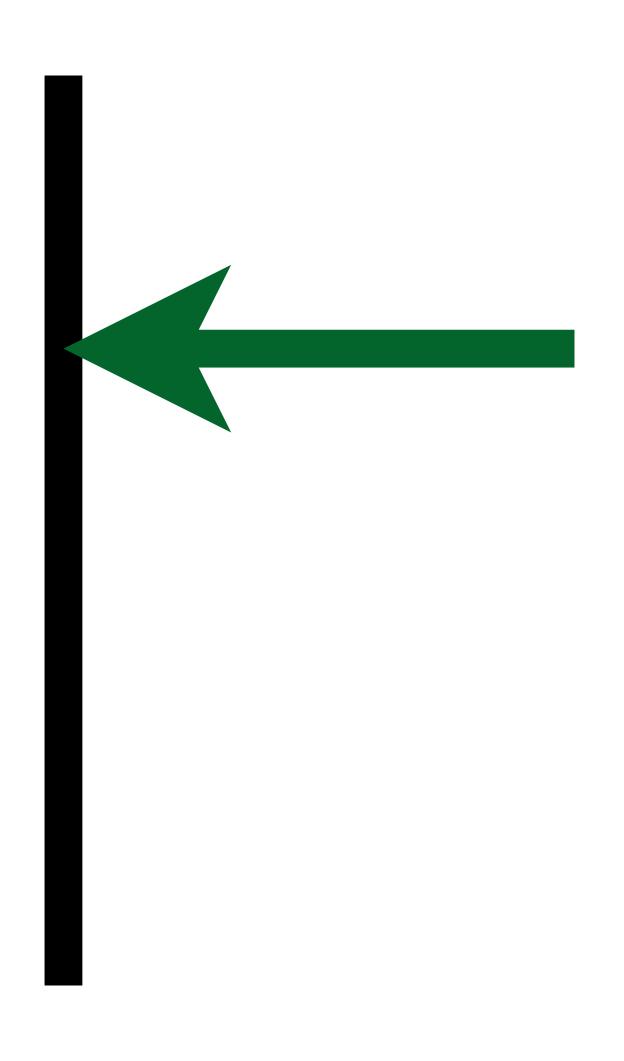












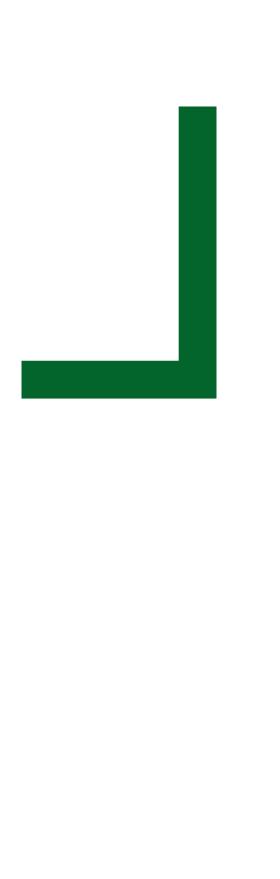


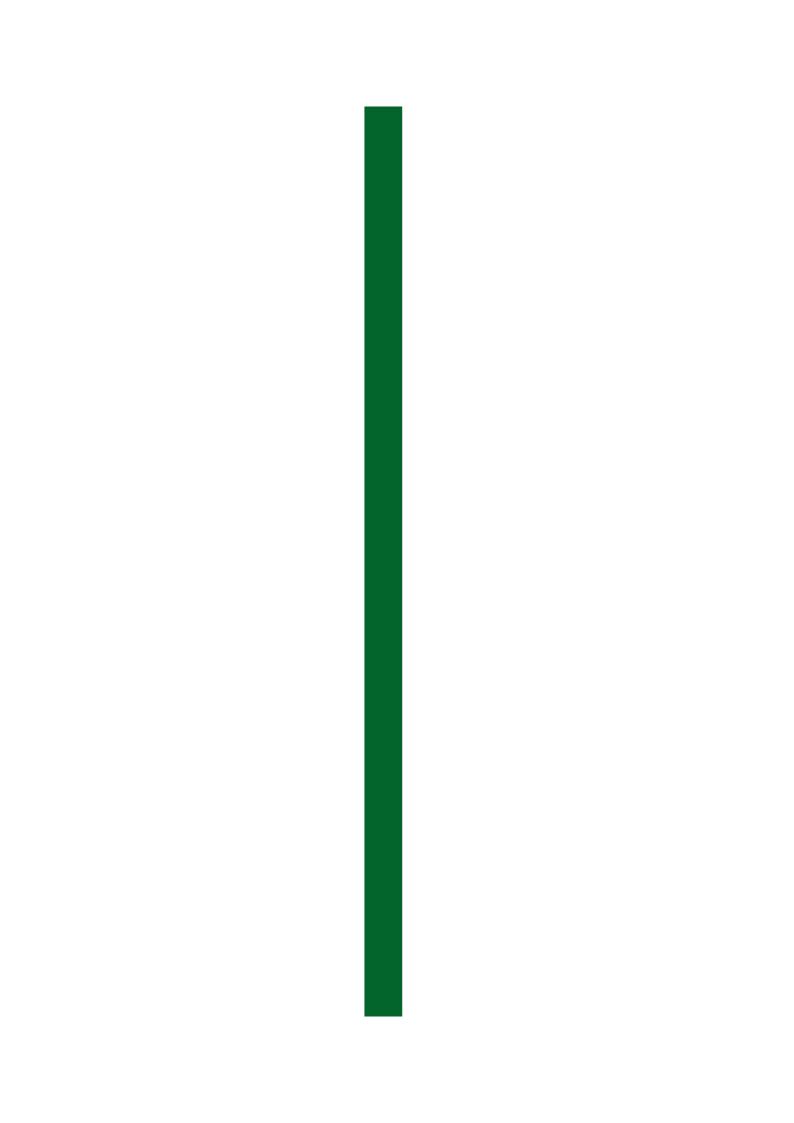












COC



