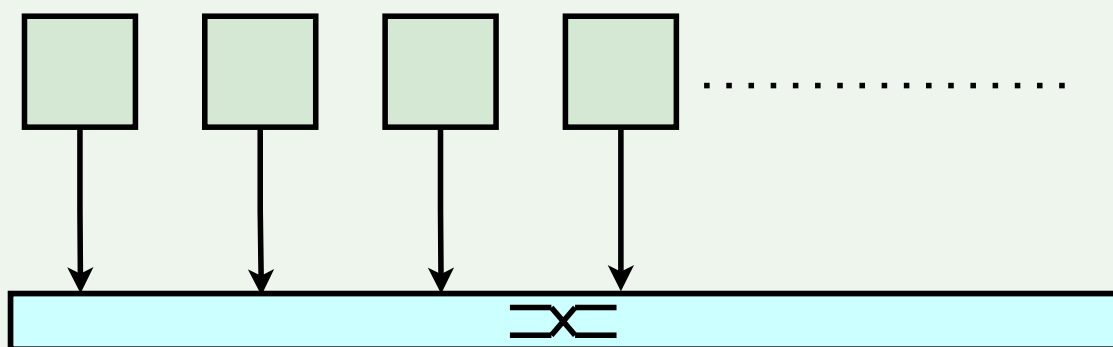
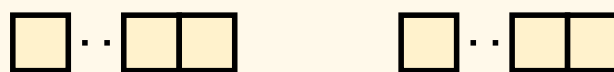


**L3: Channel
Memory**



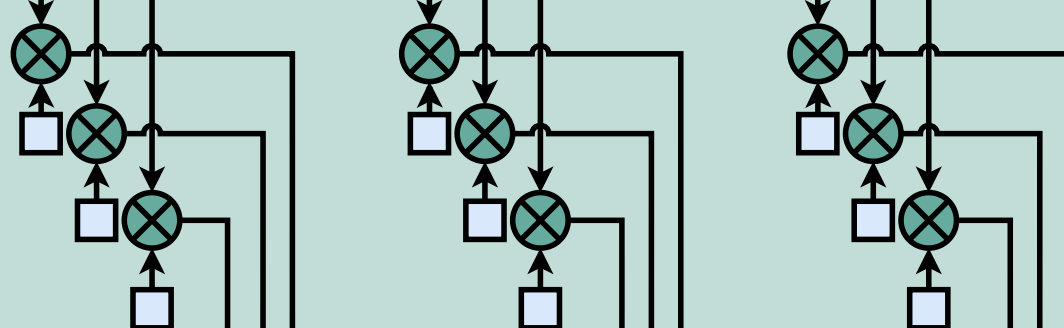
**L2: Verticle
Reuse Memory**



**L1: Horizontal
Reuse
Memory**



**Weight
Multiplication**



**Partial Sum
Reduction Tree**

