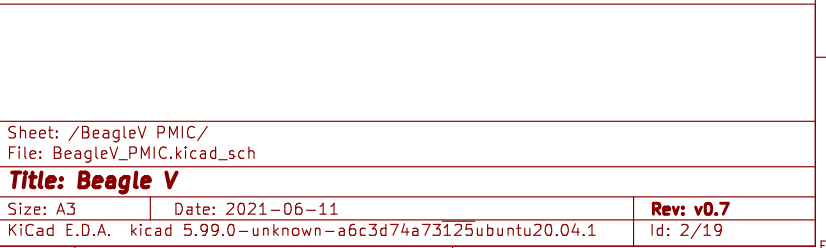
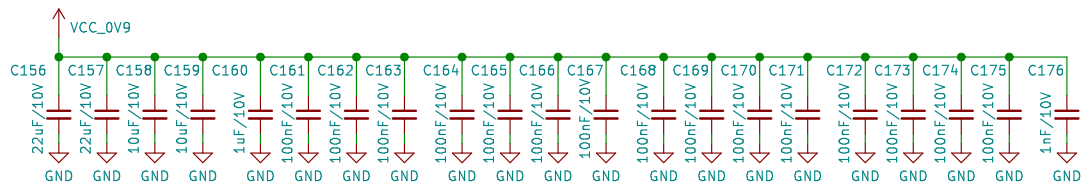
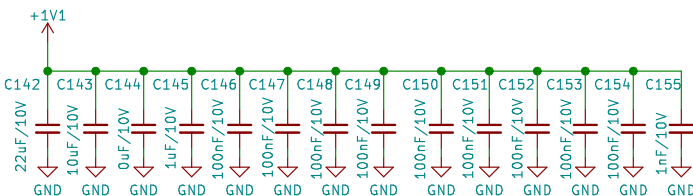
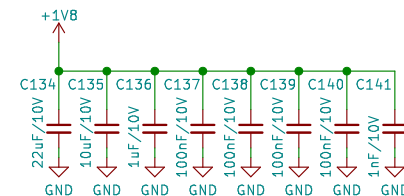
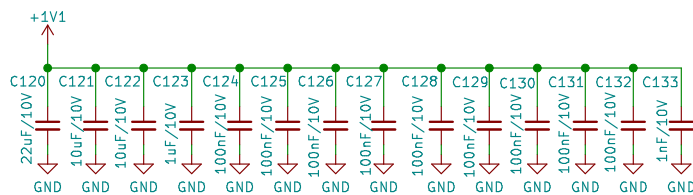
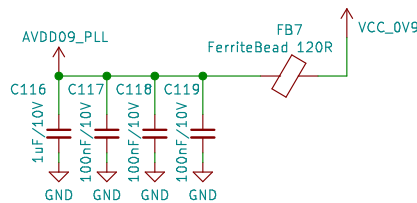
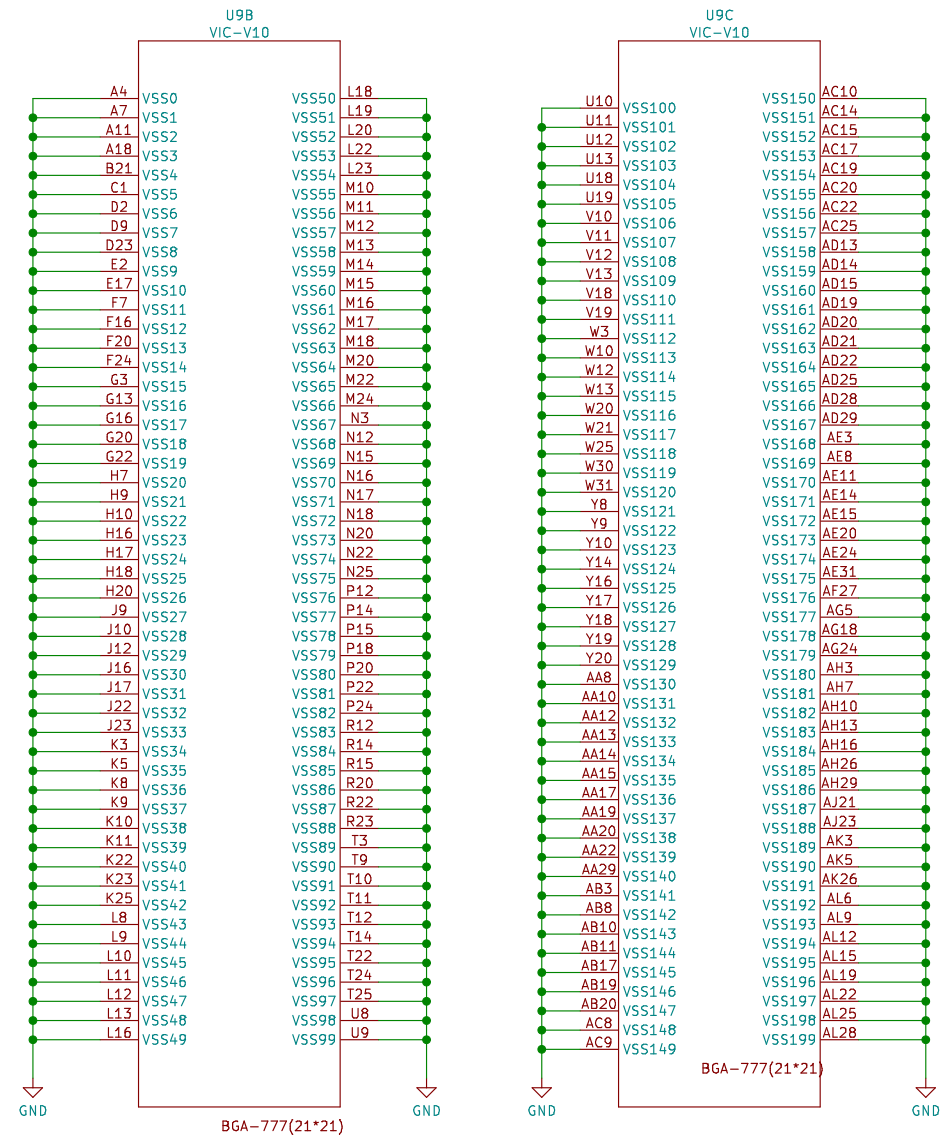
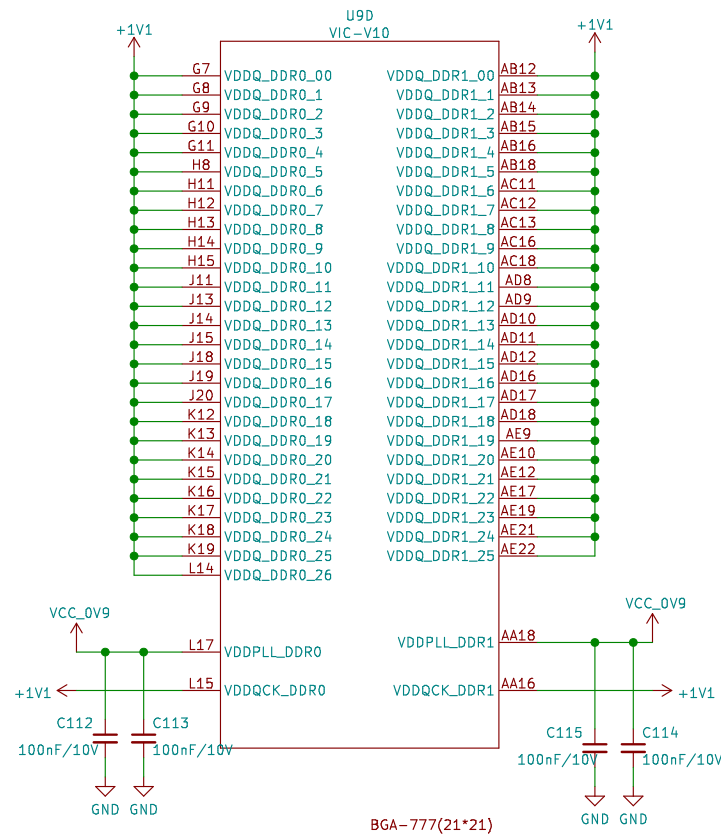
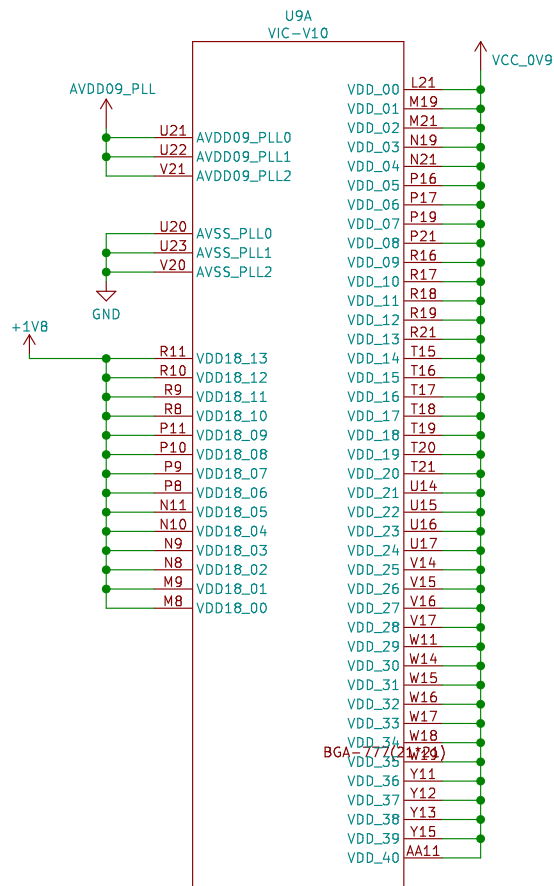


1	2	3	4	5	6		
A	<div>BeagleV PMIC</div> <div></div> <div>File: BeagleV_PMIC.kicad_sch</div>	<div>VIC Power</div> <div></div> <div>File: VIC_Power.kicad_sch</div>	<div>BeagleV VIC DDR Ctrl</div> <div></div> <div>File: BeagleV_VIC_DDR_Ctrl.kicad_sch</div>	<div>BeagleV VIC ChipLink &amp; CM</div> <div></div> <div>File: BeagleV_VIC_ChipLink.kicad_sch</div>	<div>BeagleV VIC LCD &amp; GMI &amp; GPIOs</div> <div></div> <div>File: BeagleV_VIC_LCD.kicad_sch</div>	<div>BeagleV HighIF &amp; Ctrl Other</div> <div></div> <div>File: BeagleV_VIC_HighIF.kicad_sch</div>	A
B	<div>BeagleV LPDDR (A)</div> <div></div> <div>File: BeagleV_LPDDR_A.kicad_sch</div>	<div>BeagleV LPDDR (B)</div> <div></div> <div>File: BeagleV_LPDDR_B.kicad_sch</div>	<div>BeagleV Type C, uSD, QSIP Flash</div> <div></div> <div>File: BeagleV_Type_C_uSD.kicad_sch</div>	<div>BeagleVUSB 3.0 HUB</div> <div></div> <div>File: BeagleV_USB_3_HUB.kicad_sch</div>	<div>BeagleV USB TYPE A (A)</div> <div></div> <div>File: BeagleV_USB_Type_A_A.kicad_sch</div>	<div>BeagleV USB TYPE A (B)</div> <div></div> <div>File: BeagleV_USB_Type_A_B.kicad_sch</div>	B
C	<div>BeagleV CSI CONN</div> <div></div> <div>File: BeagleV_CSI_Conn.kicad_sch</div>	<div>BeagleV DSI, AUDIO JACK</div> <div></div> <div>File: BeagleV_DSI_Audio.kicad_sch</div>	<div>BeagleV HDMI FRAMER</div> <div></div> <div>File: BeagleV_Hdmi_Framer.kicad_sch</div>	<div>BeagleV W iFi, Bluetooth</div> <div></div> <div>File: BeagleV_Wifi_Bluetooth.kicad_sch</div>	<div>BeagleV 10/100/1000 ETHERNET</div> <div></div> <div>File: BeagleV_Ethernet.kicad_sch</div>	<div>BeagleV RPI EXP CONN, LED, BUTTON</div> <div></div> <div>File: BeagleV_RPI_Conn.kicad_sch</div>	C
D	<div></div> <div>Sheet: / File: BeagleV.kicad_sch</div> <div><b>Title: Beagle V</b></div> <div>Size: A4      Date: 2021-06-11      <b>Rev: v0.7</b></div> <div>KiCad E.D.A.   kicad 5.99.0-unknown-a6c3d74a73125ubuntu20.04.1      Id: 1/19</div>					D	
1	2	3	4	5	6		

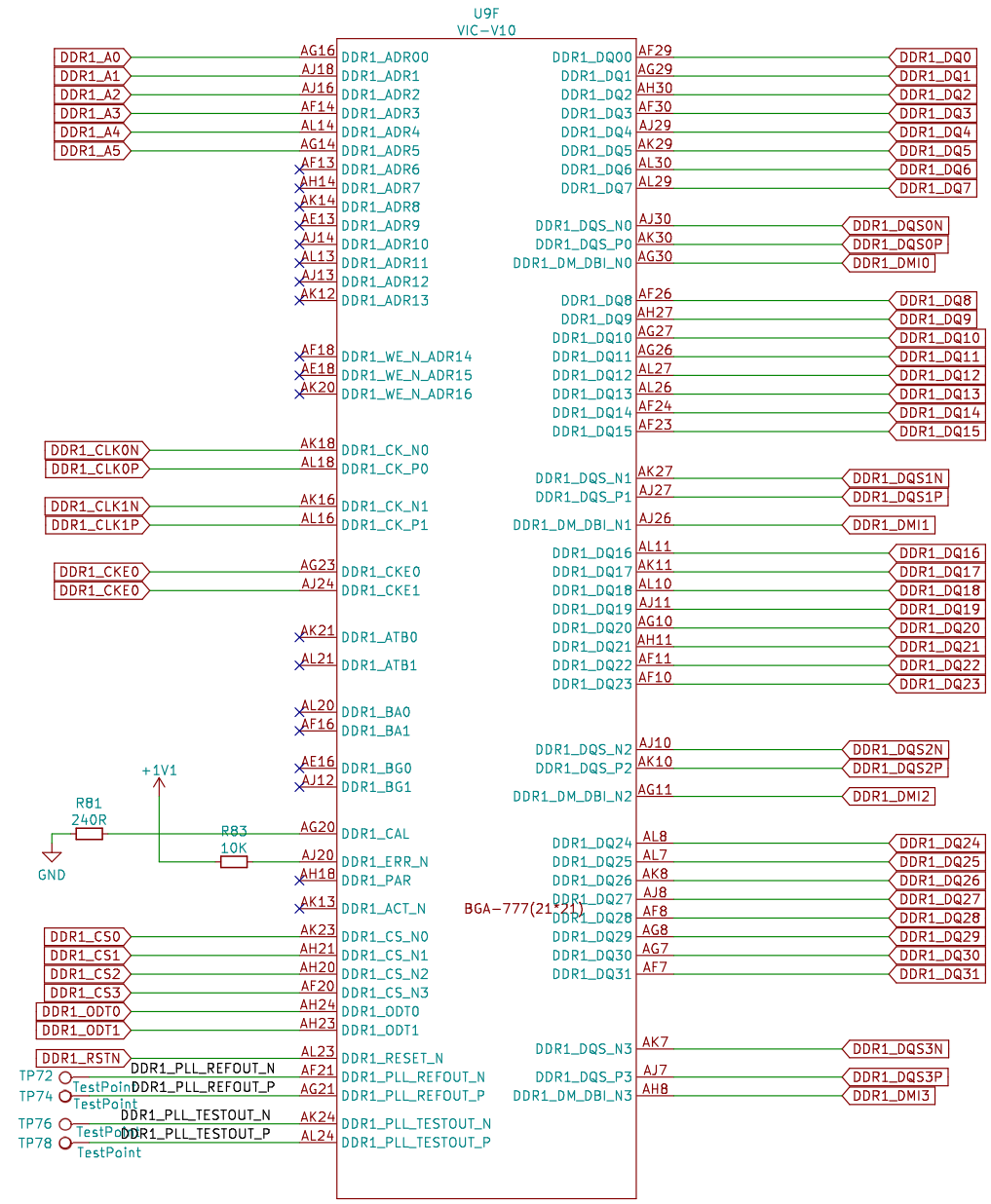
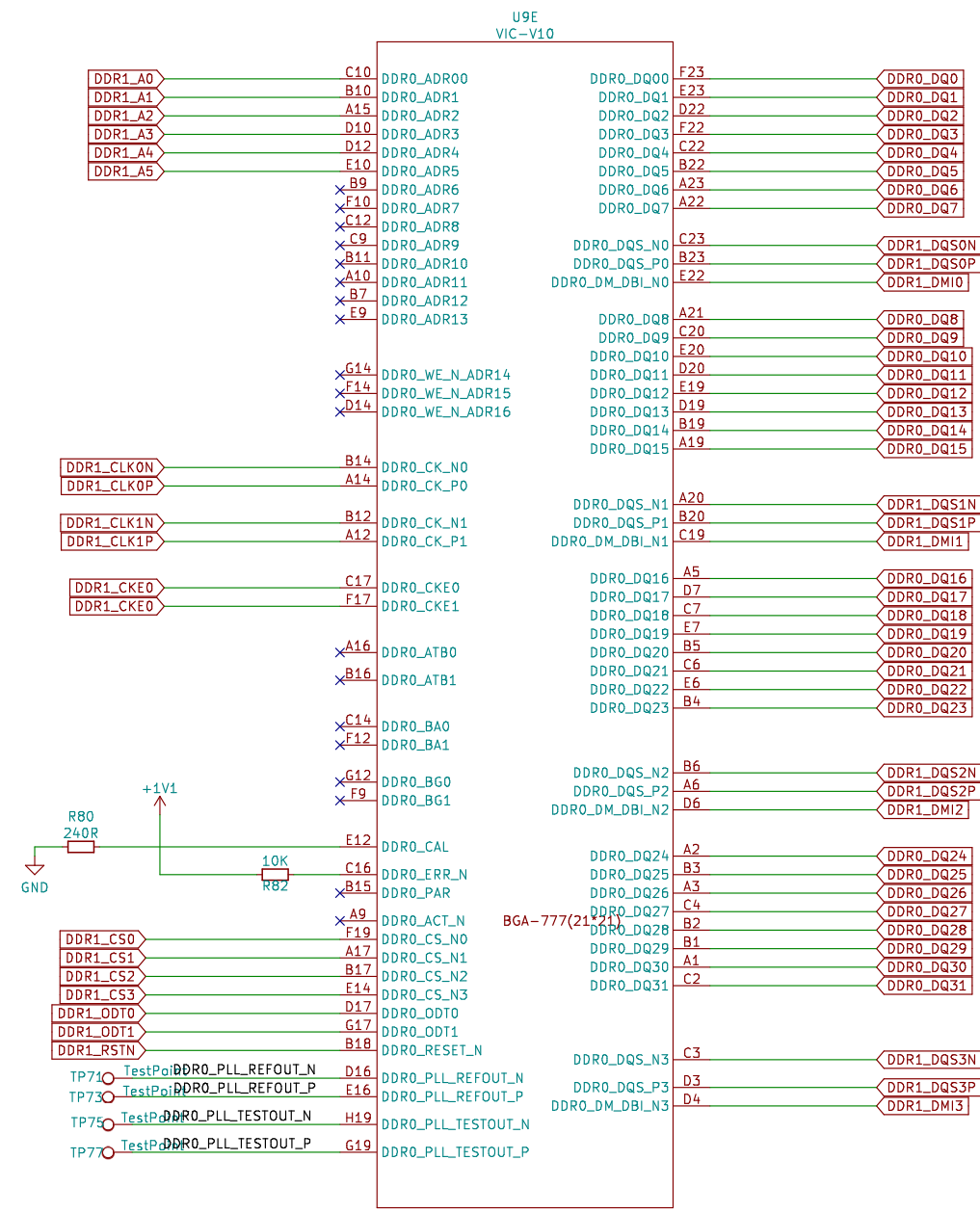
The schematic diagram illustrates the LMR62014 buck converter circuit. The input is a +5V supply connected to the VIN pin of the LMR62014 (U13) through a 10uF capacitor (C303) and a 1M resistor (R263). The SW pin is connected to the inductor L6 (10uH), which is in series with the PMEG3010CEJ diode. The FB pin is connected to the output voltage divider, consisting of a 49.9K resistor (R261) and a 13.3K resistor (R264). The SHDN pin is connected to ground. The output is filtered by a 1nF/50V capacitor (C302) and a 10uF/10V capacitor (C304). The output voltage is VSYS\_5v8.



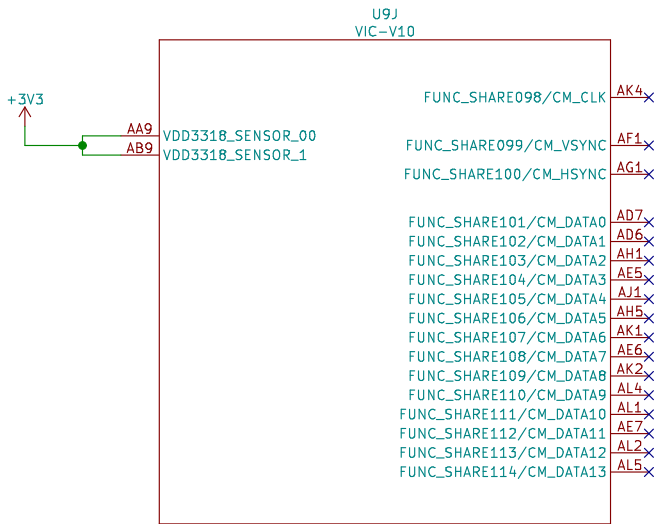
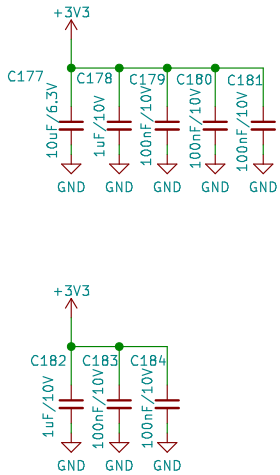
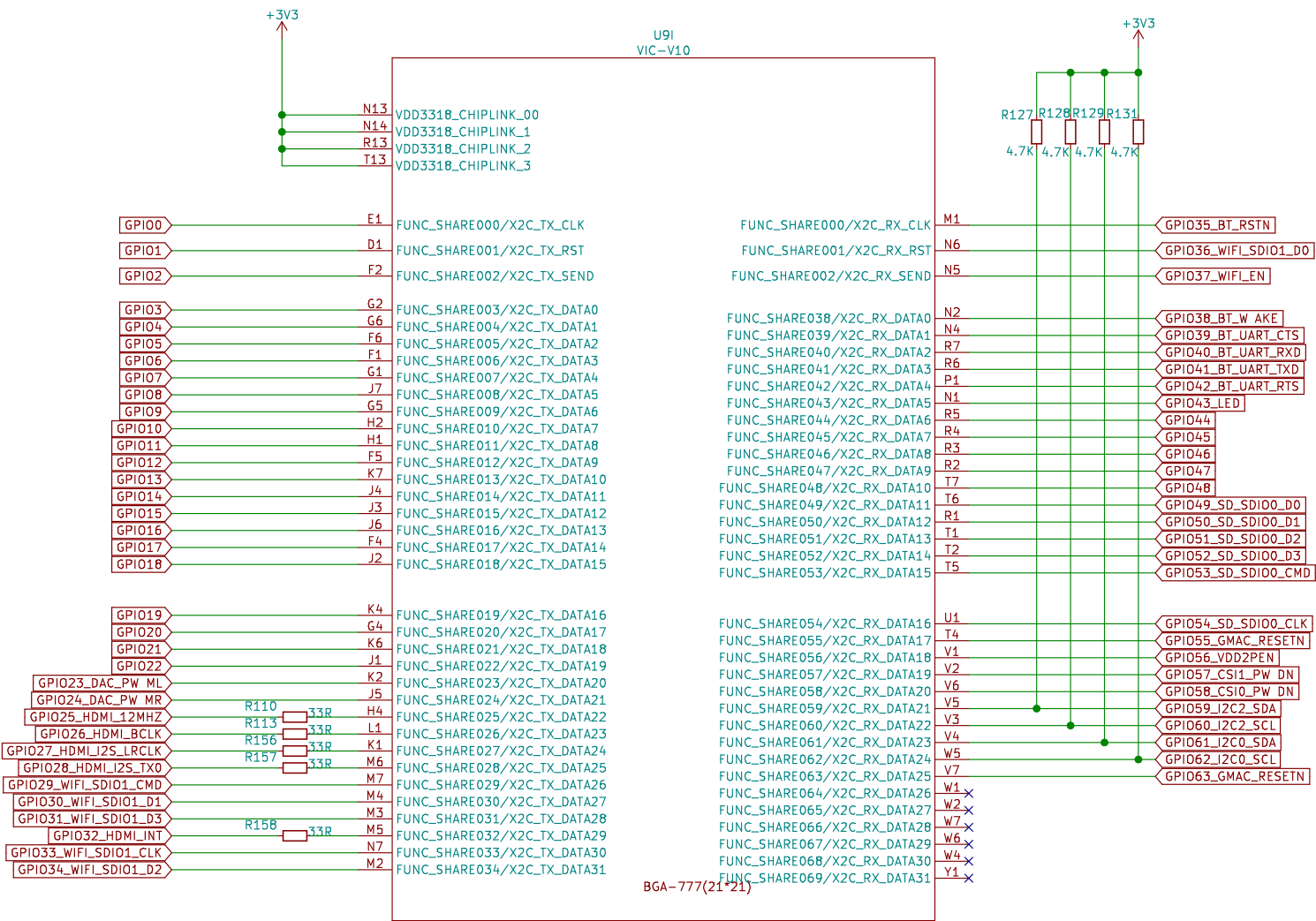
# VIC Power



# VIC DDR controller



VIC 3.3V GPIOs



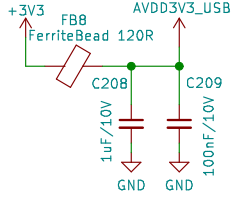
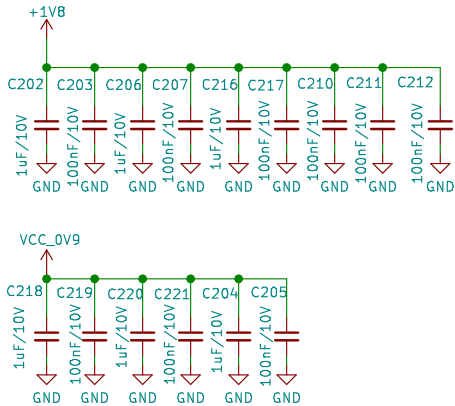
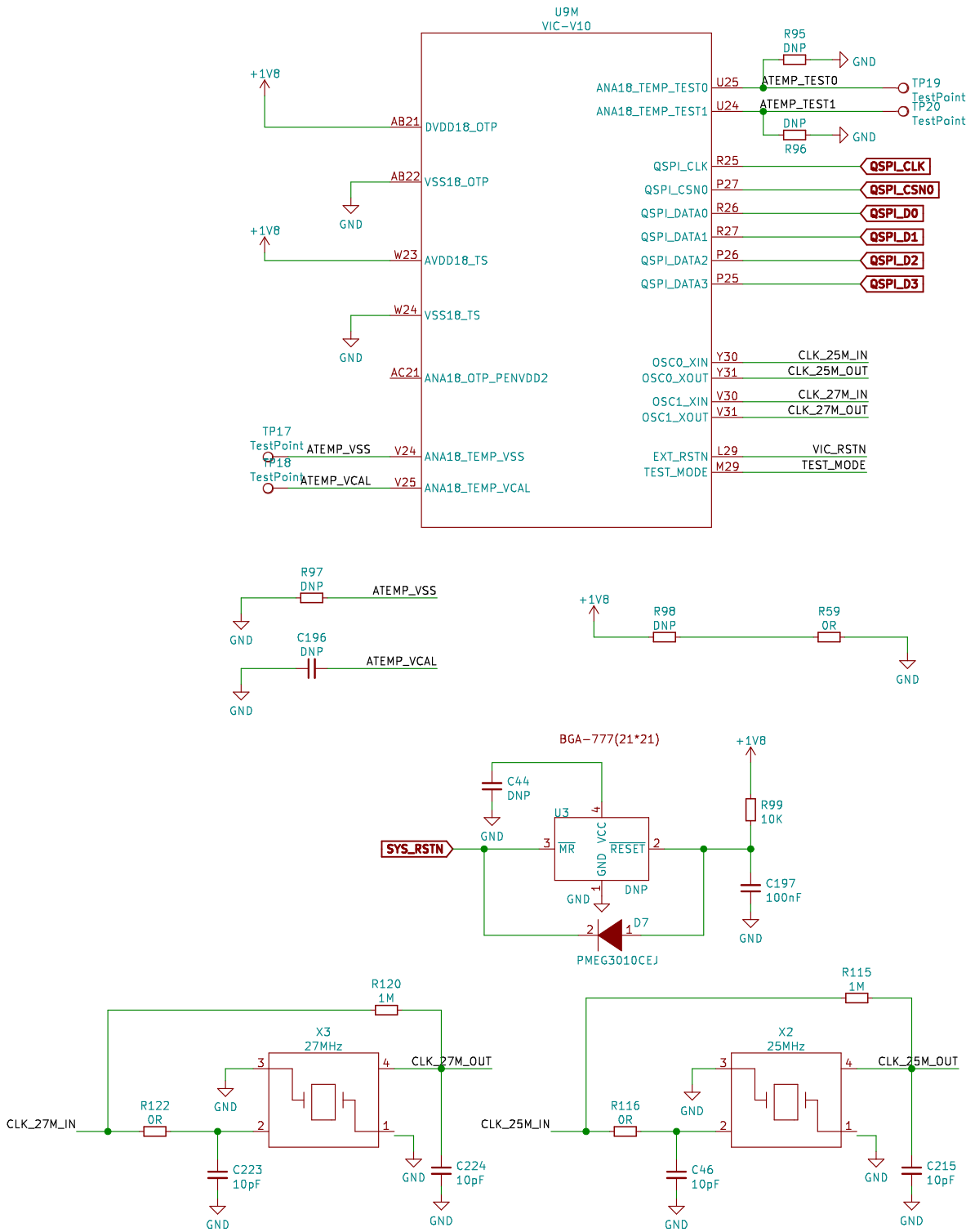
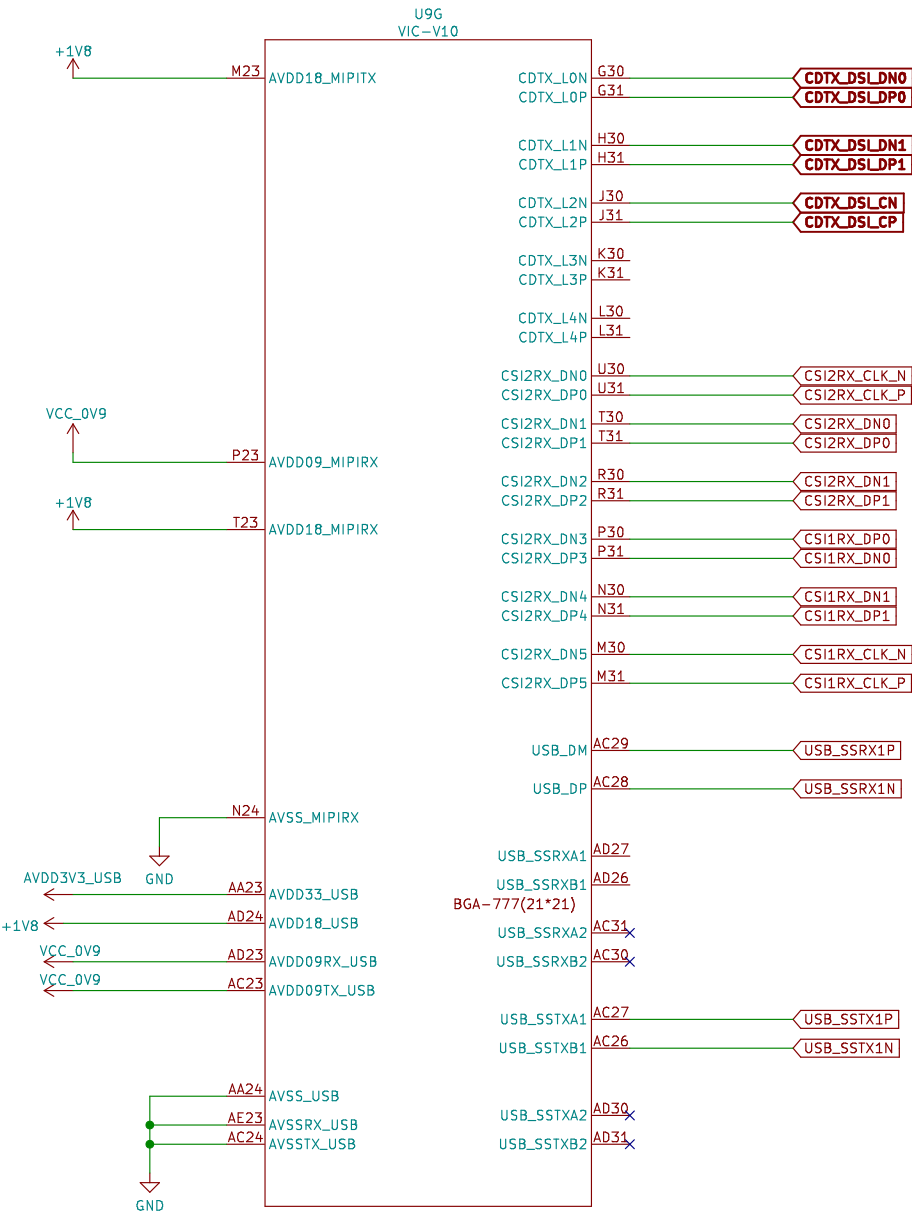
# VIC LCD&RGMII



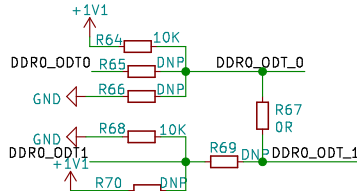
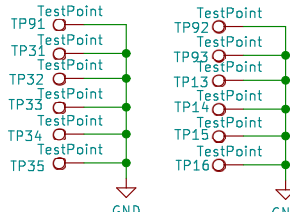
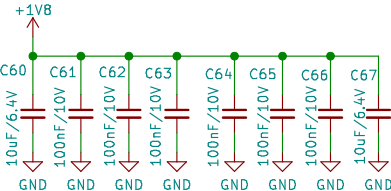
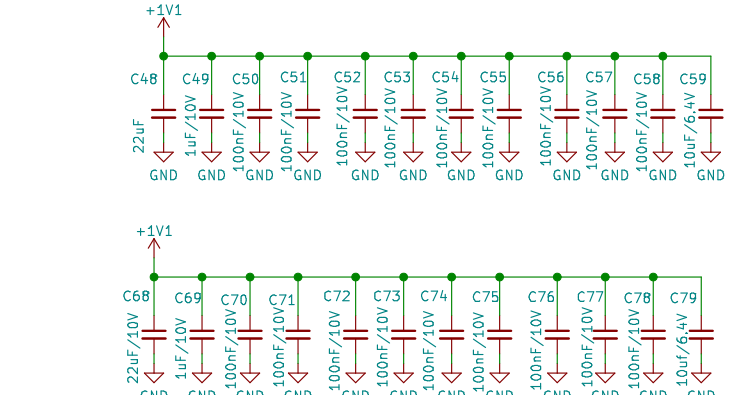
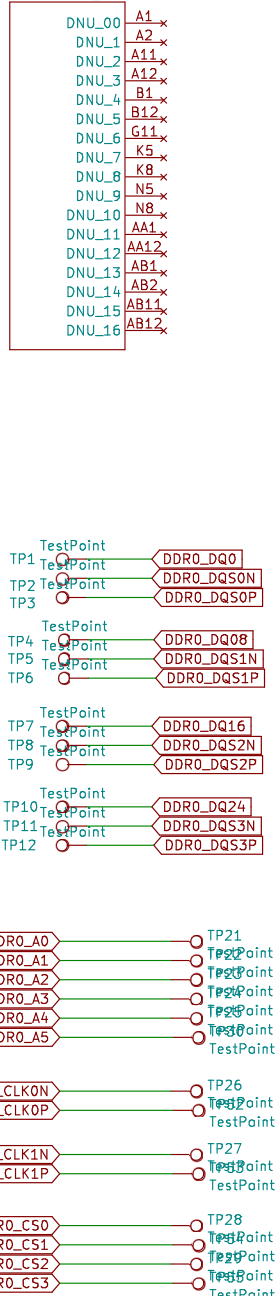
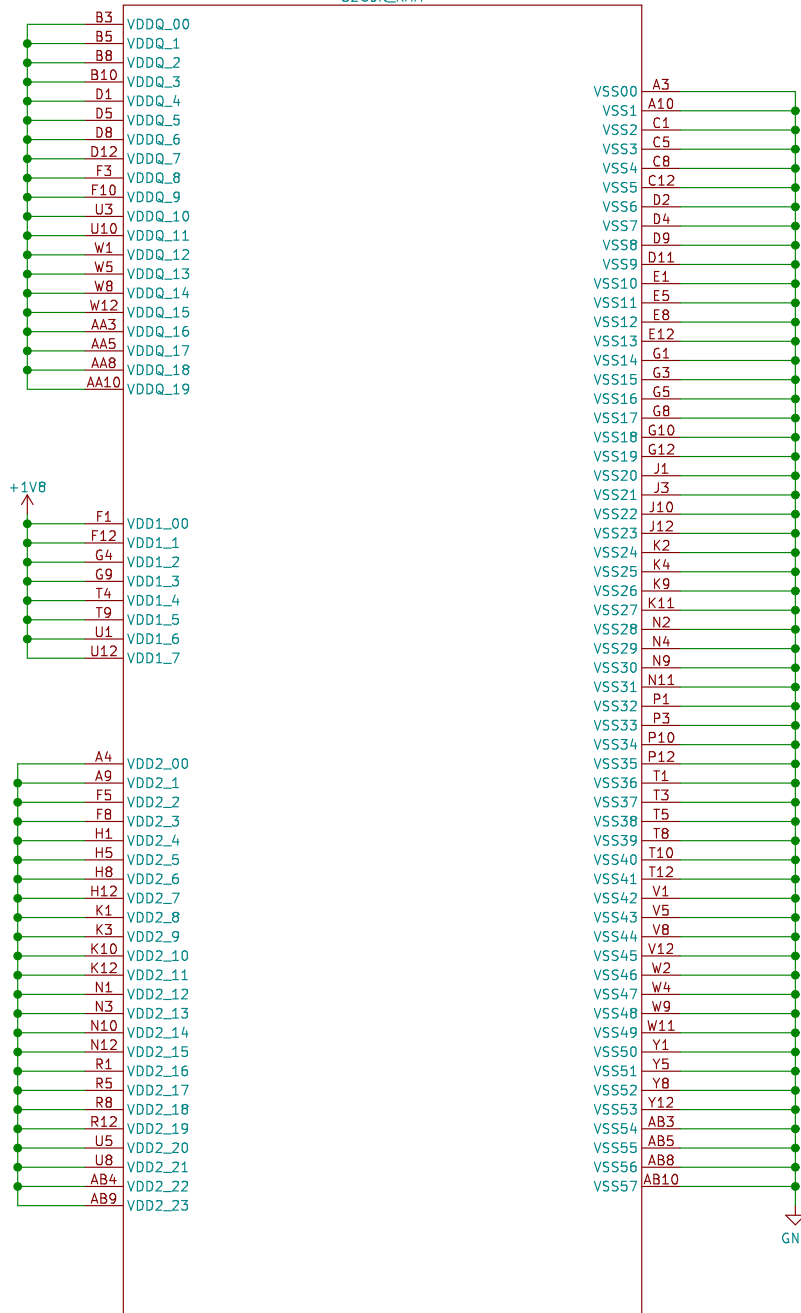
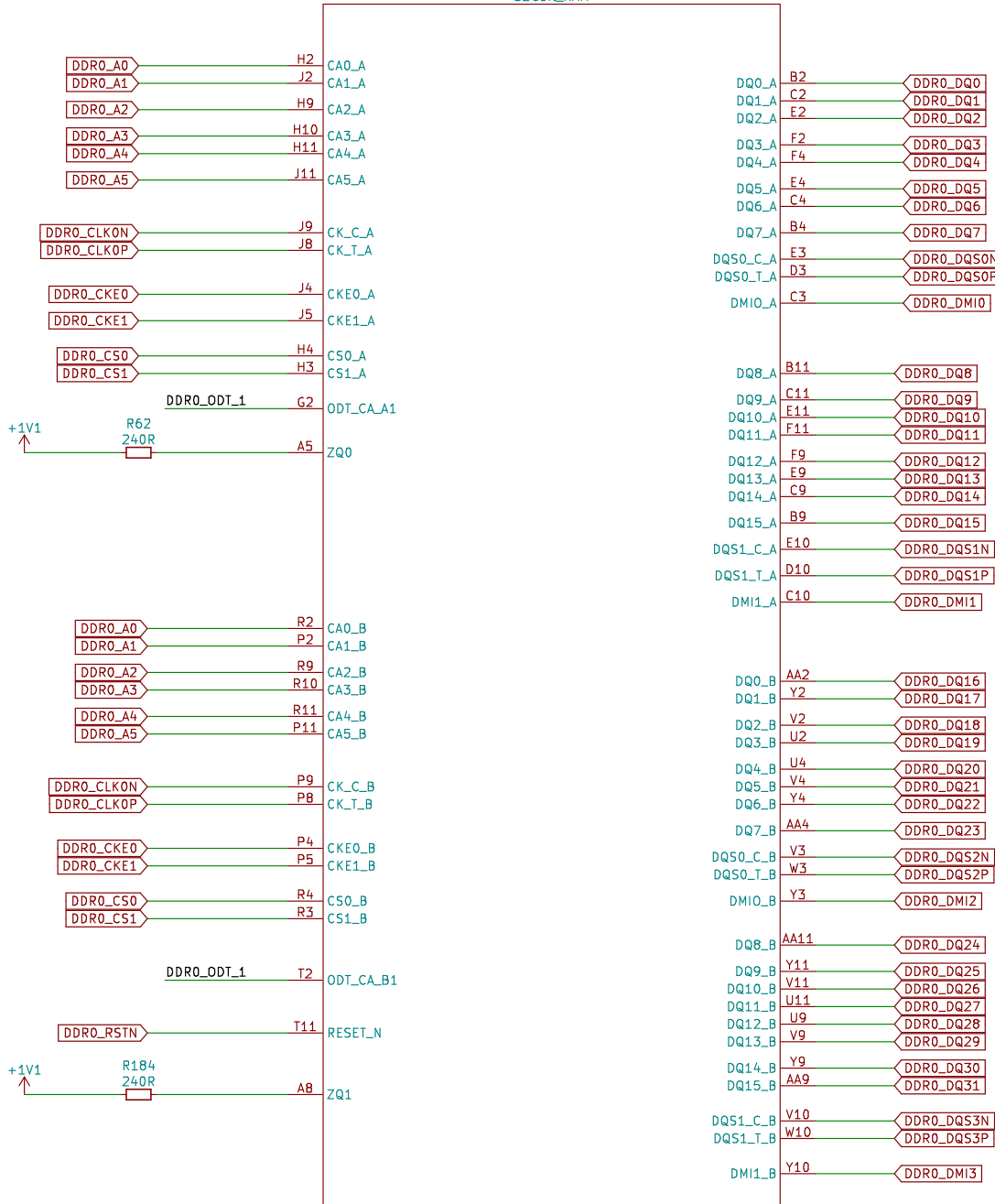


VIC MIPI&USB

VIC CLOCK

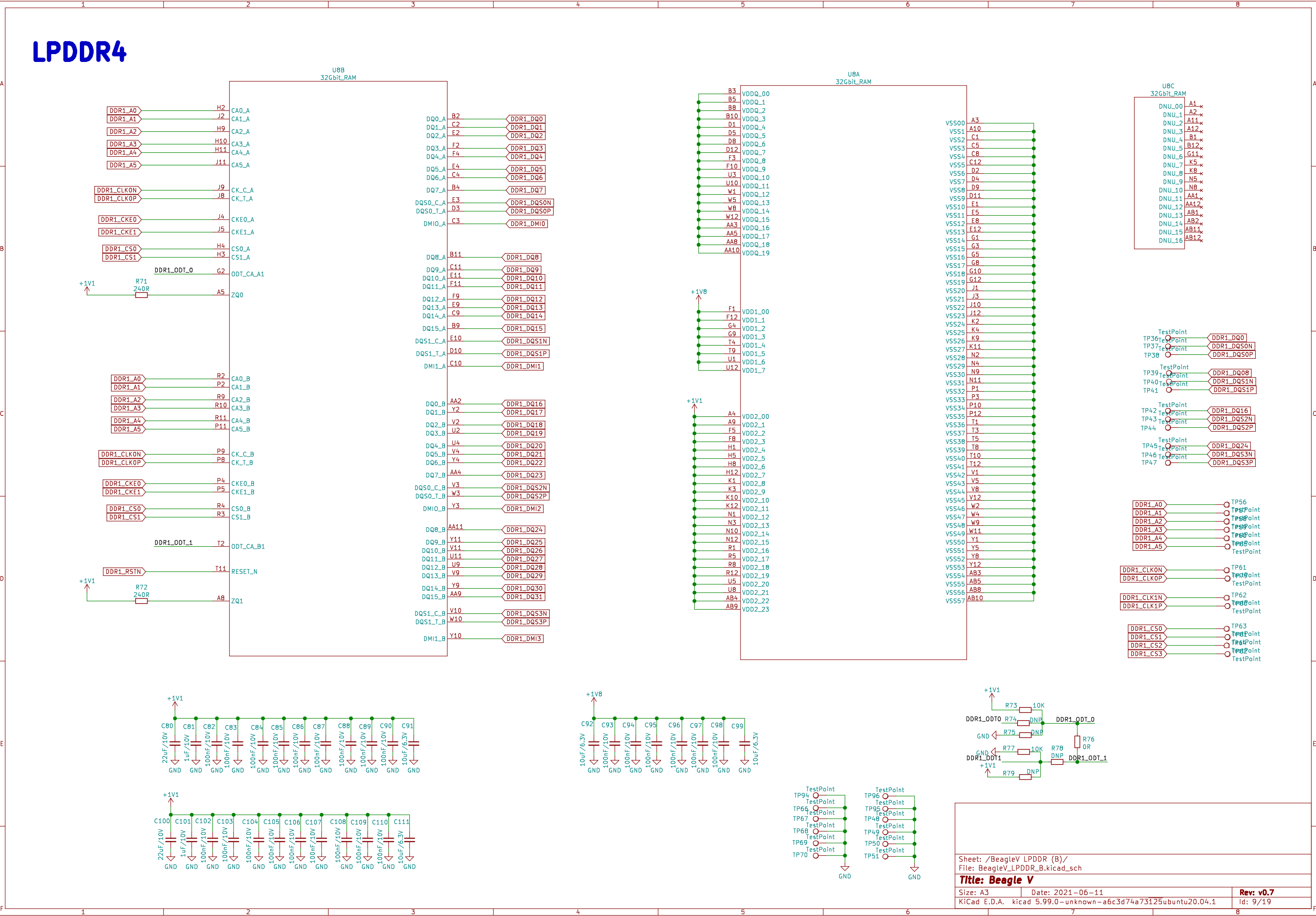


## LPDDR4

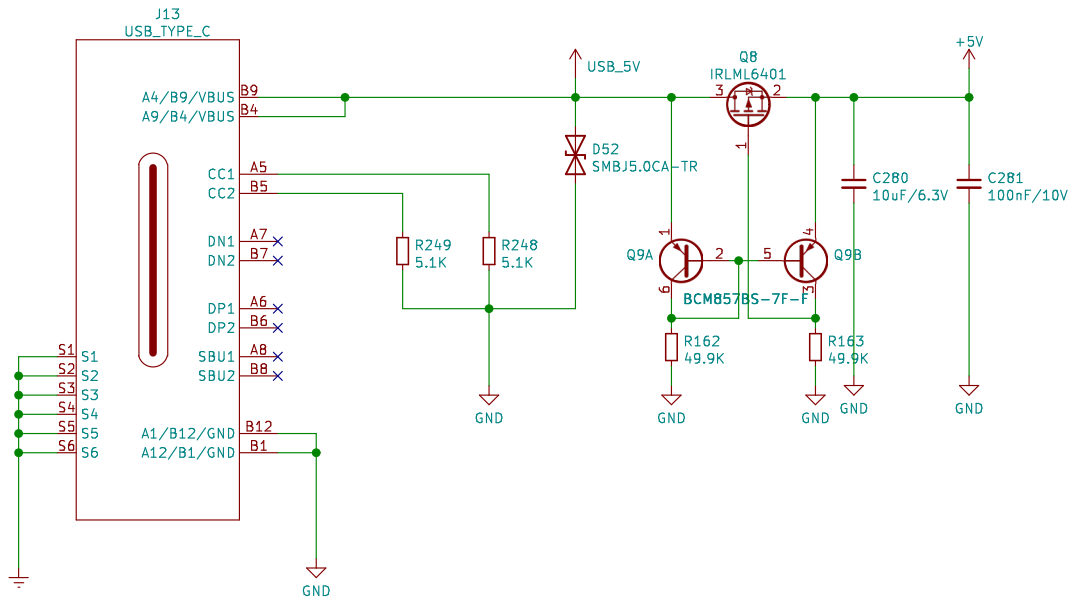




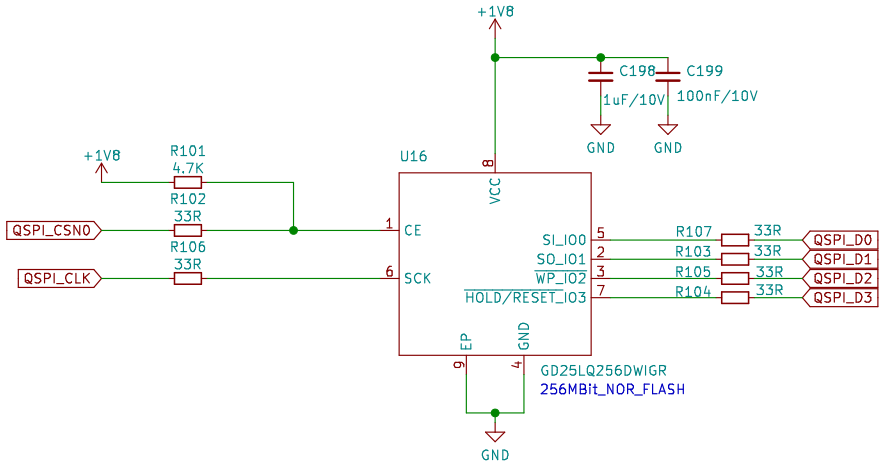
LPDDR4



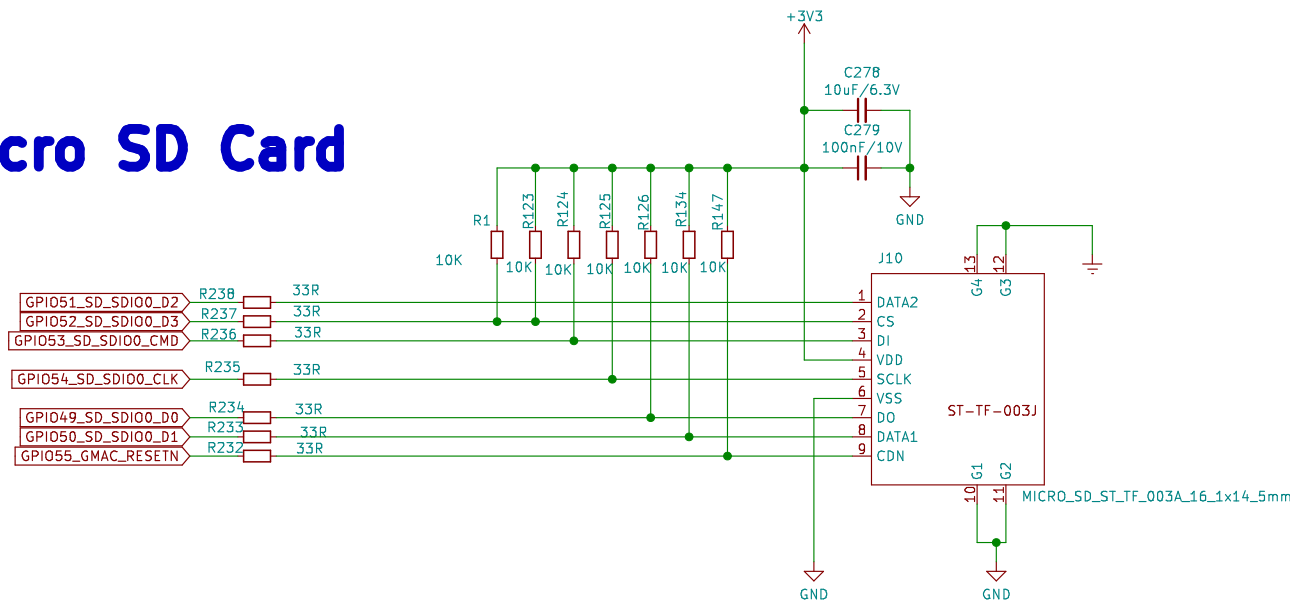
# USB Type-C Power



# Nor Flash For Boot



# Micro SD Card



Sheet: /BeagleV Type C, uSD, QSPI Flash/  
File: BeagleV\_Type\_C\_uSD.kicad\_sch

**Title: Beagle V**

Size: A3	Date: 2021-06-11	Rev: v0.7
KiCad E.D.A. kicad 5.99.0-unknown-a6c3d74a73125ubuntu20.04.1		Id: 10/19

USB 3.0 HUB  
TBD

The schematic diagram illustrates a USB 3.0 Hub (TBD) circuit. It features two USB 3.0 controllers, U1A and U1B, both of the GL3520-0S322 model. The circuit is powered by a +5V supply and includes a +1V2 supply. Key components include:

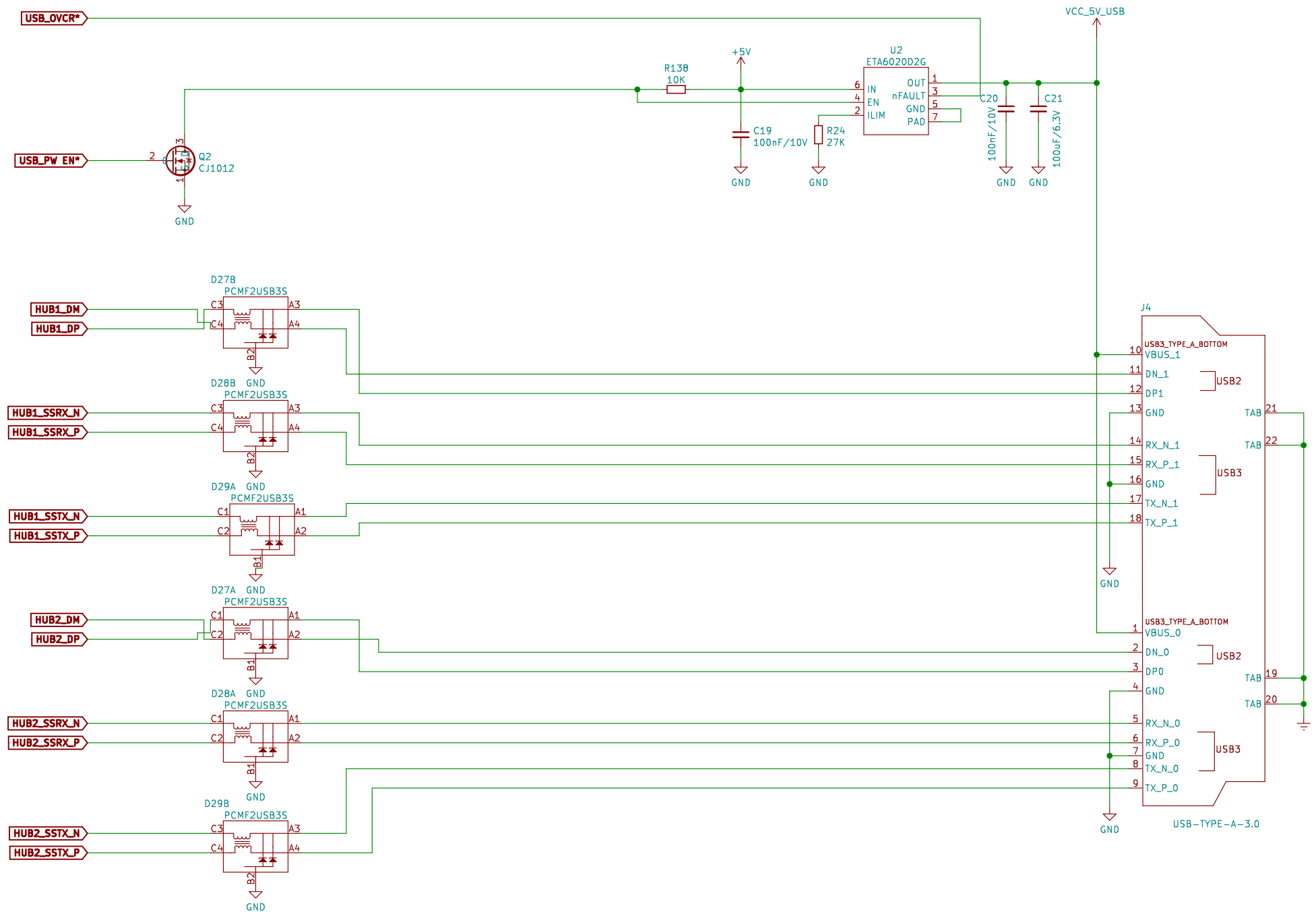
- Power Supply:** +5V and +1V2 supplies are connected to the circuit. A 10uF/6.3V capacitor (C10) is connected to the +1V2 supply. A 10uF/6.3V capacitor (C22) is connected to the +5V supply.
- USB 3.0 Controllers:** U1A and U1B are connected to the power supply and signal lines. U1A is connected to the +5V supply and signal lines. U1B is connected to the +5V supply and signal lines.
- Signal Lines:** The circuit includes signal lines for USB 3.0 data (DP0, DM0, TXP\_UP, TXN\_UP, RXP\_UP, RXN\_UP, TXP\_DS1, TXN\_DS1, RXP\_DS1, RXN\_DS1, TXP\_DS2, TXN\_DS2, RXP\_DS2, RXN\_DS2, TXP\_DS3, TXN\_DS3, RXP\_DS3, RXN\_DS3, TXP\_DS4, TXN\_DS4, RXP\_DS4, RXN\_DS4) and USB 3.0 power (V33, VBUS, DVDD, AVDD, NC1, NC2, NC3, NC4, NC5, NC6, PAD).
- Passive Components:** Various capacitors (C10, C16, C17, C18, C19, C20, C21, C22, C23, C24, C25, C26, C27, C28, C29, C30, C31, C32, C33, C34, C35, C36, C37, C38, C39, C40, C41, C42, C43, C44, C45, C46, C47, C48, C49, C50, C51, C52, C53, C54, C55, C56, C57, C58, C59, C60, C61, C62, C63, C64, C65, C66, C67, C68, C69, C70, C71, C72, C73, C74, C75, C76, C77, C78, C79, C80, C81, C82, C83, C84, C85, C86, C87, C88, C89, C90, C91, C92, C93, C94, C95, C96, C97, C98, C99, C100) and resistors (R143, R144, R145, R146, R147, R148, R149, R150, R151, R152, R153, R154, R155, R156, R157, R158, R159, R160, R161, R162, R163, R164, R165, R166, R167, R168, R169, R170, R171, R172, R173, R174, R175, R176, R177, R178, R179, R180, R181, R182, R183, R184, R185, R186, R187, R188, R189, R190, R191, R192, R193, R194, R195, R196, R197, R198, R199, R200) are used for signal conditioning and power regulation.

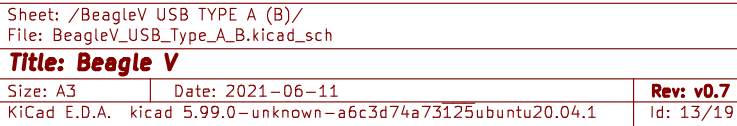
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**Title: Beagle V**

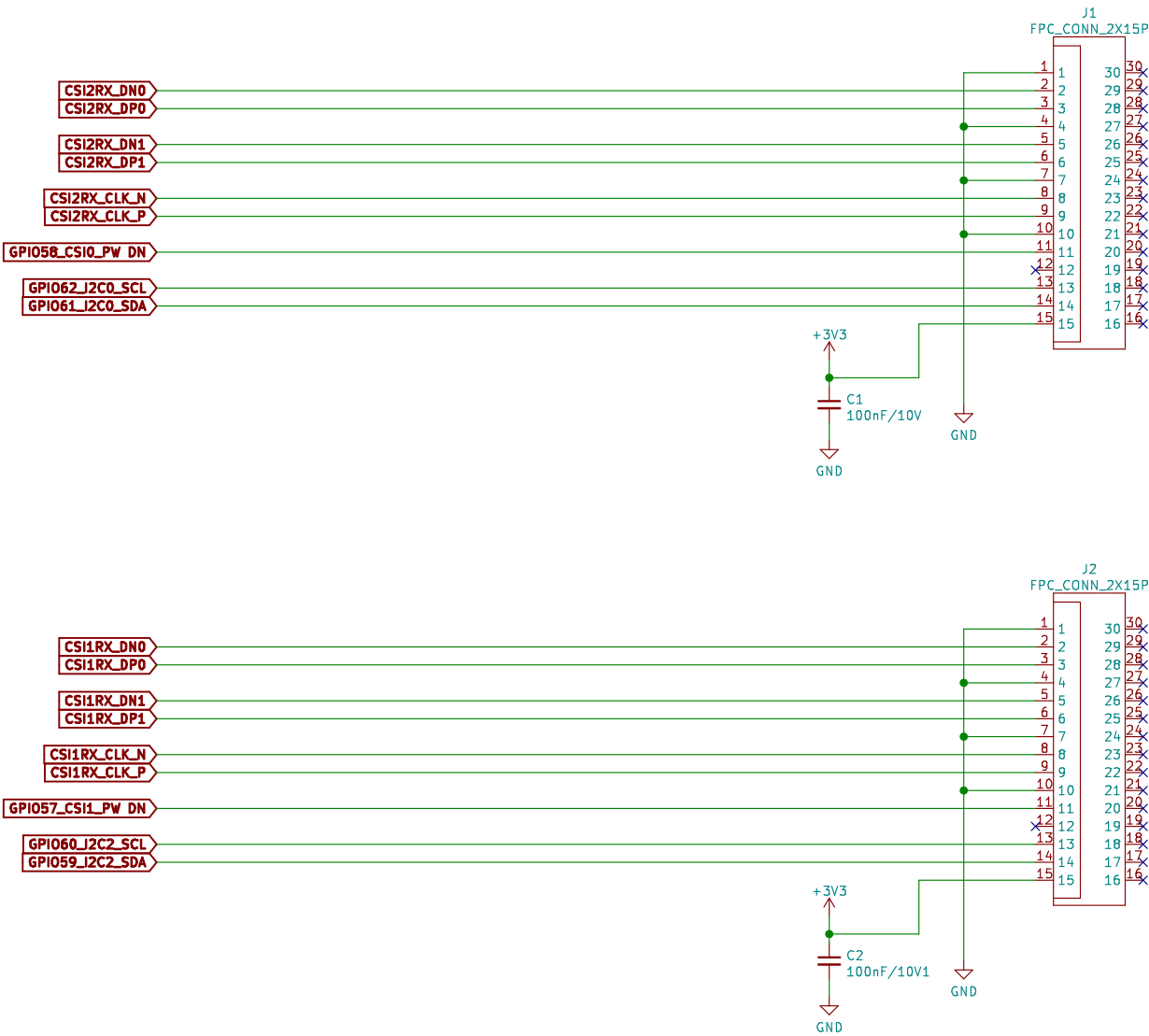
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Size: A3	Date: 2021-06-11	Rev: v0.7
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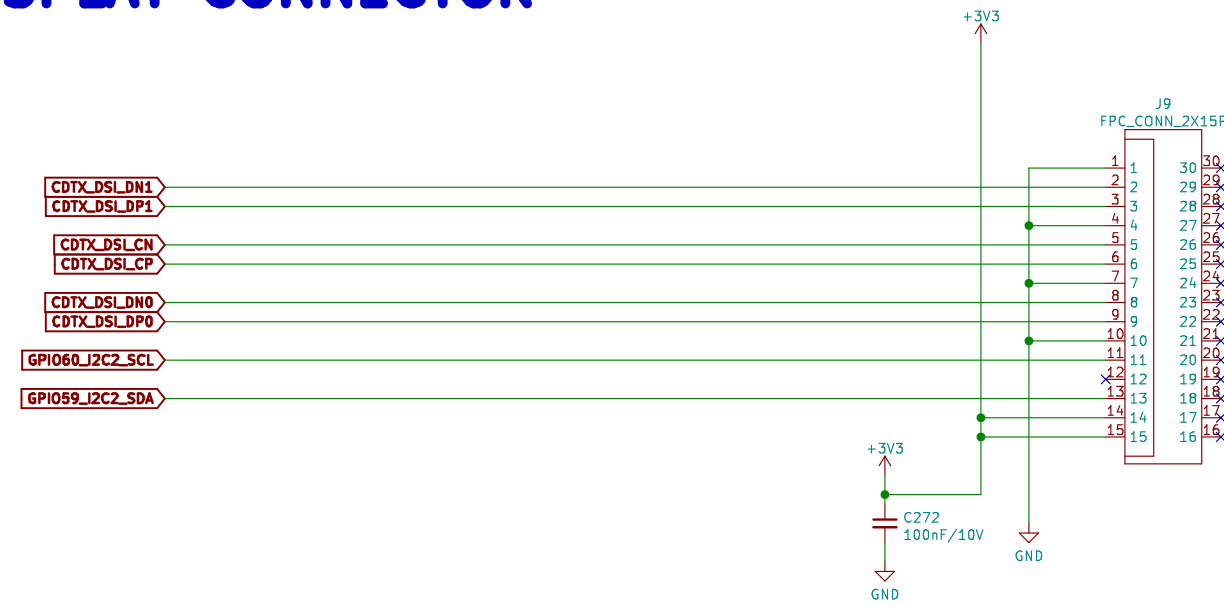


CAMERA CONNECTOR

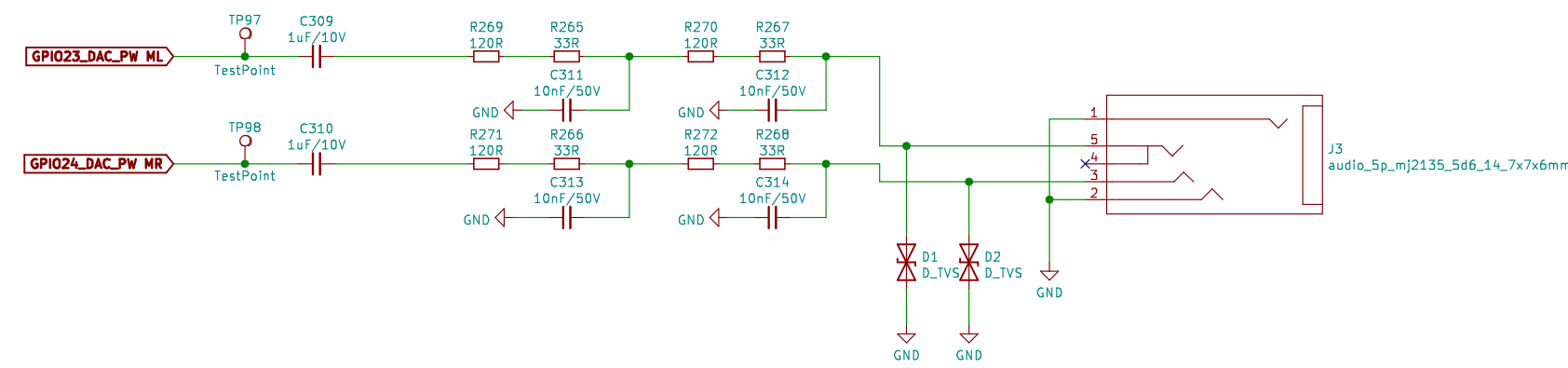




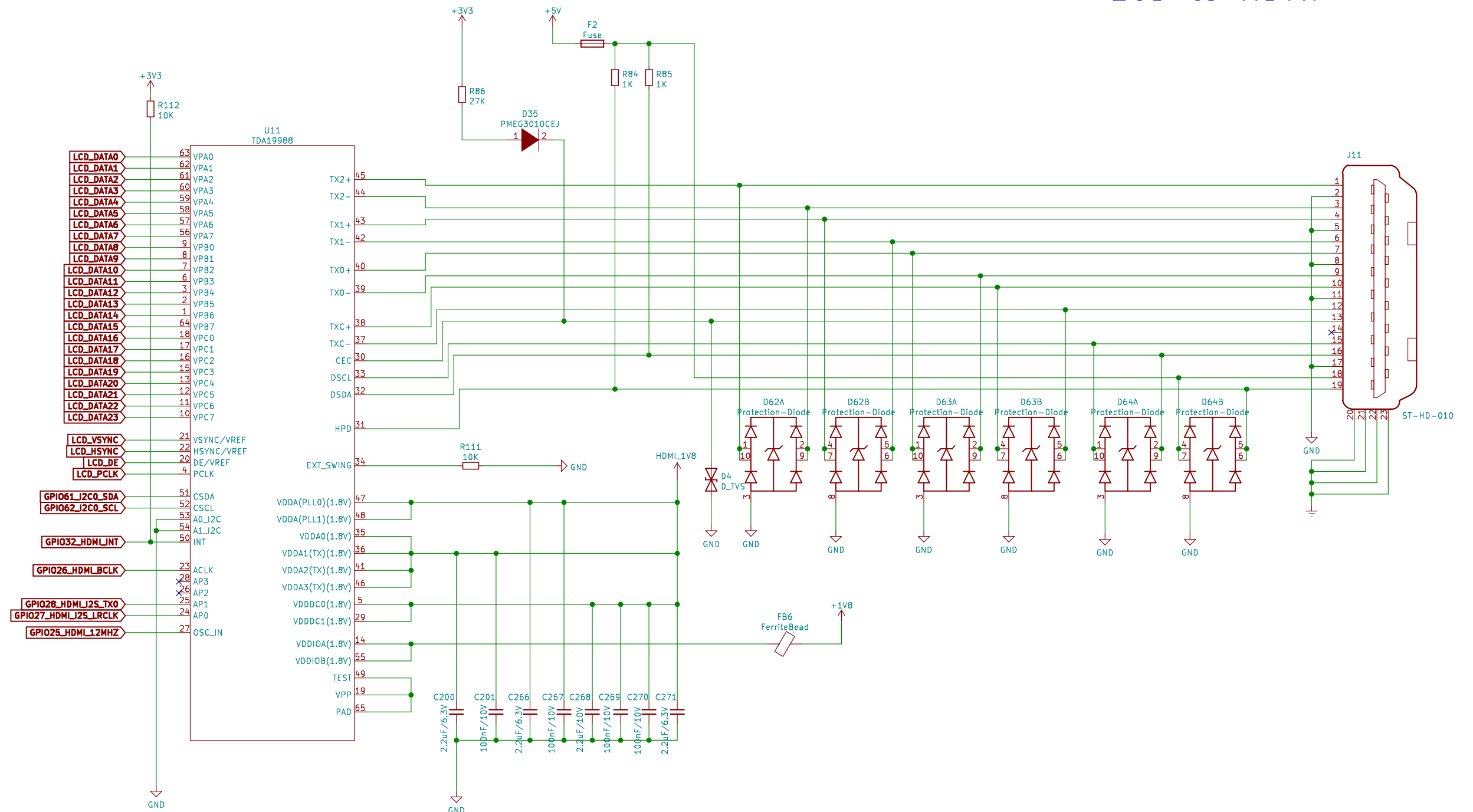
# DISPLAY CONNECTOR



# AUDIO



## LCD to HDMI

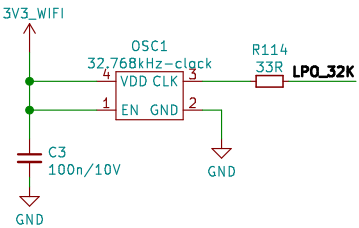
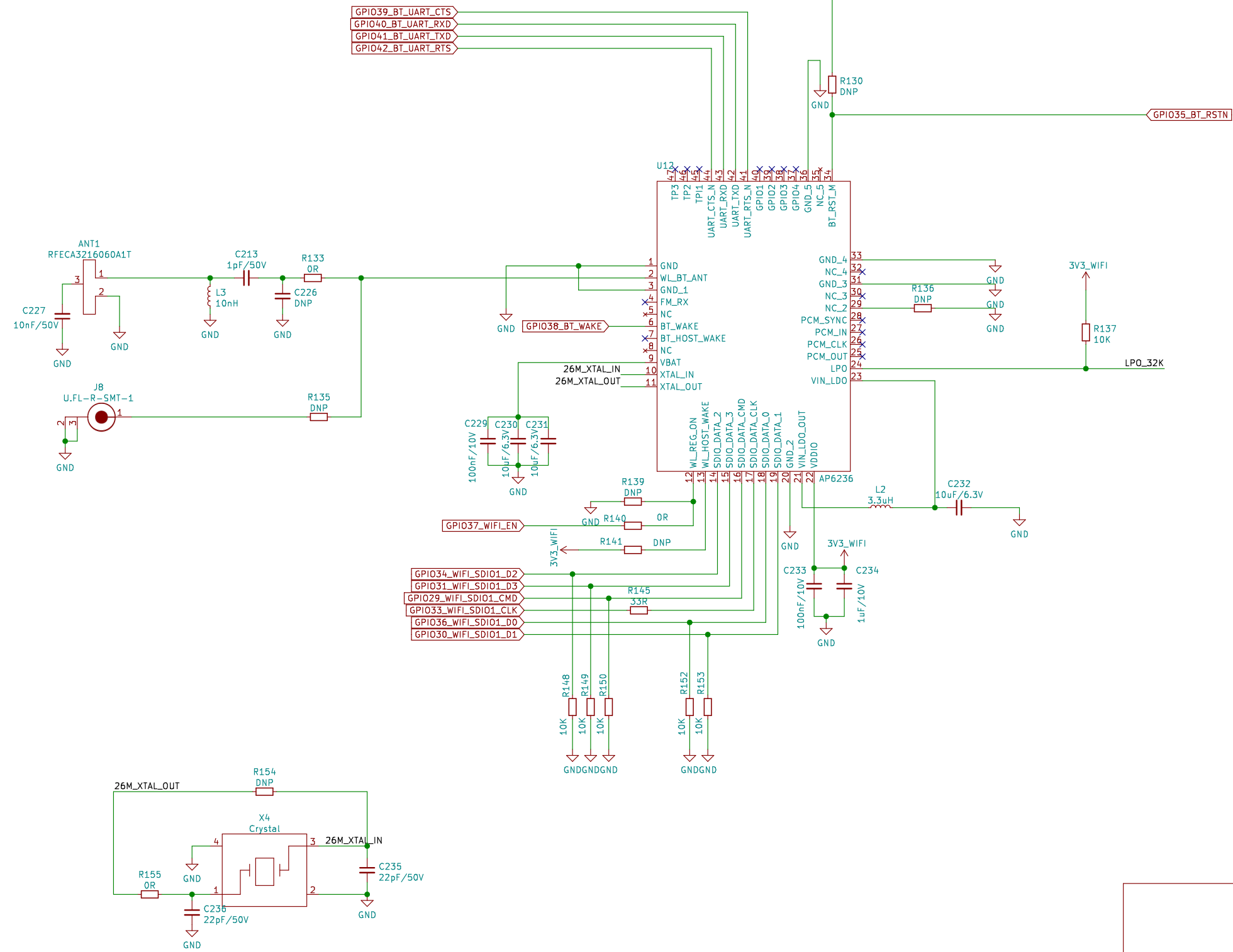
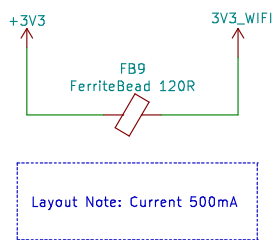


Sheet: /BeagleV HDMI FRAMER/  
File: BeagleV\_Hdmi\_Framer.kicad\_sch

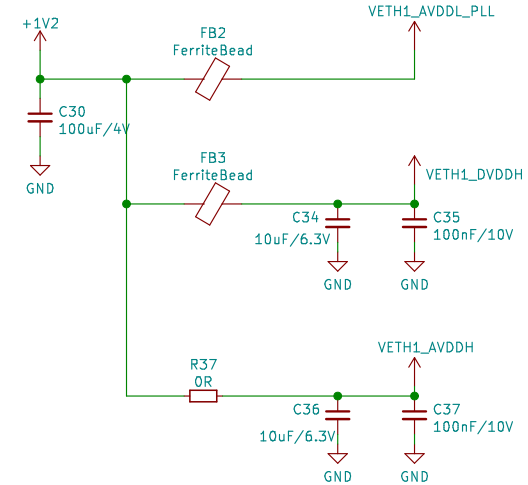
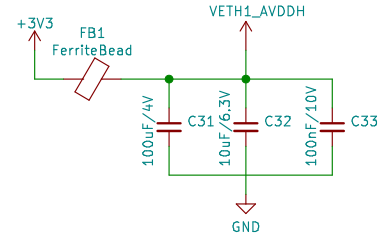
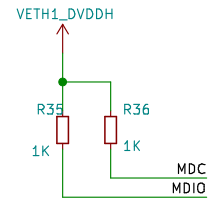
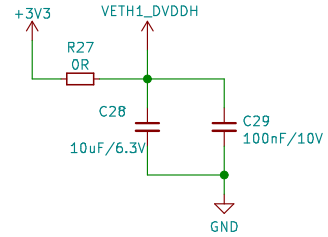
**Title: Beagle V**

Size: A3	Date: 2021-06-11	Rev: v0.7
KiCad E.D.A. kicad 5.99.0-unknown-a6c3d74a73125ubuntu20.04.1		Id: 16/19

# WiFi & Bluetooth



# Gigabit Ethernet

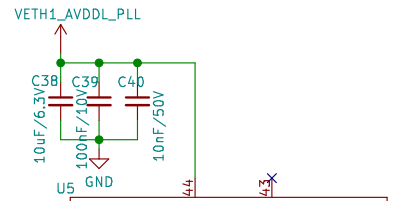


**Layout Note:**

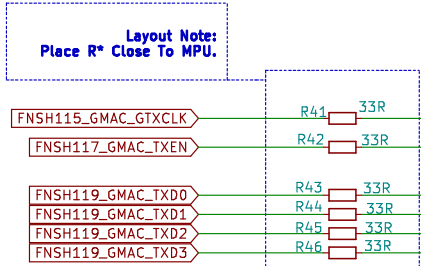
**The RGMII1 signals must be length-matched by TX and RX groups:**

That is, the TX group should be matched within 300 Mil (7.62 mm), and the RX group should be matched within 300 Mil (7.62 mm). Total length should not exceed 1750 Mil (44.5 mm).

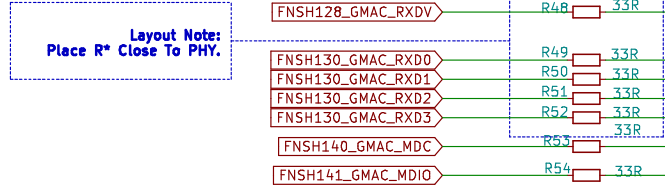
There is no requirement to match the TX and RX groups because their clocks are not related.



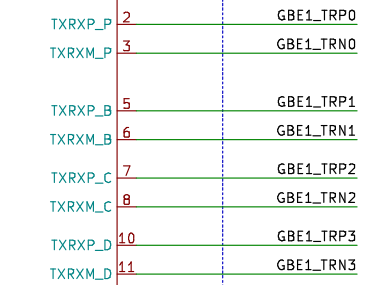
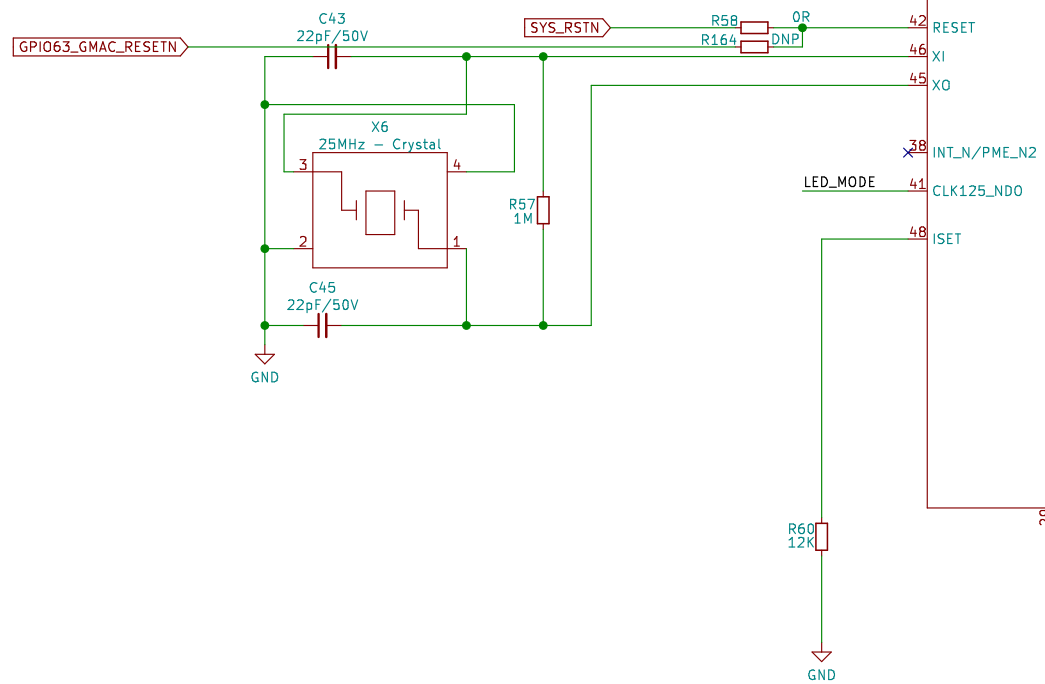
**Review Note:**  
The KSZ9031RX reduces board cost and simplifies board layout by using on-chip termination resistors for the four differential pairs, so no other resistors are needed.



**Layout Note:**  
**Place R\* Close To MPU.**

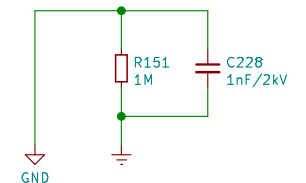
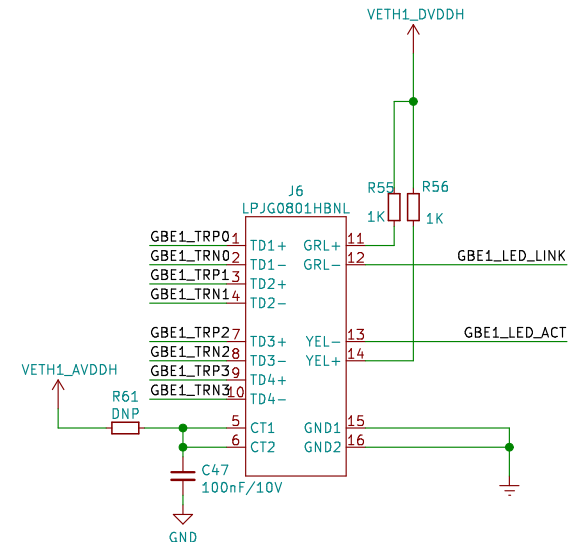
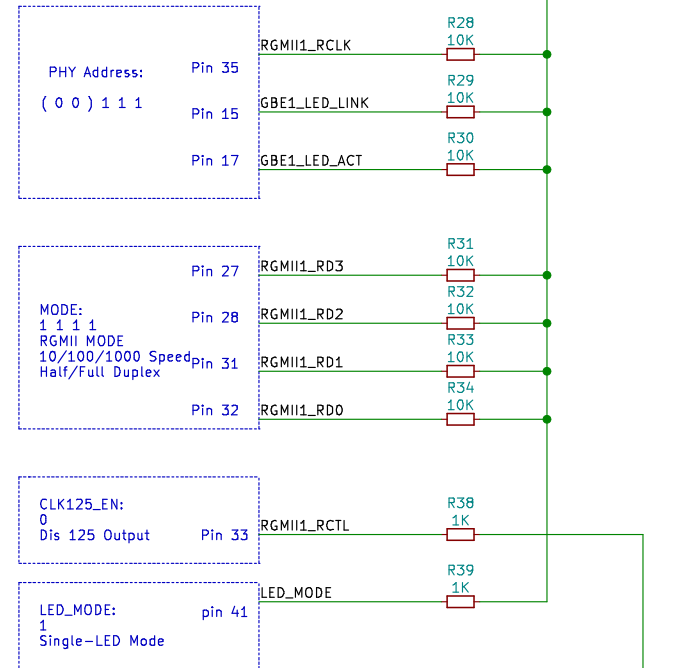


**Layout Note:**  
**Place R\* Close To PHY.**



Layout Note:

Max trace-length mismatch between GBE signals pairs should be no greater than ?  
100 ohms differential trace impedance.



Sheet: /BeagleV 10/100/1000 ETHERNET/  
File: BeagleV\_Ethernet.kicad\_sch

**Title: Beagle V**

Size: A3

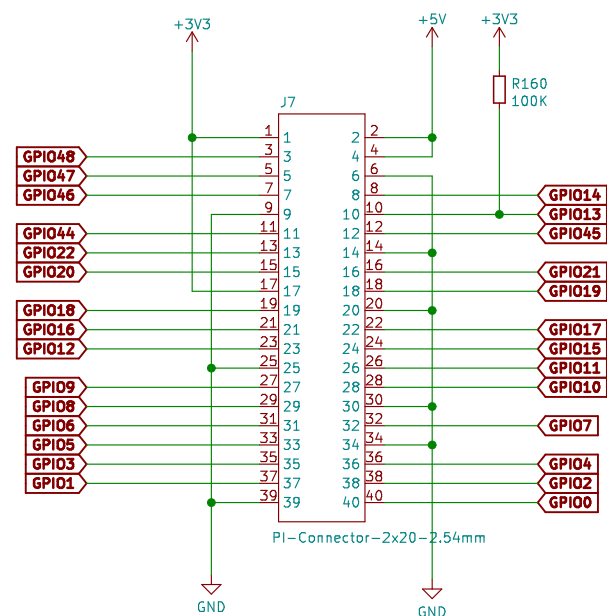
Size: A3	Date: 2021-06-11
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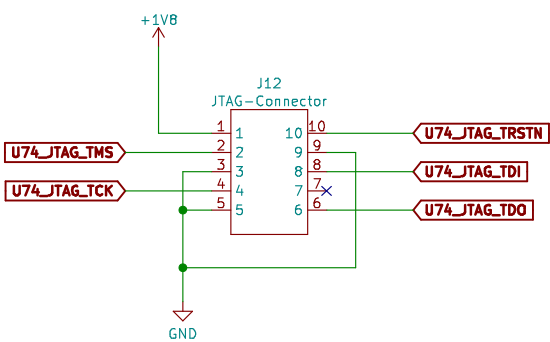
rev: v0.7

18/19

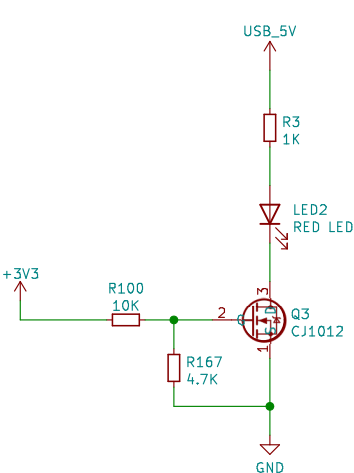
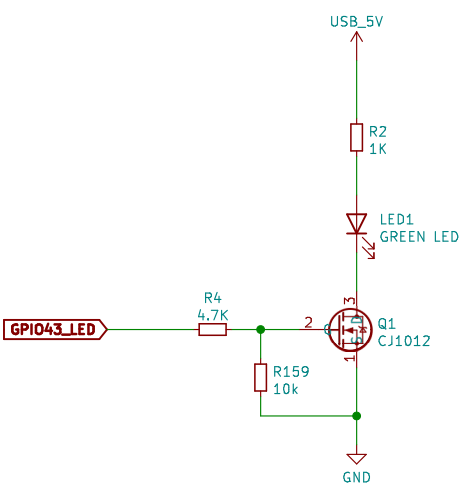
# PI Connector



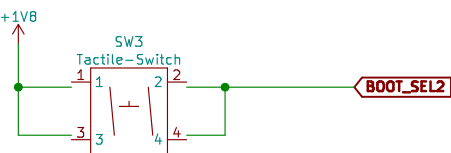
# JTAG 1V8 for Tag-Connect cable



# ACK&Power LEDs



# Boot from UART



Sheet: /BeagleV RPI EXP CONN, LED, BUTTON/  
File: BeagleV\_RPI\_Conn.kicad\_sch

**Title: Beagle V**

Size: A3 Date: 2021-06-11

KiCad E.D.A. kicad 5.99.0-unknown-a6c3d74a73125ubuntu20.04.1

Rev: v0.7

Id: 19/19