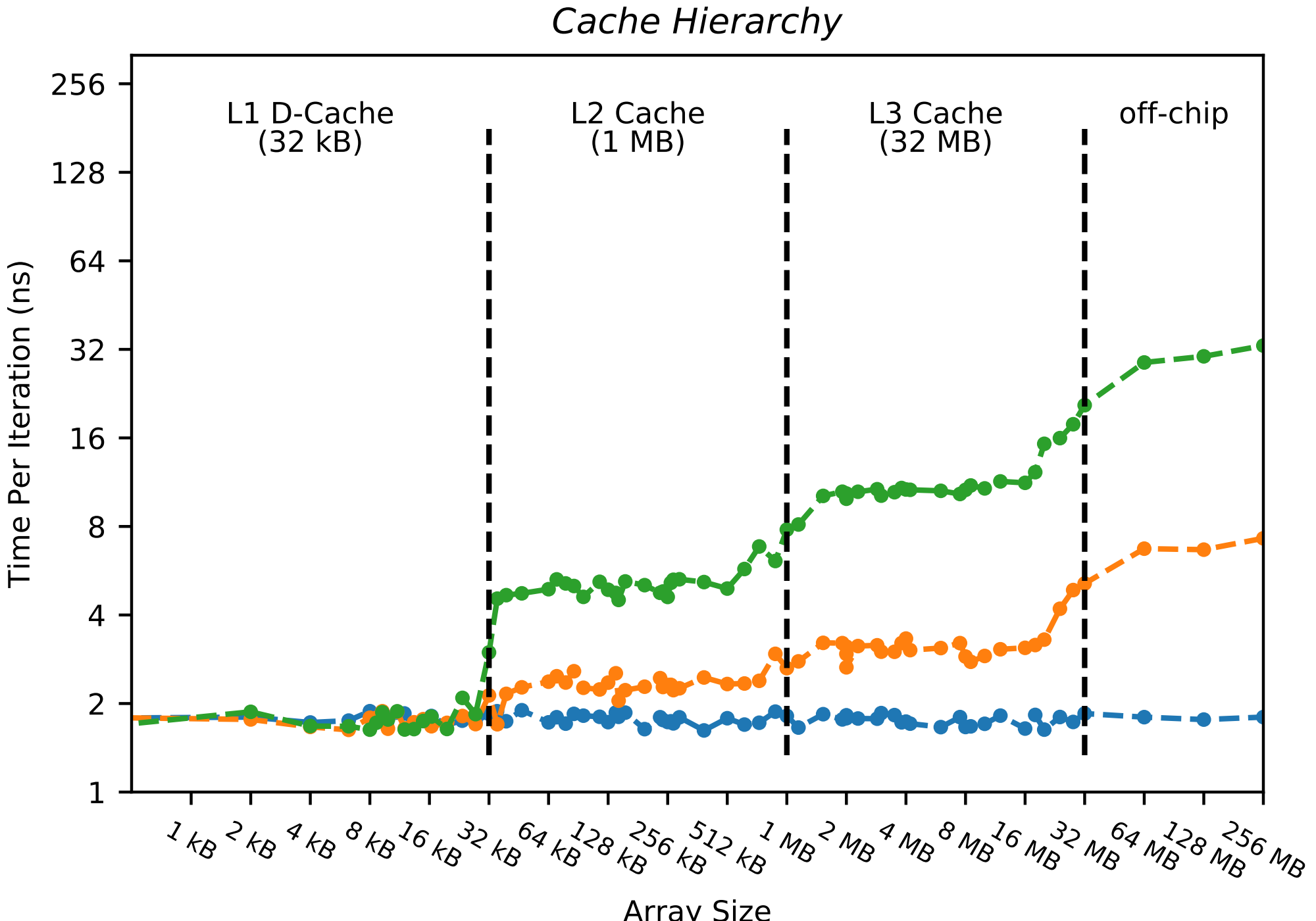
Cache Hierarchy
L1 D-Cache
(32 kB)
L2 Cache
(1 MB)
L3 Cache
(32 MB)
off-chip
Time Per Iteration (ns)
256
128
64
32
16
8
4
2
1
1 kB
2 kB
4 kB
8 kB
16 kB
32 kB
64 kB
128 kB
256 kB
512 kB
1 MB
2 MB
4 MB
8 MB
16 MB
32 MB
64 MB
128 MB
256 MB
Array Size