APPENDIX II: ASSEMBLER DIRECTIVES and SUPPORTED MIPS PSEUDO-INSTRUCTIONS

Directive	Description
`.text`	Start of the assembly instruction section – note you may assume the first instruction of your program will be located in memory address **0x00400000** by convention
`.data`	Start of the static data declaration section that contains data directives – note you may assume the static data area in memory starts at **0x10010000** by convention
`.word`	Data directive for storing a 32-bit integer to the next memory location in static data section

Pseudo-instruction	Assembly syntax	Expansion using MIPS instructions
`la`	`la $t1, label`	`lui $t1, label[31:16]` `ori $t1, $t1, label[15:0]`
`move`	`move $t1, $t2`	`addu $t1, $zero, $t2`
`sgt`	`sgt $t1, $t2, $t3`	`slt $t1, $t3, $t2`